



**SECTION - B (35 Marks)**Answer **ALL** questions**ALL** questions carry **EQUAL** Marks (5 × 7 = 35)

Module No.	Question No.	Question	K Level	CO
1	11.a.	Discuss the Verilog module with an example.	K4	CO1
	(OR)			
	11.b.	Analyze the various keywords used in Verilog.		
2	12.a.	Explain the working of Bidirectional switches.	K4	CO2
	(OR)			
	12.b.	Brief the operation of multiple input & output gates.		
3	13.a.	Develop a Verilog program for full adder.	K5	CO3
	(OR)			
	13.b.	Explain the CASE statement with an example.		
4	14.a.	Elaborate how to read a file in Verilog.	K6	CO4
	(OR)			
	14.b.	Develop a Verilog code with Mixing style.		
5	15.a.	Explain the functions of PAL.	K5	CO5
	(OR)			
	15.b.	Discuss the structure of PLA.		

**SECTION -C (30 Marks)**Answer **ANY THREE** questions**ALL** questions carry **EQUAL** Marks (3 × 10 = 30)

Module No.	Question No.	Question	K Level	CO
1	16	Illustrate the various operators used in Verilog with an example for each.	K5	CO1
2	17	Discuss the functions of Combinational UDP.	K4	CO2
3	18	Design a Multiplexer in Verilog using Data Flow style.	K6	CO3
4	19	Describe the various modeling styles with an example.	K5	CO4
5	20	Analyze the FPGA physical design.	K4	CO5

Z-Z-Z      END