



**SECTION -C (30 Marks)**Answer **ALL** questions**ALL** questions carry **EQUAL** Marks

(5 x 6 = 30)

11. (a). Enumerate the architecture of 8085 micro processor with neat diagram.

(OR)

- (b). Interpret the bus organization of 8085 microprocessor and its components.

12. (a). Formulate the timing diagram of IN 80H instruction with its purpose

The instruction and the corresponding codes and memory locations are given below

Address	Mnemonics	Opcode
800F	IN 80 H	DB
8010		80

(OR)

- (b). Elucidate the memory organization of 8085 micro processor.

13. (a). Differentiate the continuous loops with conditional loops in 8085.

(OR)

- (b). Develop a program to demonstrate the time delay using 8-bit counter.

14. (a). Construct an assembly language program to add two 8-bit numbers and store the result at locations 8050H and 8051H.

(OR)

- (b). Elucidate an assembly language program to find the largest number in array of data.

15. (a). Analyze the pin diagram of 8255-PPI with a proper sketch.

(OR)

- (b). Assess the functioning of water level indicator with the use of 8255 PPI.

Z-Z-Z`

END