

PSG COLLEGE OF ARTS & SCIENCE
(AUTONOMOUS)

MSc(SS) DEGREE EXAMINATION MAY 2024
(Third Semester)

Branch - SOFTWARE SYSTEMS (five years integrated)

COMPUTER ORGANIZATION

Time: Three Hours

Maximum: 50 Marks

SECTION-A (5 Marks)

Answer ALL questions

ALL questions carry EQUAL marks (5 x 1 = 5)

1. CPU has built-in ability to execute a particular set of machine instructions, called as _____.
(i) Instruction Set (ii) Registers
(iii) Sequence Set (iv) User instructions
2. The Stack follows the sequence _____.
(i) first-in-first-out (ii) first-in-last-out
(iii) last-in-first-out (iv) last-in-last-out
3. Any electronic holding place where data can be stored and retrieved later whenever required is _____.
(i) memory (ii) drive
(iii) disk (iv) circuit
4. Which of the following is not a point-and-draw device?
(i) Keypad (ii) Trackball
(iii) Touch screen (iv) Mouse
5. The feature of the multi-microprocessor architecture is
(i) task dependent (ii) single bus provider for many processors
(iii) design is for a specific task (iv) all of the mentioned

SECTION - B (15 Marks)

Answer ALL Questions

ALL Questions Carry EQUAL Marks (5 x 3 = 15)

6. a) Discuss in details about stored program organization.
OR
b) Explain about common bus system.
7. a) Analyze data transfer instruction.
OR
b) Explain in details about data manipulation instruction.
8. a) Illustrate the associative mapping.
OR
b) Explain in details about direct mapping.
9. a) Discuss about ASCII alphanumeric characters.
OR
b) Explain about I/O bus and interface modules.
10. a) Illustrate about multiport memory.
OR
b) Explain in details about crossbar switch.

Cont...

SECTION -C (30 Marks)

Answer ALL questions

ALL questions carry EQUAL Marks

(5 x 6 = 30)

11. a) Elucidate instruction set completeness.
OR
b) Enumerate the types of instructions.
12. a) Explain in details about conditional branching.
OR
b) Evaluate mapping of instruction.
13. a) Determine set associative mapping.
OR
b) Examine cache initialization in cache memory.
14. a) Differentiate I/O versus memory bus.
OR
b) Enumerate examples of I/O interface.
15. a) Discuss about parallel processing.
OR
b) Evaluate multistage switching network.

Z-Z-Z

END