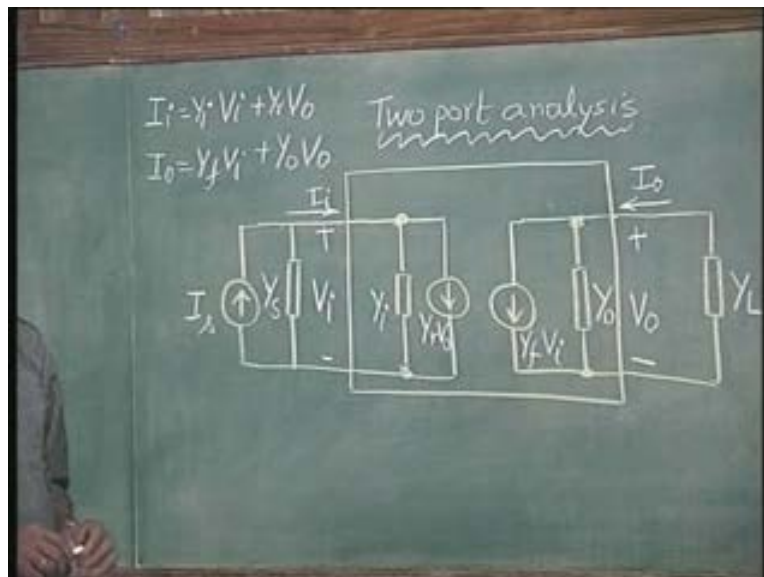


Electronics for Analog Signal Processing - I
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Indian Institute of Technology - Madras

Lecture - 16
Two Port Analysis

In yesterday's class, we made use of the two port network theory to evaluate the performance factors of amplifiers, such as, input impedance, output impedance, voltage gain, current gain and power gain.

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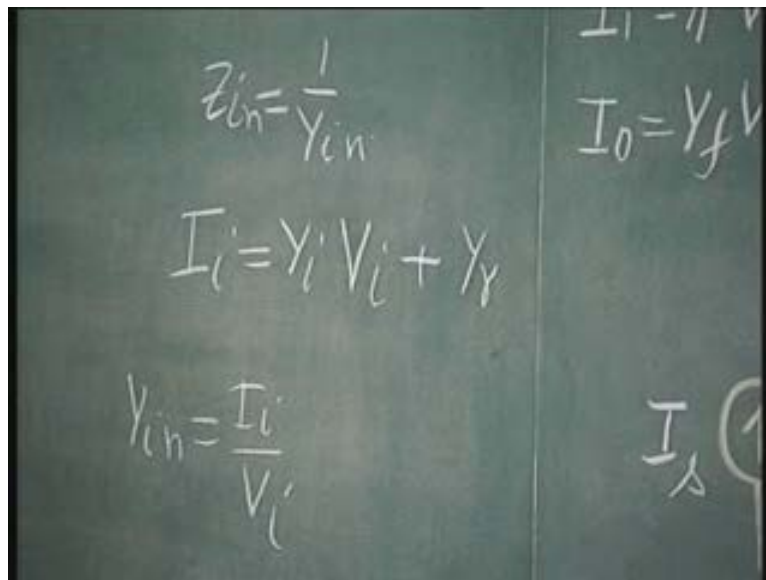


These are the important factors, performance factors, associated with any amplifier. Taking for example, Y parameter, I had shown you how to evaluate the input impedance and asked you to evaluate the output impedance; and I evaluated the voltage gain, asked you to evaluate the current gain and power gain. Today, let us see whether you have really understood this. In case you have any difficulty in evaluating this, we would work out the whole thing for the Y parameter case now.

I would request you to perform the same calculations for all other parameters in almost identical fashion and see what these input impedance, output impedance and power gain, voltage gain, current gain are in the respective parameters. So, I would like you to concentrate on the method that is adopted here. The same method must be adopted for the rest of the three parameters.

So, first, let us see how we can evaluate the input impedance. So, we take this equation I_i ; and Y_i is the self-impedance at the input; this multiplied by V_i and Y_r ; and you have to replace this V_{naught} by something that is dependent upon V_i so that we can find out the input impedance, which is Z_i ; Z_{in} , which is one over Y_{in} ; and Y_{in} is I_i by V_i .

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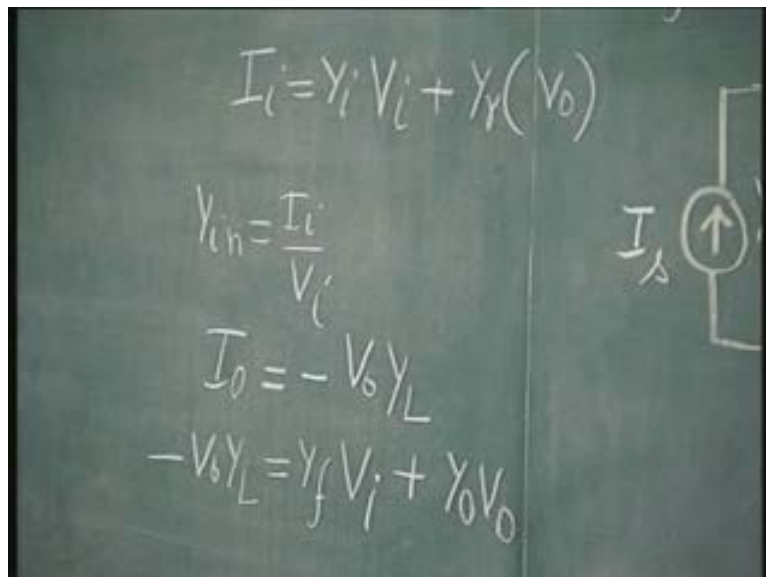


This is the input impedance seen from here. In fact, if Y_r is zero, this is same as Y_i . If Y_r is not zero, this is Y_r into V_{naught} ; and this V_{naught} has to be replaced in terms of V_i . So, it is necessary for us to evaluate and find out what is the relationship with V_{naught} and V_i . That we can get from the second equation.

We know that I_{naught} , I_{naught} is the output current here and I_{naught} is equal to V_{naught} into Y_L . But the current then will flow this way; so minus V_{naught} into Y_L . So, I_{naught} in any of these circuits is always equal to minus V_{naught} into Y_L . This is always the case. I_{naught} is known in terms of V_{naught} and load admittance.

So, because this is assumed as plus, minus, the current would have flown this way and I_{naught} is assumed to be opposite, we put the negative sign. So, substitute this I_{naught} in the second equation. So, we have minus $V_{naught} Y_L$ equals $Y_f V_i$ plus $Y_{naught} V_{naught}$.

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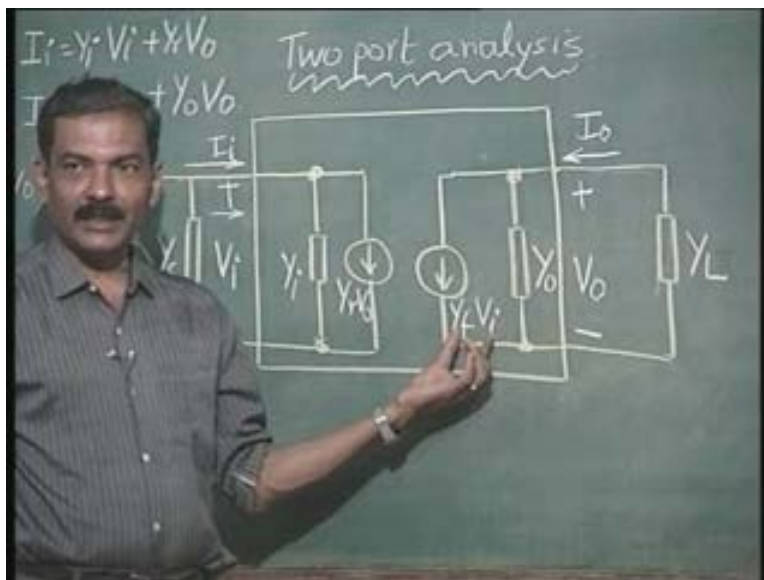
So, this equation is the relationship between V_{naught} and V_i as V_{naught} over V_i is nothing but minus Y_f divided by Y_{naught} plus Y_L .

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$$\frac{V_o}{V_i'} = \frac{-Y_f}{Y_o + Y_L}$$

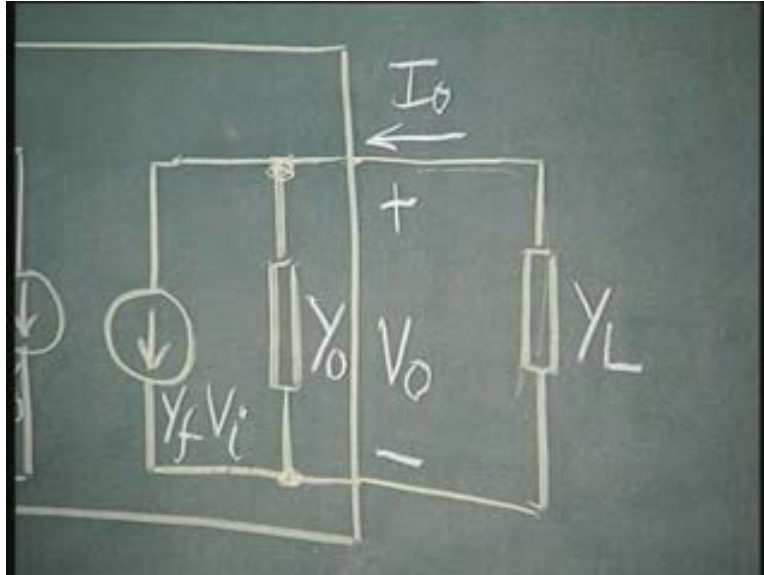
This, in the last class, we had very simply evaluated from this circuit itself. Y_f into V_i is the current in this.

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This current will flow through the total admittance which is Y_f plus Y_L ; the resultant voltage therefore has to be Y_f into V_i into 1 over Y_f plus Y_L with a negative sign.

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That is what we have derived here. So, this is an important equation for voltage gain V_o over V_i .

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$$\frac{V_o}{V_i} = \frac{-Y_f}{Y_o + Y_L}$$

Now, once we know V_{naught} in terms of V_i , we can substitute this in the first equation and get I_i equals $Y_i V_i$ minus, Y_r into Y_f divided by Y_{naught} plus Y_L . This is ... This minus Y_f into Y_{naught} by Y_L , minus Y_f divided by Y_{naught} plus Y_L , is the term that we are replacing it with, into V_i is V_{naught} . So now, we will get I_i by V_i which is nothing but input admittance of the amplifier, is equal to Y_i minus $Y_r Y_f$ by Y_{naught} plus Y_L .

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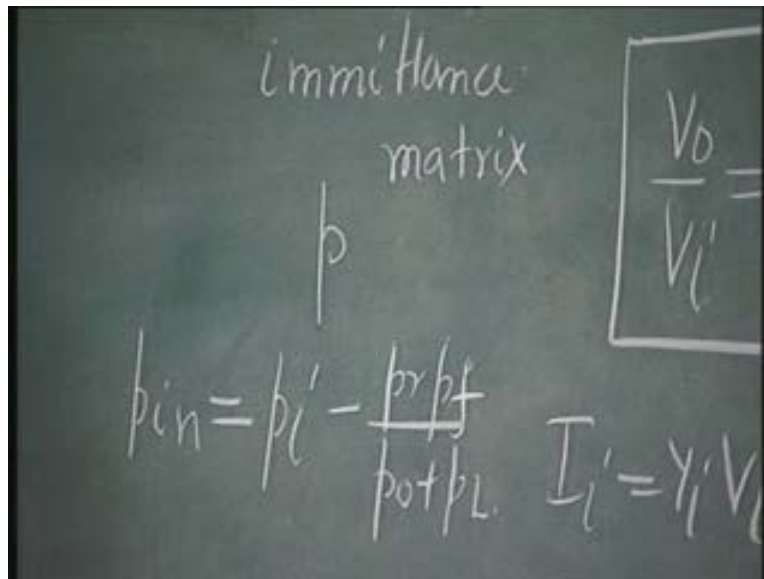
$$I_i = Y_i V_i - \frac{Y_r Y_f}{Y_o + Y_L} V_i$$

$$\frac{I_i}{V_i} = Y_{in} = Y_i - \frac{Y_r Y_f}{Y_o + Y_L} V_o$$

An important relationship, in the sense, that this relationship is valid, whatever be the parameter you select, as long as we replace this parameter with the corresponding parameter. That, what I mean is, if it is Z parameter, then Z_{in} is always equal to Z_i minus $Z_r Z_f$ divided by Z_{naught} plus Z_L .

If it is h parameter, h_{in} is equal to, h_i minus $h_r h_f$ by h_{naught} plus h_L . If it is g parameter, g_{in} is bound to be equal to g_i minus $g_r g_f$ by g_{naught} plus g_L . And therefore, it is immaterial; that is why I introduced the term immittance. Instead of calling this impedance or admittance, it is immaterial what the parameter is. It is called immittance matrix; and a general term that is used is p . So, you can just say that p_{in} is always equal to p_i minus $p_r p_f$ divided by p_{naught} plus p_L .

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So, now that you know the results in all the other parameters, I would request you to adopt the equivalent circuit approach or equations to derive these relationships and convince for yourself that this is **(... Refer Slide Time: 08:50)** and it remains unaltered in its nature. So, this is as much about input admittance and voltage gain. This, we had derived in the last class itself by some other approach.

We will now see the other parameters, other important performance factors associated with the amplifier, which is, I_{nought} over I_i , current gain. Now, this can be again derived from the equations; but I would like to do it slightly differently, just as we did it in the beginning of the class on amplifiers.

Now that we have evaluated V_{nought} over V_i , why not we evaluate I_{nought} over I_i , in terms of V_{nought} over V_i ? That is the best way; because, we have solved already all these equations and therefore we know V_{nought} over V_i . So, V_{nought} is going to be equal to, we know again, in terms of I_{nought} , we have got that, is equal to I_{nought} , minus I_{nought} , by Y_L .

This is from this equation.

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Handwritten equations on a chalkboard:

$$\frac{V_0}{V_i} = \frac{I_0}{I_i}$$

$$I_i = Y_i V_i + Y_r (V_0)$$

$$I_i = Y_i V_i - \frac{Y_r Y_f V_i}{Y_0 + Y_L} \quad Y_{in} = \frac{I_i}{V_i}$$

$$\frac{I_i}{V_i} = Y_{in} = Y_i - \frac{Y_r Y_f}{Y_0 + Y_L} \quad I_0 = -V_0 Y_L$$

$$\frac{V_0}{V_i} = Y_{in} = Y_i - \frac{Y_r Y_f}{Y_0 + Y_L} \quad -V_0 Y_L = Y_f V_i + Y_0 V_0$$

V_0 is already known in terms of I_0 . Another important parameter, we also know V_i in terms of I_i . What is it? V_i equals I_i divided by Y_{in} , from this. You have already evaluated this; V_i is equal to I_i , this goes there, divided by Y_{in} . Subsequently, therefore, we have V_0 over V_i is equal to minus I_0 over I_i into Y_{in} by Y_L .

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Handwritten equations on a chalkboard:

$$V_0 = \frac{-I_0}{Y_L}$$

$$V_i = \frac{I_i}{Y_{in}}$$

$$\frac{V_0}{V_i} = -\frac{I_0}{I_i} \frac{Y_{in}}{Y_L}$$

$$I_i = Y_i V_i - \frac{Y_r Y_f V_i}{Y_0 + Y_L}$$

$$\frac{I_i}{V_i} = Y_{in} = Y_i - \frac{Y_r Y_f}{Y_0 + Y_L}$$

Important relationship, once again. That is, I can either express the voltage gain in terms of current gain into Y_L or, I_o over I_i , therefore, is equal to minus V_o over V_i into Y_L divided by Y_{in} .

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The image shows a chalkboard with several handwritten equations. The most prominent one is:

$$\frac{I_o}{I_i} = -\frac{V_o Y_L}{V_i Y_{in}}$$

To the right of this equation, there is a boxed equation:

$$\frac{V_o}{V_i} =$$

Below the main equation, there are two other equations:

$$V_o = -\frac{I_o}{Y_L}$$

$$I_i = Y_{in} V_i$$

Is that clear? So, this is a simple way of always remembering how to evaluate I_o over I_i , given V_o over V_i . We do not have to start all over again solving the equation. That can be done independently by solving the equation, eliminating the other variables, V_o and V_i from the two equations.

So, this is done knowing the relationship V_o over V_i , how to find out I_o over I_i . If you know this, then, the power gain becomes a very simple factor. Now, one word of caution. Power, what is power input? Power gain here, in general, is going to be output power divided by input power. This is one way of defining power gain. There are several other ways. This is the most common way of defining power gain.

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Power gain = $\frac{\text{Output power}}{\text{input power}}$

$$\frac{I_o}{I_i} = \frac{V_o Y_L}{V_i Y_{in}}$$

$\frac{V_o}{V_i}$

$V_o = -I_o$

Let us now see how this can be evaluated in general. We have done this already for amplifiers. Let us do it for a two port general network. What is output power? We know that output power is going to be expressed as V naught square, that is the output voltage, rms value, V naught square, divided by R_L ; or, into, real part of, what? Y_L ; because, we are talking of admittances now.

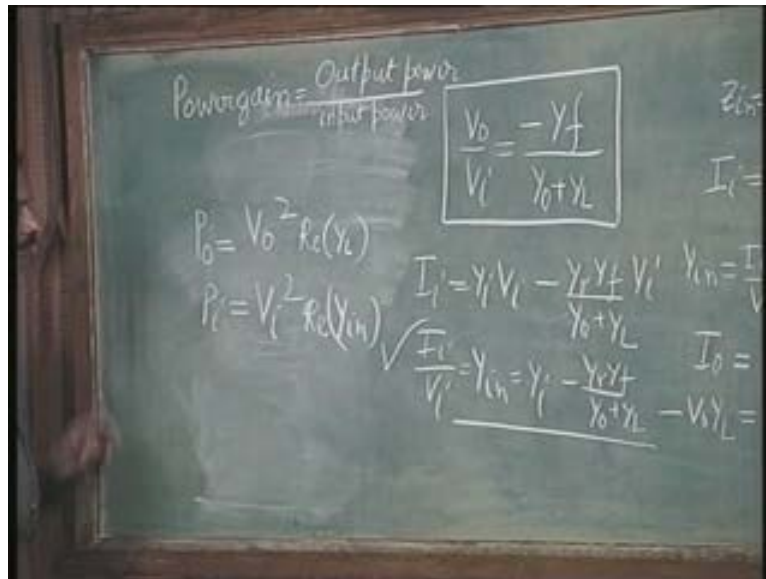
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$$P_o = V_o^2 \text{Re}(Y_L)$$

I_L

Real part of Y_L . This is the input power. This is output power. Input power is, in a similar fashion, what is it? We can now easily see. This is output voltage square into real part of Y_L . This should be, input voltage square, real part of what is it? Y_{in} , which is already evaluated. Y_{in} is already evaluated. It is the real part of this that you have to take in order to find out what the actual power that is inputted.

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So, power gain therefore is the ratio of these two; that is, voltage gain V_o over V_i square, V_o over V_i square, into real part of Y_L by real part of Y_{in} .

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Handwritten equations on a chalkboard:

$$P_o = V_o^2 \operatorname{Re}(Y_L)$$

$$P_i = V_i^2 \operatorname{Re}(Y_{in})$$

$$I_i' = Y_i' V_i'$$

$$\text{Power gain} = \frac{V_o}{V_i} \sqrt{\frac{I_i'}{V_i'}} = \frac{V_o}{V_i} \sqrt{Y_{in} = Y_i'}$$

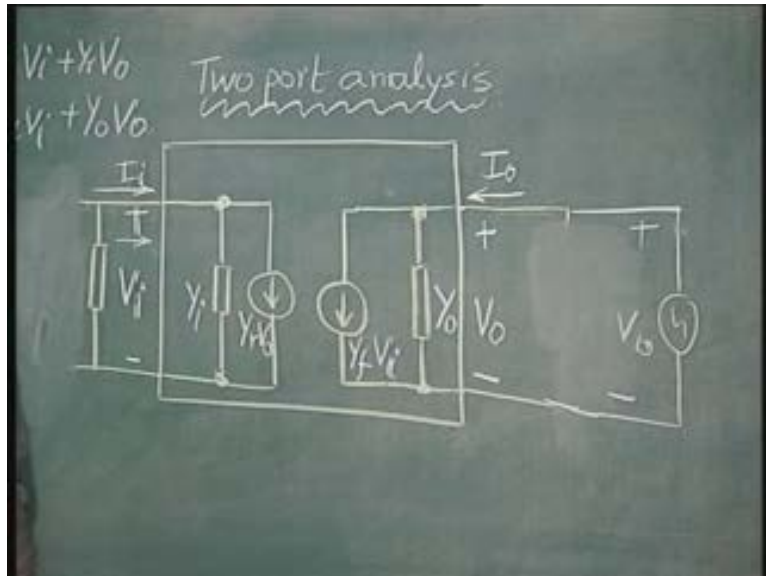
So, the same thing can be derived for any of the parameters, Z , g or h , by adopting the similar procedure and similar definitions. So, in summary, I can say that we have understood how to derive for any two port network. The important performance factors of the amplifier; these are: voltage gain, current gain, power gain and input impedance and output impedance. These are the five important parameters associated with the performance factors.

So, these can be derived in terms of any one of the other three parameters that we have left out; Z , g and h . These are going to be left out as homework problems for you to show that input immittance is going to take the nature exactly similar to this.

Now, output admittance, I have not derived that. We can derive this. Again, I must mention that when I am evaluating output admittance, this is something that has not been understood clearly, I am trying to find out what the output current is. I will remove the load. This is... I will excite it by means of a voltage here, which is V naught. This is not the voltage which has been generated because of input. This is a voltage which I am applying here and finding out what this current is. That is the definition for output impedance.

V_{naught} by I_{naught} , when I am exciting the output. And then, this excitation has to be, this is an independent excitement, this has to be removed. In this case, obviously, if Y_f is not existent, then, output impedance is going to be $1/Y_{naught}$ straightaway. This is called the self-admittance, Y_{naught} .

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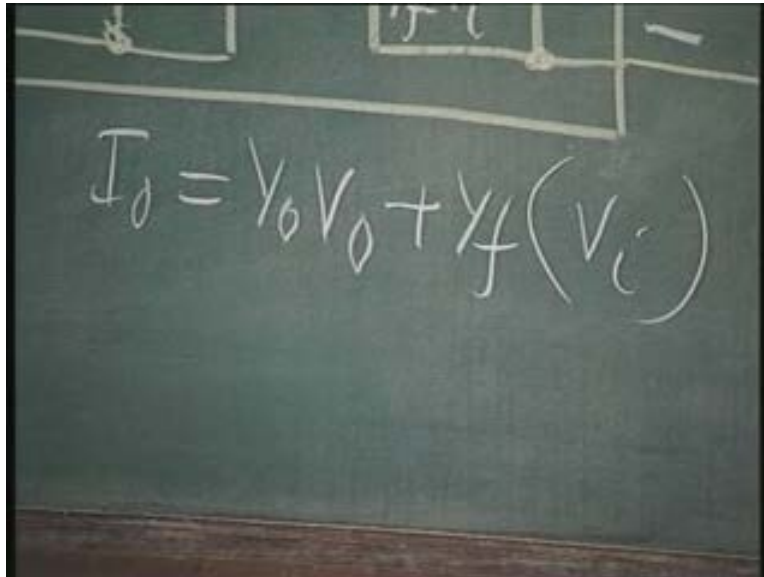
Otherwise, it is going to be determined by this Y_f and Y_r . Y_r is absent; again, the same thing is going to happen. It is not going to be dependent upon what is happening at the... That is, both Y_r and Y_f , if they are absent, these are responsible for forward and reverse transmission.

If something is applied at the output, this Y_r should be present, so that it conveys it to the input and this input is in turn bringing it to the output. So, both Y_r and Y_f should be present in order to fill something at the output, when I am exciting at the output. If Y_r is zero, then nothing happens; nothing happens that is transferred from input to output. So, this impedance is going to remain as the output impedance.

Let us now see what it is. Here, the circuit looks exactly similar to the circuit that is used for evaluating the input admittance. If you notice, V_{naught} is what I am applying here

and I have to evaluate I_{naught} . I_{naught} is given by $Y_{naught} V_{naught}$, this is the second equation, $Y_{naught} V_{naught} + Y_f \text{ into } V_i$. This V_i , I had to replace in terms of what? V_{naught} . Then, I will get the relationship between I_{naught} and V_{naught} .

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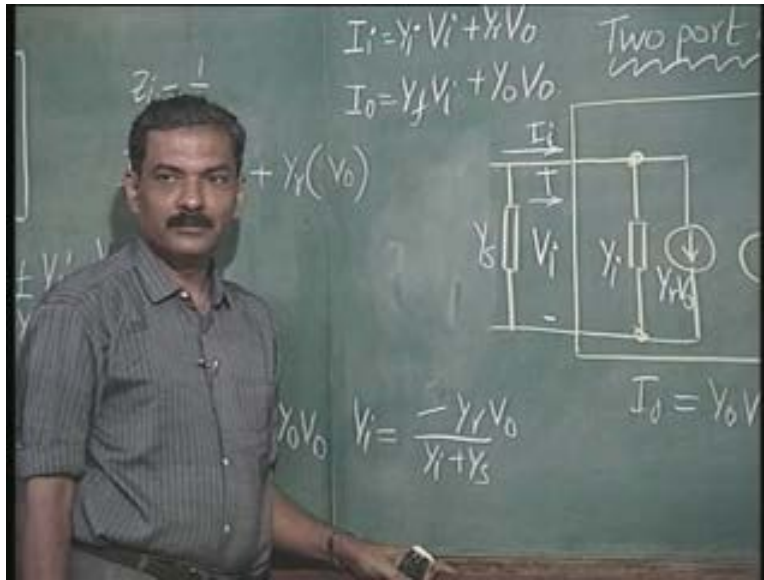


$$I_0 = Y_0 V_0 + Y_f (V_i)$$

So, because of applying V_{naught} , there is something that is happening here. There is a current that is generated here, which is $Y_r \text{ into } V_{naught}$. This is the reverse transmission. So, $Y_r \text{ into } V_{naught}$ generates a current here; and because of this, there is going to be current pumped in this, which will generate a V_i . And what is that V_i is what we have to substitute.

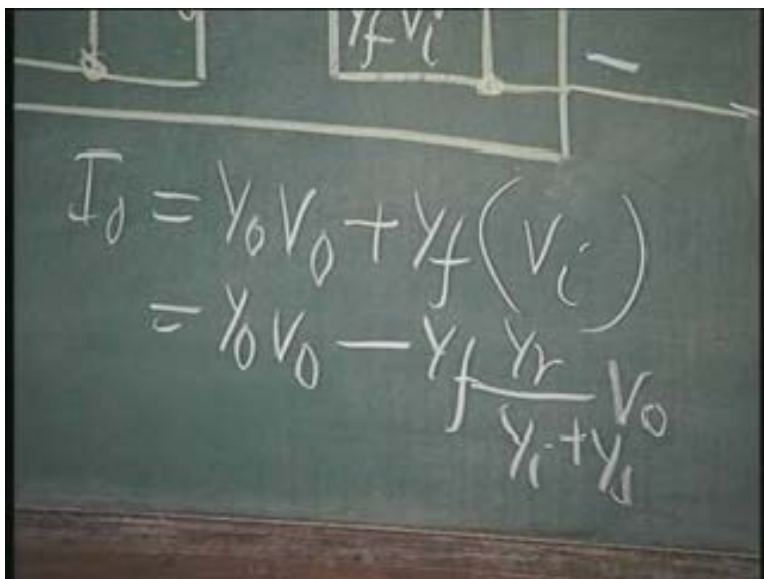
So, V_i is generated here because of V_{naught} . So $Y_r \text{ into } V_{naught}$ is the current. And how much is the current that is flowing in this? This current is going to get divided between this admittance and this admittance, so that, that admittance that is going to take up current from this, this is nothing but Y_s here, the source admittance. So, what is the current that is going there? That is nothing but Y_r divided by Y_i plus Y_s into... with a negative sign. This is going to be the voltage at this point. This is going to be equal to V_i .

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Y_r into V_o is the current through this; this current is going to flow through the entire admittance, which is Y_i plus Y_s ; develop a voltage which is V_i . Since the current direction is this way, the voltage is going to be plus and minus. That means, there is a negative sign that is to be put; and therefore, this is equal to $Y_o V_o$ minus Y_f into Y_r by Y_i .

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Is this clear? So, I_{naught} by V_{naught} is going to be Y_{naught} minus $Y_f Y_r$ divided by Y_i plus Y_s . You can now compare this with what we got for... This is the Y_{out} . This is the Y_{out} .

(Refer Slide Time: 21:36)

$$I_o = Y_o V_o + Y_f (V_i - V_o)$$

$$I_o = Y_o V_o - Y_f V_o$$

$$Y_{out} = \frac{I_o}{V_o} = Y_o - \frac{Y_f Y_r}{Y_i + Y_s} //$$

So, compare this with what we got for input Y_{in} , is equal to Y_i minus $Y_r Y_f$ by Y_{naught} plus Y_L . Wherever i is there, replace it with o , wherever o is there, replace it with i ; wherever L was there, replace it with s ; and Y_r and Y_f also have to be replaced. But, since they come as a product, it is of no consequence; it will always remain as Y_r into Y_f . So, you can see Y_r into Y_f product is going to remain unaltered in this change.

So, Y_i minus Y_r Y_f by Y_n plus Y_L is the input admittance; Y_n minus Y_f by Y_r by Y_i plus Y_s is the output admittance. And in the immittance matrix, p_{out} ... So, this is what we can write. p_{out} is going to be p_n minus $p_r p_f$ divided by p_i plus p_s . This is the general output immittance formula.

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The image shows a chalkboard with the following handwritten equations:

$$p_{out} = p_o - \frac{p_r p_f}{p_i + p_s}$$

$$Z_{in} = \frac{1}{Y_{in}}$$

$$I_i = Y_i V_i + Y_r (V_o)$$

On the right side of the board, there are two equations:

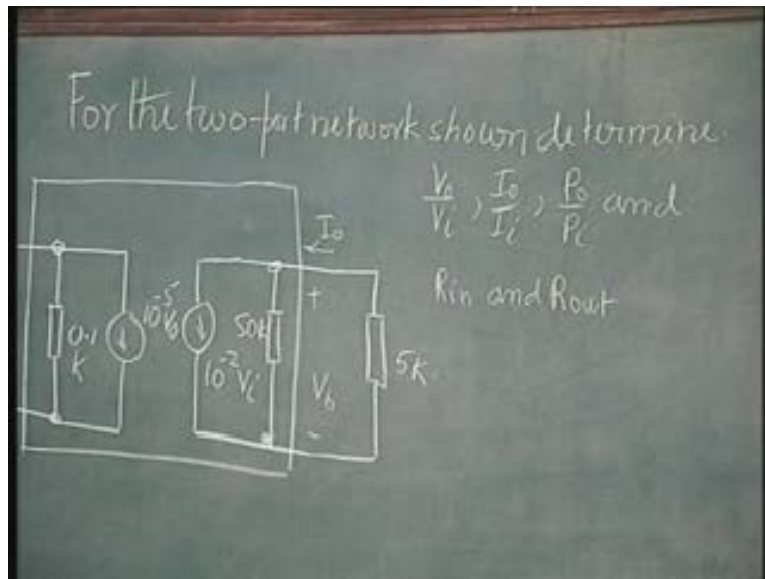
$$I_i =$$

$$I_o =$$

Once again, I request you to show that this is the case in the other three parameters by adopting either equivalent circuit approach or solving the simultaneous equations.

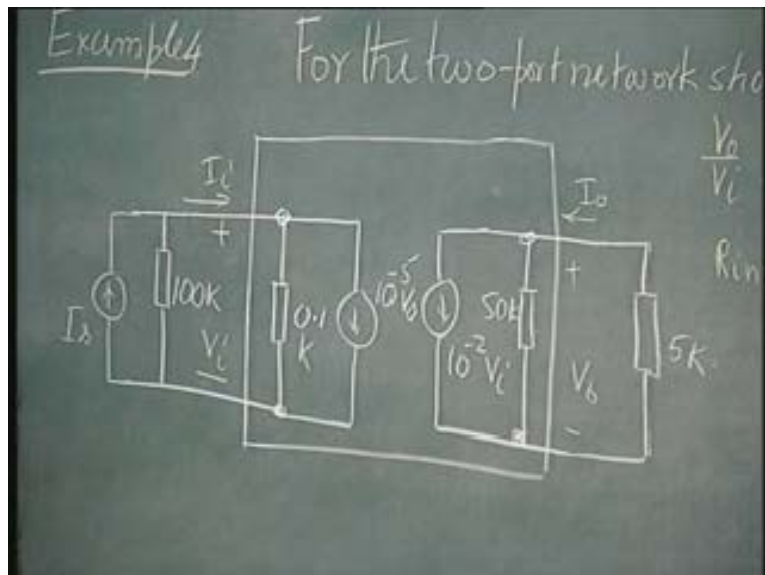
Now, let us take an example here to illustrate what we have learned. For the two port network shown, determine V_n over V_i , I_n over I_i , p_n over p_i , R_{in} ; that is, input resistance and output resistance.

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Now, the only difference between what we had earlier worked out in the amplifier case and this; the earlier amplifiers were all unilateral what it meant was that Y_r or h_r or Z_r or g_r was zero.

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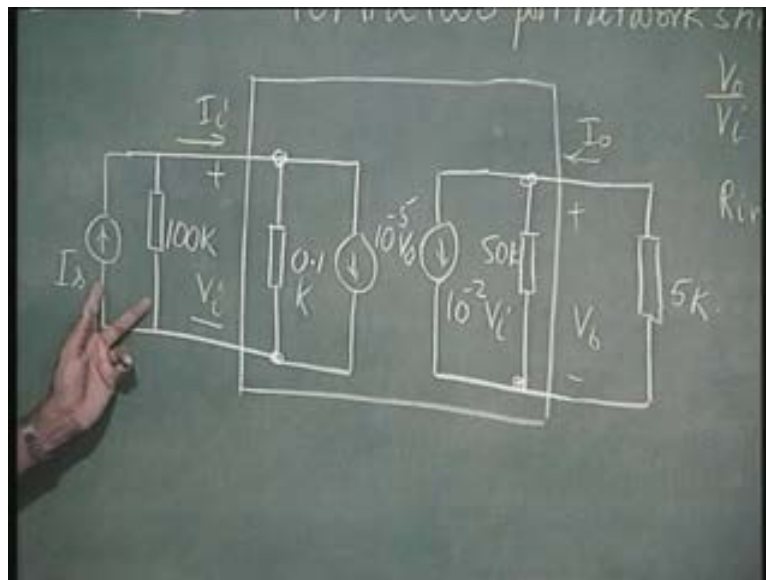


So, transmission was possible only in one direction, forward direction. Whatever was happening here was not getting reflected on to this side. Now, we have put non-idealities

everywhere. So, here is the case where we have some reverse transmission. Whatever is happening here is felt here. So, this... what happens here is felt here; what happens here is anyway going to be felt here; that is what we want in amplifier. What is happening here is to be amplified; but what is happening here, should not be reflected here.

So, we do not want any reverse transmission in a good amplifier. But, this is not a good amplifier; there is some reverse transmission here. So, under this situation what happens? Let us consider this. Here, we have a current source feeding on to the input. Whether it can be represented as a current source or otherwise is justified by comparing the impedances. This is 100 K and this is point 1 K; so this is definitely a current source drive. There is no doubt about it.

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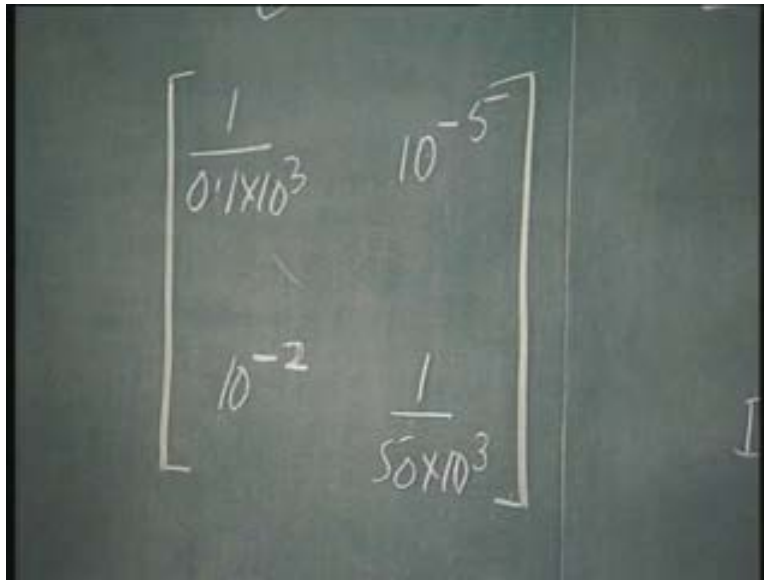
The source impedance is very high compared to what it is here. At this point, the control source, this... whether it is correctly represented as a current source or not can be again justified by comparing it with the load, 5 K. So, 5 K is very small compared to its source impedance of 50 K; so, it is correctly represented as a current source.

Now, if these are the currents due to output and input respectively, then this whole representation can be identified by comparison with what you have learned with Y parameters. So, this is what you should be able to do. That is because, here, I_i is equal to V_i divided by $10^5 K$ plus 10^{-5} times V_o . That is the total **((... Refer Slide Time: 26:00))** that is the equation, one equation. The other equation is, I_o is equal to V_o divided by $50 K$ plus 10^{-2} into V_i . These are the two equations that describe this two port now, which is nothing but Y parameter representation of the equivalent circuit.

So obviously, I have to now put down the Y matrix for this network as... This is the Y matrix. Y_{11} is going to be $1/10^5 K$; Y_{12} , this is Y_{21} ; Y_{12} is 10^{-5} . You can see that this should be a very small factor in terms of siemens, compared to what? Because, this is now having a dimension of siemens. Compared to other parameters, this parameter, these are all Y parameters; so, this should be very small. Then only, I can call it as a very good amplifier. If it is zero, it is the ideal amplifier.

So now, on this side, Y_{22} is going to be $1/50 K$ volts; and Y_{21} is the main transmission, 10^{-2} siemens. So all these are siemens. We can see that this should be really a large factor compared to this, in any case, so that it is more unilateral. That means, transmission from here to here is more than reverse transmission. So, these are the factors of it.

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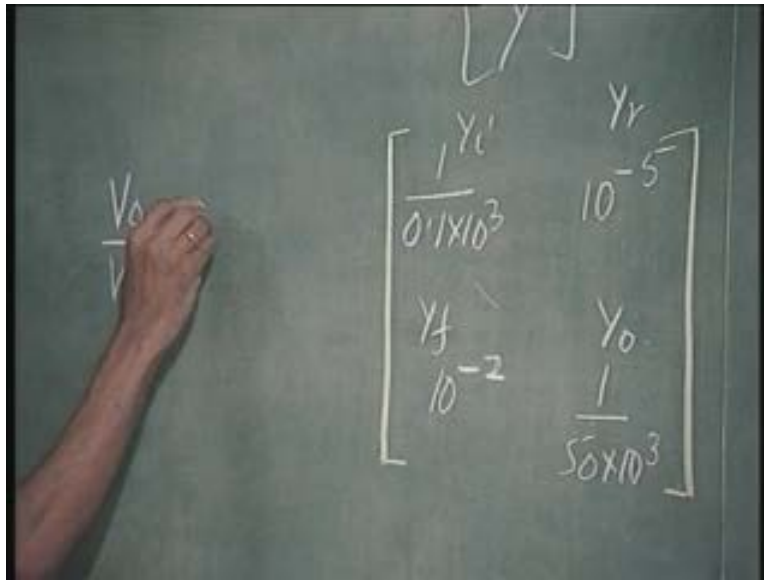


A photograph of a chalkboard showing a handwritten 2x2 matrix. The matrix is enclosed in large square brackets. The elements are: top-left is $\frac{1}{0.1 \times 10^3}$, top-right is 10^{-5} , bottom-left is 10^{-2} , and bottom-right is $\frac{1}{50 \times 10^3}$. To the right of the matrix, there is a vertical line and the letter 'I'.

Now, because of these two factors, there is interaction between input port and output port. What happens at the output port is going to be reflected at the input port. So, the input impedance is going to be different from the original impedance of point one K which is the self-impedance. If this is not there, if this is zero, it would have been just point 1 K. Because of this now, it is going to be different from point 1 K. How different it is going to be is what we are going to now see.

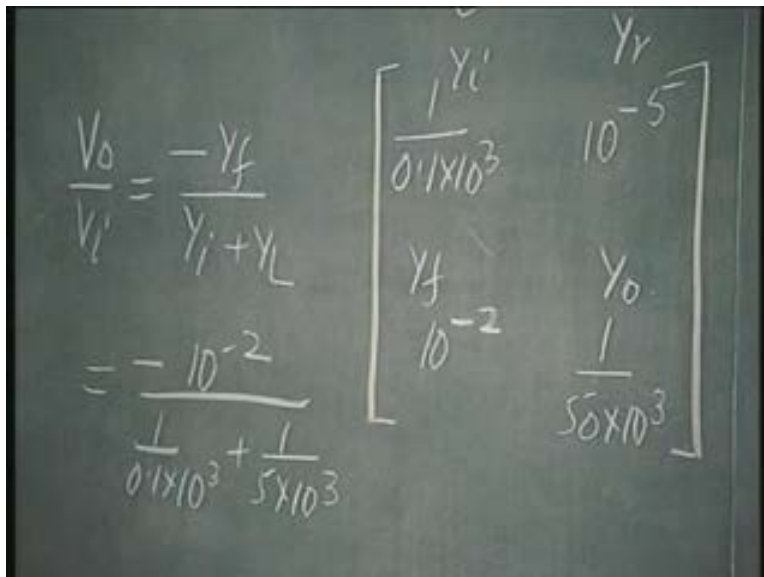
Let us first evaluate the voltage gain for this. V_o over V_i . This is going to be therefore Y_i , this is Y_r , this is Y_f , this is Y_o .

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... is going to be minus Y_f , divided by Y_i plus Y_L . So, minus Y_f by Y_i plus Y_L .

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That is the value, which comes out as minus 10 by 10 point 2, sorry, Y naught, this minus Y f by Y naught plus Y L.

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$$\frac{V_o}{V_i} = \frac{-Y_f}{Y_o + Y_L}$$

$$= \frac{-10^{-2}}{\frac{1}{0.1 \times 10^3} + \frac{1}{50 \times 10^3}}$$

$$= \frac{-10}{10.2}$$

So, which comes out as... This is therefore, 50. So, how much it comes? So, this is 1 by 50 plus 1 by 5; which is, minus 50 by 1 point 1. Minus 45 point 4. So, this is the voltage gain.

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$$= \frac{-10}{\frac{1}{50} + \frac{1}{5}}$$

$$= \frac{-10}{\frac{1}{4.75}}$$

$$= \frac{-50}{1.1} = -45.45$$

Next, Y_{in} is going to be $Y_i - Y_r Y_f$ divided by $Y_0 + Y_L$. This factor, we have already evaluated as minus 45 point 45. So, this is Y_i . Y_i is going to be 1 by point 1 into 10 to power 3, minus $Y_r Y_f$, 45 point 45 into, that is Y_f by $Y_0 + Y_L$; minus of that is, minus 45 point 45, into Y_r , which is 10 to power minus 5.

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The image shows a chalkboard with the following handwritten equations:

$$Y_{in} = Y_i - \frac{Y_r Y_f}{Y_0 + Y_L}$$

$$= \frac{1}{0.1 \times 10^3} - \frac{45 \cdot 45 \times 10^{-5}}{}$$

... which is going to be... this is 10 millisiemens or 10 into 10 to power minus 3, minus, point 4545 into 10 to power minus 3. This is going to be... how much is it?

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Handwritten mathematical derivation on a chalkboard:

$$= \frac{1}{0.1 \times 10^3} - 45.45 \times 10^{-5}$$

$$= 10 \times 10^{-3} - 45.45 \times 10^{-3}$$

$$= \underline{9.54 \times 10^{-3}}$$

9 point ... which is to mean that R in is equal to 1000 divided by 9 point 54 which is 104 point 8 ohm.

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Handwritten mathematical derivation on a chalkboard:

$$R_{in} = \frac{1000}{9.54} = \underline{104.8 \Omega}$$

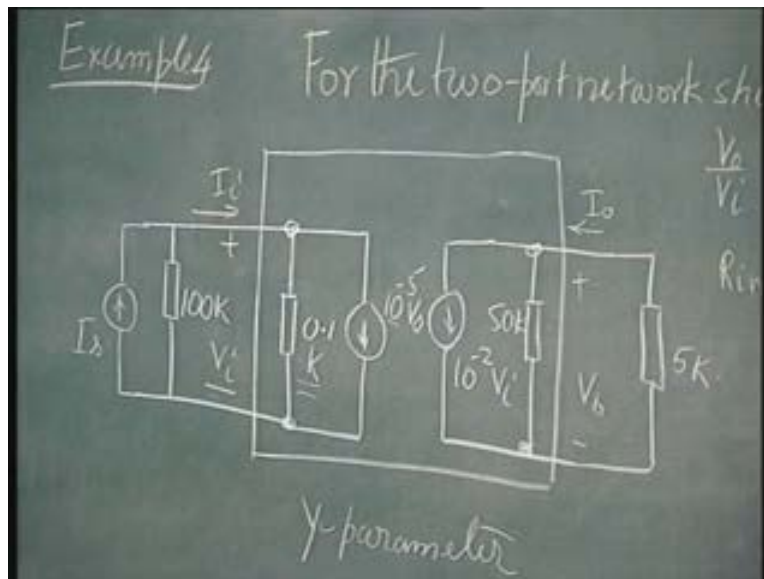
$$Y_{in} = Y_i - \frac{Y_r Y_f}{Y_o + Y_L}$$

$$= \frac{1}{0.1 \times 10^3} - 45.45 \times 10^{-5}$$

Compare this with what we had earlier. It was 100 ohms; now it has become more than 100 ohms. How did it happen? Because, earlier 100 ohms. Why did it become more than 100 ohms? That is, the effect of the feedback is such that it is now simulating a negative resistance here, such that, the combination of this 100 ohms and what is simulated here is more than 100 ohms.

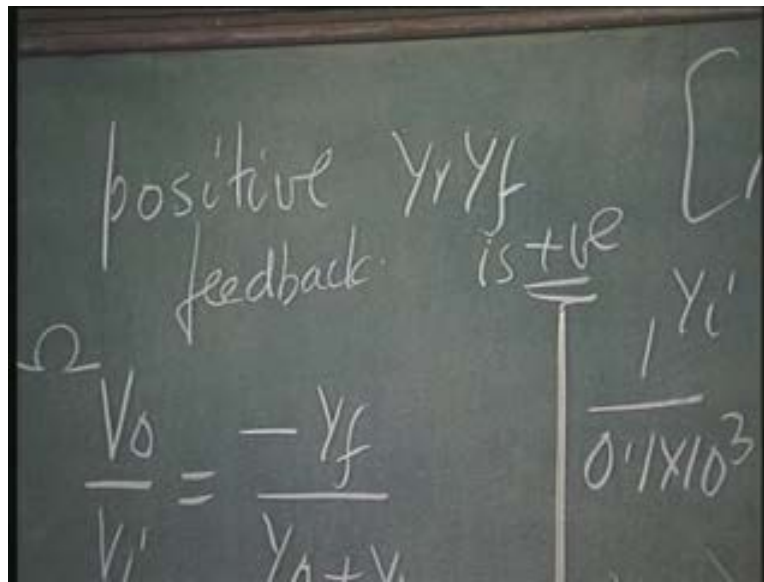
Because, if it is a positive resistance that it assimilated, it would have become less than 100 ohms. So, the effect of the interaction of the output at the input here is to boost up the impedance from the original impedance of 100 ohms. Original impedance without any feedback would have been just 100 ohms. Because of this reverse transmission, it has become more than 100 ohms. Why did it happen? Because, it is now simulating a negative resistance across this positive resistance and boosting up the input impedance.

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This is called positive feedback. This is called positive feedback. So, why is this impedance more than the self-impedance at the input port? That is because, it is simulating a negative resistance and effective resistance is more than the original impedance.

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So, this is called positive feedback. How do you recognize that it is a positive feedback? What is the factor that was responsible for this? We know that it is Y_r into Y_L divided by Y_o plus Y_L , which is coming additionally. If this is, assuming that these are positive, this becomes positive feedback only when Y_r into Y_f is positive. So, we can quickly identify whether it is positive feedback or negative feedback by seeing the product Y_r into Y_f .

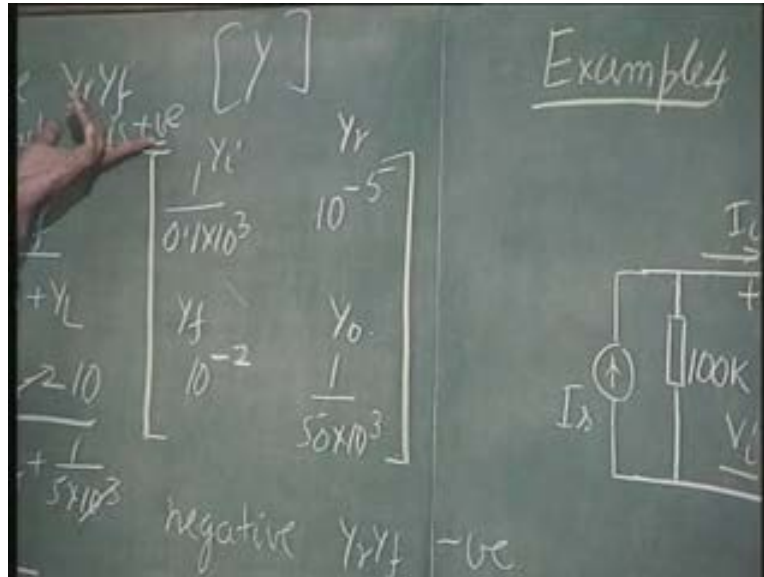
Here, 10 to the power minus 2 into 10 to the power minus 5 ; that is the factor by which the effect of output is reflected at the input. And, it is getting subtracted from the input admittance. That is, effective admittance becomes lesser or impedance becomes higher.

So, this is the effect of positive feedback. We will discuss more about positive feedback much later; but you should now know that in an amplifier, this, an ideal amplifier, this is not at all wanted. If Y_r into Y_f is negative, then, it is called negative feedback. That means, negative, what happens at... then? You can look at it.

If this is negative, in this case it is not negative, let us say this is minus or this is minus; if any one of this is minus, then, it can be negative feedback. Then, what happens? This

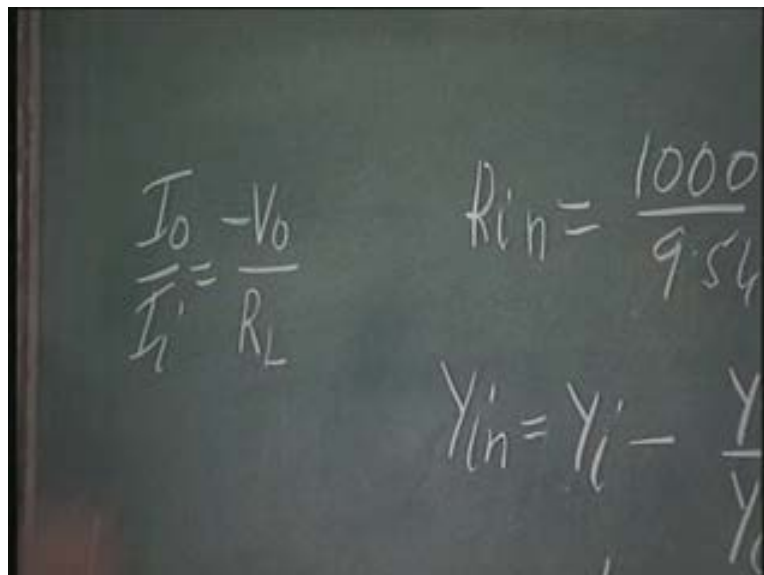
becomes positive; that means, the total admittance increases or impedance decreases. So, this is what happens in negative feedback. In this example, we are giving positive feedback here; and that is why, the impedance is increasing.

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Next, we would like to evaluate I_o over I_i . I_o is nothing but V_o over R_L , minus V_o over R_L .

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And, I_i is nothing but V_i divided by R_{in} . That is right. So, if you know the voltage gain, which is V_o over V_i , which has already been evaluated as minus 45 point 45, minus, minus, becomes plus; this into R_{in} ; R_{in} is also evaluated as 104 point 8; that divided by R_L , which is given as 5 into 10 to power 3. This is the current gain.

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$$I_o = \frac{-V_o}{R_L}$$

$$I_i = \frac{V_i}{R_{in}}$$

$$\frac{I_o}{I_i} = \frac{-V_o}{R_L} \cdot \frac{R_{in}}{V_i}$$

$$= \frac{-45.45 \times 104.8}{5 \times 10^3} = \frac{1}{0.1 \times 10^3}$$

How much is it? Now, this is a good exam... point 9, where the current gain is less than 1; still it is acting as a power amplifier. It is giving you voltage gain and it is having a current gain which is less than 1.

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$$Y_{in} = Y_i - \frac{Y_o}{Y_o}$$
$$= \frac{45.45 \times 10^48}{5 \times 10^3} = \frac{1}{0.1 \times 10^3}$$
$$= \underline{\underline{0.952}} = 10 \times 10^{-3}$$

This is quite possible in practical amplifiers; either it has to give a current gain or voltage gain so as to give ultimately power gain, so that it can retain the name of amplifier. So, what is the power gain? This is current gain into voltage gain, magnitude of that; it is 45 point 45 into point 952; which is 43 point 3.

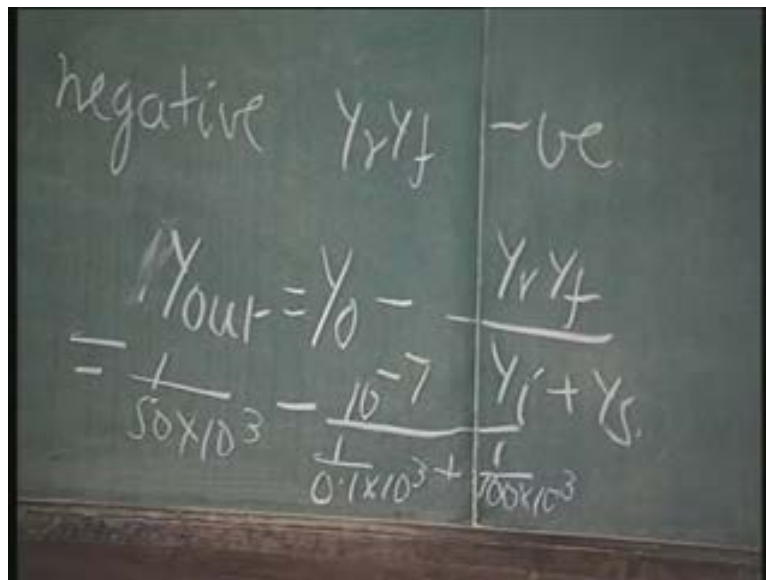
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$$\frac{0.1 \times 10^3}{0.952} = 10 \times 10^{-3} - 45.45 \times 10^{-3}$$
$$= \underline{\underline{9.54 \times 10^{-3}}}$$
$$P_{gain} = 45.45 \times 0.952 = \underline{\underline{43.3}}$$

As long as the power gain is greater than 1, it is called an amplifier. This is an active device; if the power gain is less than 1, the two port network can be termed as a passive device, of no use, for amplification purposes. So, power gain has to be greater than 1 in order to treat it as an amplifier. Even though this is giving you less than 1 as current gain, it is ultimately acting as an amplifier because power gain is greater than 1.

Now, we have to evaluate only the final thing, that is R_{out} . So, Y_{out} is equal to Y_{naught} minus $Y_r Y_f$ divided by Y_i plus Y_s . This is what we have evaluated earlier. So, how much is this? Y_{naught} is going to be 1 over 50 Kilo ohms, minus $Y_r Y_f$ is same as 10 to power minus 7 , divided by Y_i plus Y_s which is 1 by point 1 K plus 1 by 100 K.

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negative $Y_r Y_f$ -ve

$$Y_{out} = Y_0 - \frac{Y_r Y_f}{Y_i + Y_s}$$

$$= \frac{1}{50 \times 10^3} - \frac{10^{-7}}{0.1 \times 10^3 + 100 \times 10^3}$$

We will write it more clearly up somewhere here. So, Y_{out} is going to be equal to 1 over 50 into 10 to power minus 3 minus; now this 10 to power 3 , 10 to power 3 goes, 10 power minus 4 ; so, 10 to power minus 4 divided by 10 point 01 . So, this is going to be 10 to power minus 3 into 1 point 001 . Is it correct? 10 to power minus 4 ; this is 10 to power... just a minute. 10 to power minus 4 divided by 10 point 01 ; this is 10 to power minus 5 into 1 point 001 .

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Handwritten equations on a chalkboard:

$$Y_{out} = \frac{1}{50} \times 10^{-3} - \frac{10^{-5}}{10000} ?$$
$$R_{in} = \frac{1000}{9.54} = 104.8 \Omega$$
$$Y_{in} = Y_i - \frac{Y_r Y_f}{V_i}$$

So, how much is this now? This is going to give you 100 by 50 into 10 to power minus 5. That is, 2 into 10 to power minus 5. This is very nearly 1, into 10 to power minus 5. So, this is equal to 10 to power minus 5 siemens, which is therefore going to give you R out, equal to, 100 K.

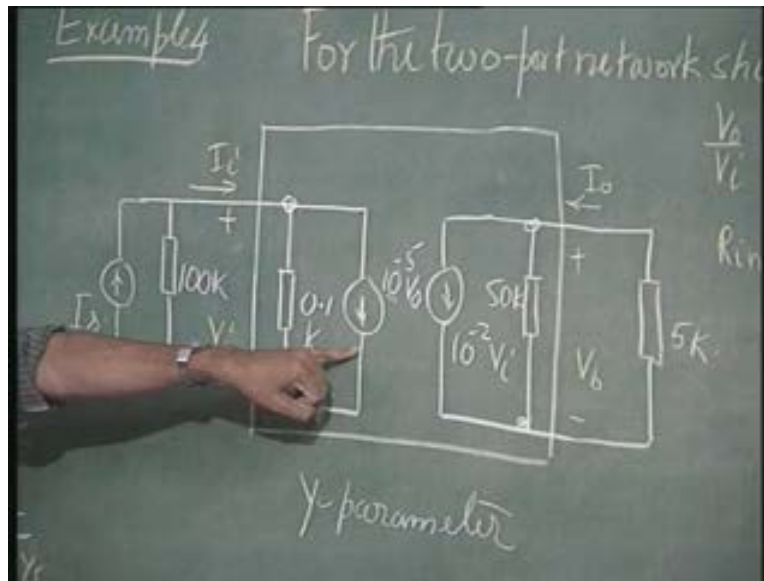
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Handwritten equations on a chalkboard:

$$Y_{out} = \frac{2 \times 10^{-5}}{5} - \frac{10^{-5}}{10000} ?$$
$$R_{in} = \frac{1000}{9.54} = 104.8 \Omega$$
$$Y_{in} = Y_i - \frac{Y_r Y_f}{V_i}$$

Again, you will notice; a very, the same strange thing that has happened. The self-admittance at the output is 1 over 50 K; or, the output impedance, self-impedance is 50 K. But now, because of interaction from the input, it has become 100 K. It has increased. That means, the same effect is there. Because of positive feedback, the impedance level, both at the input and output, have gone up. From 50 K, it has gone up to 100 K. That means, there has been an effect of negative resistance on this side also. So, this clearly illustrates what happens because of this dangerous negative, that is, reverse transmission factor, which is causing positive or negative feedback.

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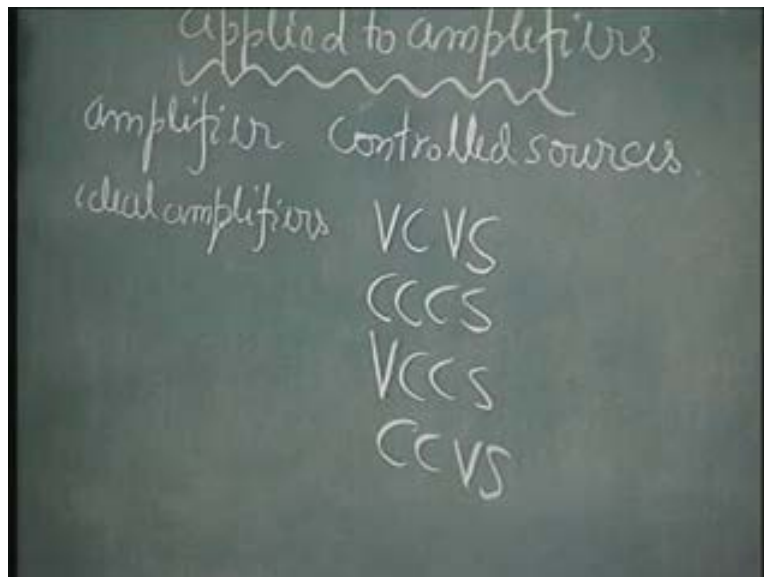


Now, we will, let us see that we can purposely introduce positive or negative feedback, once we understand the amplifier thoroughly. So, before understanding the amplifier thoroughly, we must design amplifiers which have no reverse transmission. This reverse transmission is going to play lot of role in later designs; both in negative feedback amplifiers as well as oscillators. So, we will preserve this knowledge for use at a later date. Henceforth, we will assume that for all our amplifier discussion, until we again raise it, the reverse transmission is zero.

I just wanted to bring this about so as to make it clear to you that this factor is sometimes a troublesome factor in amplifier design. An amplifier has to be unilateral. It has to transmit only from input to output. There should not be any feedback. It is this feedback which causes most of the troubles faced by designers of amplifiers.

So, now that we have discussed two port theory and amplifiers, let us summarize the entire thing. Two port theory as applied to amplifiers, we would like to understand. We said that there are four basic amplifier configurations. They are all called controlled sources.

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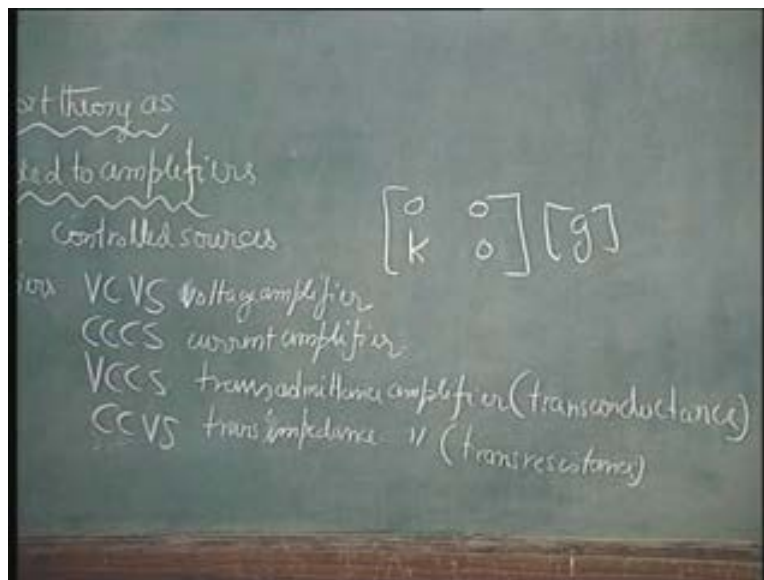
Amplifier, therefore according to network theory people, are nothing but controlled sources; and therefore, ideal amplifiers are the following: voltage controlled voltage source, current controlled current source, voltage controlled current source and current controlled voltage source. These are the four types of amplifiers that can be designed.

Now, we saw that these two are more basic than these two because if you have ideal voltage controlled current source and ideal current controlled voltage source, by cascading this with this, we can get voltage controlled voltage source; by cascading this

with this, we can get current controlled current source. There is no need for therefore synthesizing these two blocks at all. If you have these two, you can obtain ideal amplifiers of all the four categories. So, these two are more basic than these two.

This is called voltage amplifier, traditional voltage amplifier. This is called current amplifier. This, this is a voltage controlled current source; this is called trans..., it is a current source so, admittance amplifier, or more popularly called, transconductance amplifier. This one is called transimpedance amplifier or more popularly called transresistance amplifier. This is what we have discussed. Then we said, an ideal voltage amplifier has a matrix representation, which is, zero, zero, zero, whatever it is... and has a voltage ratio which is K, something like that. So, obviously, this voltage ratio comes only as basic definition in g matrix. So, g parameter is the only parameter which can represent a voltage amplifier.

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What does it mean? What it simply means is, if I try to represent a current amplifier or a transadmittance amplifier or transimpedance amplifier in terms of g parameters, these will go towards infinity; any one of these three. So, ideal amplifier, these things will

become meaningless. Only in g matrix, this can be meaningfully represented. And, these parameters should go to zero; and this is the only ((trans...Refer Slide Time: 48:05)).

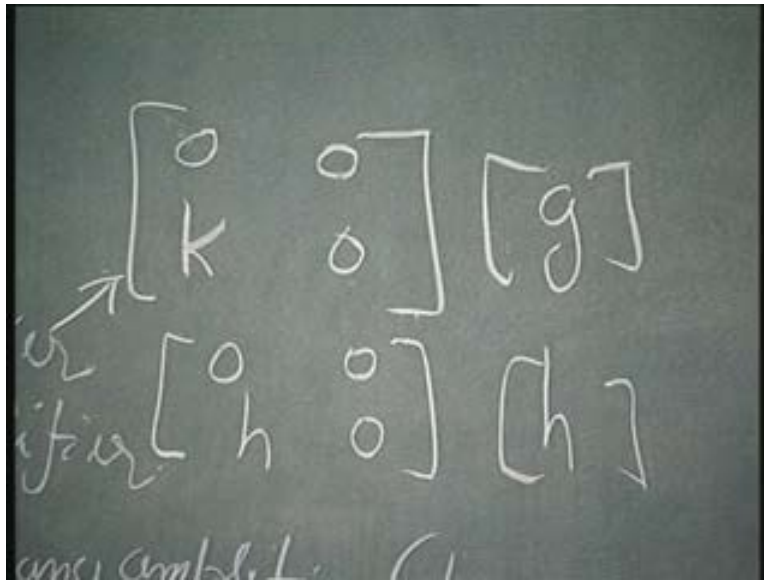
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So, that defines a voltage controlled voltage source. If you see this here, this will define g i which is going to zero; that means, the impedance is infinity. This g is going towards zero; this output conductance. So, it is a voltage source. Now, it is unilateral; so, no reverse transmission. So, these things are going towards zero.

Again, this is the representation. Therefore, for current amplifiers, the dual of this; that is, only h parameter, it is the only parameter which is suitable for representation; no other parameter is suitable for...

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I mean you can represent a non-ideal amplifier by any one of the four; but once you recognize the source impedance and load impedance, you will recognize that particular thing as a particular amplifier. And therefore, you must adopt the proper parameter for analysis; then you would know an idea about the magnitude of these. These will go towards zero and this is the only parameter which is going to be realistically large.

Then, transadmittance amplifier. So, you can see this - voltage controlled current source. Again, this will be zero. The current source magnitude I , is going to be represented as V into g . So, this is going to be conductance or admittance. So, this is going to be represented by Y .

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Handwritten on a chalkboard, the text "ideal amplifier" is written at the top right. To the left, the vector $[Y]$ is written. To the right, a transfer function matrix is shown as $\begin{bmatrix} 0 & 0 \\ G & 0 \end{bmatrix}$. An arrow points from the right side of the matrix towards the right edge of the board.

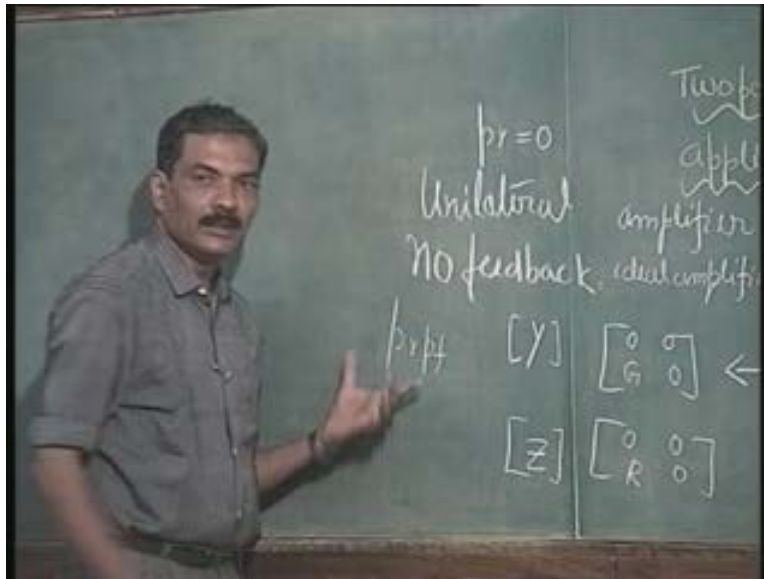
The other one is going to be zero, zero, zero, R; or Z. So this is going to be appropriately represented by Z.

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Handwritten on a chalkboard, the text "ideal amplifier" is partially visible at the top right. The vector $[Y]$ is written on the left. To its right is the transfer function matrix $\begin{bmatrix} 0 & 0 \\ G & 0 \end{bmatrix}$. Below this, the vector $[Z]$ is written, followed by the transfer function matrix $\begin{bmatrix} 0 & 0 \\ R & 0 \end{bmatrix}$. Arrows point from the right side of both matrices towards the right edge of the board.

So, this is the summary of what I was trying to impress upon you by trying to correlate the two port theory with amplifiers. This, you must remember as the basic theory necessary for you to understand the concept of amplification itself; and these factors being zero, indicate the amplifier to be unilateral.

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That means no what? feedback. So, this, let us say, $p r$ being zero in any one of the four indicate the unilateral nature of the amplifier where no feedback is there. And $p r$ into $p f$, the sign of this, indicates whether it is positive feedback if it is positive; negative will indicate that it is negative feedback. More about this feedback, we will learn later.