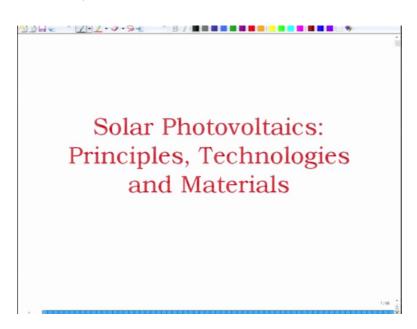
Solar Photovoltaics: Principles, Technologies and Materials Prof. Ashish Garg Department of Material Science & Engineering Indian Institute of Technology, Kanpur

> Lecture – 31 Manufacturing of Si

(Refer Slide Time: 00:18)



So, welcome again to a new lecture on Solar Photovoltaics: Principles, Technologies and Materials. So what we will do is that we will just have a quick recap of last lecture.

(Refer Slide Time: 00:27)

ŮĎH© ″<mark>ℤ⊓ℤ</mark>・┩・℈ⅎ ″В/**ДВВВВВВВ**В**В**ВВ Recap - Mono Si 2 Poly Si Solar Cells Single crystal polycrystalline 25-267. 22-237. - Strategies to impore performance - optical light complig - Electude design / buried contacts - doging - Suface recombination reduction - ARC

So, in the last few lectures we have talked about mono silicon and poly silicon solar cells. Mono silicon are basically single crystal silicon solar cells and these are poly crystalline. So, because of presence of defects the poly crystalline efficiencies are bit lower so, in this case we have efficiency above 25-26 percent efficiencies in this case we have efficiency 23 percent best efficiencies obtained.

However, they are not too bad given that there is a significant cost differential between the two. So, poly crystalline ones are cheaper than single crystals. And then we looked at the strategies to improve performance which are basically also design criterias. So, first was to improve the like optical light coupling, then we need to control the doping, electrode design, surface recombination, reduction, use of anti reflection coatings and so on and so forth and you can say here buried contacts.

So, these are certain strategies which are used to improve the quality of both poly crystalline and mono crystalline silicon solar cells. The way it is done in two solar cells may be slightly different, but they are by large similar.

(Refer Slide Time: 02:19)

Manufacturing of Si Metallurgical Grade Si X Deposition & purification \* Semiconductor Grade Si (poly-Si) I metting 2 resolidification Single Crystal Si

Now we will look at a bit of about bit about the silicon manufacturing. How do you do silicon and silicon device manufacturing?

So, to begin with we talk about silicon manufacturing. Silicon is generally manufactured in three steps. So, the first step is we have metallurgical grades silicon which is very impure. This metallurgical grade silicon is then converted into semiconductor grade silicon which is essentially you can say poly silicon and then we have third one called as single crystal silicon. So, we; obviously, cannot use this for semiconductors, but we can use these two for semiconductor processing.

Semiconductor grade silicon that is poly crystalline silicon is used for poly crystalline silicon solar cells and the silicon single crystal silicon is used for mono crystalline silicon solar cell. So, there is a additional step that is used to convert the poly crystalline into single silicon solar cell which basically involves melting followed by re solidification to make single crystals.

So, essentially it is about melting and re solidification and this is about purification and deposition; deposition and purification. So, you have two steps deposition purification. So, metallurgical grade is complete no no, but semiconductor grade and single crystal silicon both are used in practice. So, metallurgical grade silicon; how do you produce metallurgical grade silicon first of all.

(Refer Slide Time: 04:17)

Production of Metallurgical Grade S:  
- lumpy quatz (SiOz) + Carlom (Cal, ale ...)  
SiO<sub>2</sub> (S) + 2C (S) = Si(1) + 2CO (G)  
between 
$$\frac{1900-2000^{\circ}C}{SiO_2}$$
  
SiO (S) + SiC (S) = 2SiO (S) + CO (G)  
SiO (S) + SiC (S) = 2Si(1) + CO (G)  
Below 190°C  
SiO (G) + 2C(S) = SiC (S) + CO (G)  
2SiO = SiC (S) + SiO (C)  
Liqid Si - 97-97X, pure  
Impurition = Fe - 0.2 - (Y, A1-0.4 - 0.7X, C- 0.2-0.6X, ...)  
Ti - 0.1-0.2X, C- 0.1-0.15X.

So, metallurgical grade silicon is basically made from to begin with you start with lumpy quartz. Quartz we know is nothing, but SiO 2 and essentially you made it react with carbon. Carbon is in the form of coal or you can say coke etcetera various forms of carbon is present essentially you make SiO 2 react with in the solid form react with

carbon in the solid form and this gives rise to silicon that is liquid and which is in the gas form.

This is a very energy intensive process because free energy formation of silicon oxide is extremely negative. So, thermodynamically this reaction is very energy intensive; so it goes at very high temperatures. So, first you have to go to between about 1900 to 2000 degree centigrade; your silicon oxide in the solid form reacts with silicon carbide in solid form to give you 3 SiO in the gas form plus carbon monoxide in gas form.

And then this SiO 2 in gas form reacts with again silicon carbide in solid form gives you 2 Si liquid plus CO g this is a multi step process. And below 1900 degree centigrade the reactions that occur are this Si O g plus 2 C in the solid form this makes Si C solid plus CO g and then 2 Si O gives rise to Si liquid plus SiO 2 liquid. So, the liquid that you get after this processing is first you go to 1900 2000 then below 1900 gradually and then you obtain from this liquid silicon which is 99, 97 to 99 percent pure with impurities like iron is about you know 0.2 to 1 percent.

Aluminium is about 0.4 to 0.7 percent then we have calcium which is 0.2 to 0.6 percent and so on and so forth. But if there is a bit of titanium 0.1 to 0.2 percent and then we have carbon 0.1 to 0.15 percent. There may be some other impurities also this is what is the composition. This is complete nono for semiconductor processing; this is very impure

(Refer Slide Time: 07:41)

Refining 
$$\rightarrow$$
 Oxidation of impusibles  
Refining  $\rightarrow$  Oxidation of impusibles  
Padical oxidation of 5:  
 $Slag$   
Padical oxidation of 5:  
 $A1/Ca/Mg + Sio_2 \rightarrow A1_2O_5/CaO/MgO - Slag$   
 $-Casting 2 refining$   
Metallurgical Grade S:  
 $A1 - 1200 - 4000$  PPm  
 $B - 37 - 45$   
 $P - 27 - 30$   
 $Ma, Mo, Ni, Ti, V, Br (1 < 100pm)$ 

So, what is the next is next you do refining. Refining is always the energy intensive process and this is used refining is done by agents which react with the impurities. So, basically its oxidative process essentially it is oxidation of impurities. So, this is a very tricky process because you have to choose conditions which do not lead to oxidation of silicon, but the impurities and remember silicon is very silicon has high affinity to react with oxygen.

So, as a result it is not very easy. So, oxidation of impurities to form slag; slag is nothing, but the impurities molten pool of a impurities. So, essentially and but you cannot completely prevent the oxidation of silicon. There is always some oxidation of silicon that happens. So, as a result what happens is that you have you a you also add some SiO 2 in this process; so, aluminium, calcium, magnesium etcetera. They react with SiO 2 to form Al 2 O 3, CaO and MgO and these make what we call as slag.

And then of course, you do so, essentially you are saying you have partial oxidation of silicon here. And then what happens is that you do casting in further refining all right. There is a very tedious energy intensive process you have to go multiple stages of refining and that is why silicon sand is cheap, but silicon is not.

So, a typical metallurgical grade silicon wind consists of your aluminium; so metallurgical grade silicon. So, this is where we have this rather purified metallurgical grade silicon. We have aluminium of the order of 1200 to 4000 ppm, boron of the order of 37 to 45. So, all in ppm phosphorus about 27 to 30 and then we have calcium, we have chromium, we have copper iron. Iron is relatively large 600 to 3000 ppm and then we have manganese, we have moly, we have nickel, we have titanium, we have vanadium, we have zirconium and all these are less than about 100 ppm ok; all of them except iron.

So, these are less than 100 ppm typically some of these may be 200 ppm so, so, but generally about in less than 100 ppm. So, this is the metallurgical grade silicon. This is very very impure not fit for suited semiconductor processing. So, what you do is that then you process this metallurgical grade silicon to purify this using other processes.

## (Refer Slide Time: 10:53)

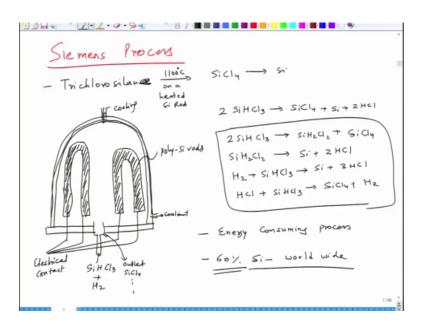
(1) Preparation of voltile Sitty (11) Purification (11) Decompose to clemental S By products - recycling Si emergy frocers - (1) -> 60% Union Castide Process -> 157. Ethyl Corporation process->

So, the semiconductor grade silicon is made by; so from this silicon that we obtain we make first volatile silicon hydride SiH 4, then we purify it, then we decompose this to elemental silicon and then we go to byproducts. In the process we have well it is not a part of the process at such, but this is what happens you have byproducts which are recycled into these stages.

So, so essentially you first fit material grade silicon and convert the metallurgical grade silicon into these silicon compounds like silicon hydride because remember no matter how much liquid processing you do, you cannot make very pure silicon. So, you have to take it to vapour stage where only silicon is able to come out rest all does not come out. So, after you have done primary refining using the metallurgy processes, you have to go to the processes which are more of deposition processes than liquid based processes. So, that in this case there are three processes that we generally use, but generally and the Siemens process is the one.

So, Siemens process is the first process that is used to make silicon and then we have union carbide process. Siemens process accounts for about 60 percent silicon in the world and union carbide process accounts for about 15 percent silicon production. And then third is what we call as ethyl corporation process which is about again 15 percent or so. So, let us see will not go into all of these, we will just look at the Siemens process ok.

## (Refer Slide Time: 13:38)



So, Siemens process; so what happens in Siemens processes, you have basically we are doing we first make trichloro silane ok; Trichlorosilane. This is thermally decomposed at 1100 degree centigrade on a heated silicon rod inside a decomposition chamber and then it gives rise to what we call as silicon SiCl 4 and this SiCl 4 is then converted into silicon. So, the way we go about it is we have this chamber which is a dome shaped chamber alright within these we have.

So, we have these silicon rods like these are silicon rods prefabricated silicon rods. So, these are you can say poly silicon rods this. And then of course, you have some cooling medium here vent sorry cooling and then what we have here is we have this; these are electrical contacts electrical contacts to heat these rods to high temperatures.

So, temperatures are about 1100 degree centigrade and then through this thing we pour in what we call as we put in Si H Cl 3 which is the silicon trichlorosilane and hydrogen and then somewhere here we also have a outlet for other gases like SiCl 4 etcetera; they come out.

So, and then that the coolant goes out from this side because we also have to keep the chamber clean cool otherwise so, it said this is a double body chamber like this. So, this is a double walled chamber. So, keep the coolant; so this has coolant flowing. So, coolant flows and keeps this cooled ok; so this is coolant. Now in this the reactions that happen are two of SiHCl 3 makes SiCl 4 plus silicon plus 2 HCl. So, it is not a very friendly

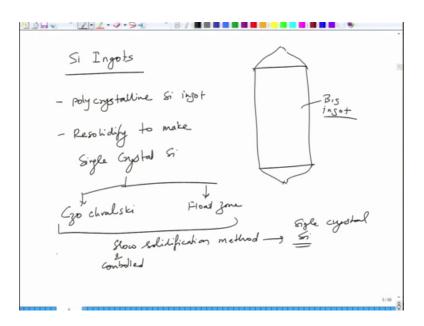
reaction you can see that the things that are forming is not very benign and this happens in multiple stages.

So, you have 2 SiHCl 3 goes to SiH 2 Cl 2 plus SiCl 4 then SiH 2 Cl 2 forms Si plus 2 HCl and then this hydrogen also reacts with HSi Cl 3. You can see, I have replaced it here. So, this becomes Si plus 3 HCl and HCl plus Si H Cl 2 they make SiCl 4 plus H 2; so, you can say this is Si HCl 3 sorry.

So, these are multiple reactions that occur in this process to give you eventually the silicon. This is a very energy consuming process. It is a slow process it is a energy consuming process and this is what is used for making silicon in the industry. Over 60 percent silicon is made worldwide using this. So, this results in the formation of silicon which is quite pure semiconductor grade silicon and basically the silicon which comes here it gets deposited on these rods.

And these rods become thicker and thicker and thicker and thicker and then you of course, take them out, cut them, make wafers that is what you do ok. And so, there is a whole scheme of this process, I will not go into details of that process. So, this is what is the basics of making silicon ok.

(Refer Slide Time: 19:28)



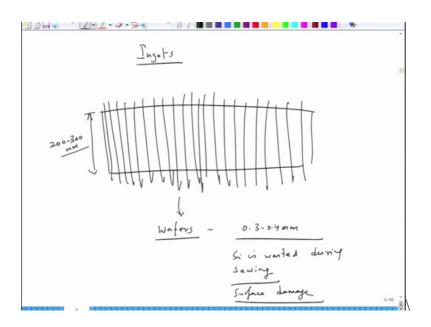
And then we make silicon in got from so. Now, if you want to make silicon single crystal, you make silicon ingots. So, silicon ingots are made by melting silicon ok. So,

whatever poly silicon you make that is pure, then you melt it. After melting you make ingot it is casted and once it is casted, it is a begin cut off. So, after casting it becomes like a big ingot and generally it is like it has a little nozzle shaped nose on the two sides. So, this is a big ingot and these ingots are then if you want to make single crystal silicon, they re casted and re solidified.

So, you can make a poly crystalline ingot or you can you need to re solidify it to make single crystal silicon; first process is called as Czo chralski Czo chr alski and then second method is called as float zone. So, principles of solidification and impurity segregation are used to make high highly pure and highly single crystalline highly defect free silicon.

So, essentially what happens here is in these two methods. So, in using these two methods basically these are slow solidifying slow solidification; slow and controlled solidification methods which give rise to formation of single crystal silicon. So, you can read them through the books Czo chralski and float zone method. They are basically solidification and methods to make single crystal silicon.

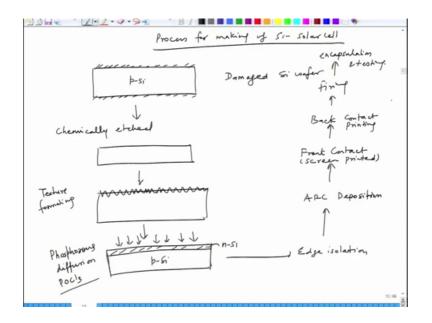
(Refer Slide Time: 21:43)



So, once you make these single crystal ingots they are like this. So, they are of the diameter about 200 to 300 millimeter. These are ingots and they are quite tall, they could be you know I think about a meter long or so out of these you need to make wafers. So, what you do is that you do wire showing of these to make wafers ok; so these are cut into small pieces to make wafers.

So, this gives rise to wafers. Each wafer is of the order of 0.3 to 0.4 mm thick when you make these wafers because of sawing, you create silicon dust. So, silicon is wasted a lot of silicon is wasted during sawing and sawing also leads to damage to the surface right; sawing also leads to damage to the surface so, wasted as well as surface damage. So, to make a device quality silicon, you again need to n do a lot of processing.

(Refer Slide Time: 23:07)



So, this is for example, so initially the silicon is more or less p type to begin with the dopants that happen to be there. So, essentially you have a silicon. This is let us say p silicon to begin with it has lot of surface damage. So, this is damaged silicon wafer, this is then chemically treated chemically etched on the surface; so that you get a nice clean surface. So, the surface is cleaned by etching to remove the rough material of the surface and then after that you do texture etching; texture formation.

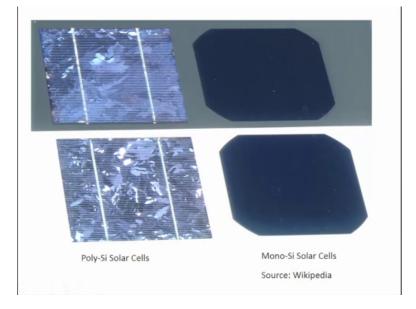
This is to improve the light coupling. So, you create this kind of so, this is called as controlled etching use of right etchants; so that you have right plane and the wafer which is etched. So, you create this kind of surface etching and then you do the diffusion of a phosphorus diffusion to make n type silicon on top. So, this is p type to be in with the phosphorous comes from top. So, gases containing phosphorus come. For example, you can have gasses like you know PoCl 3 ok. So, PoCl 3 will come from top phosphorus will diffuse.

So, this region will make n silicon, this will remain as p silicon and then you after you have done that you chop off the edges a little bit. So, you can do what we call as edge isolation because there will be some damage on the edges as well. So, you chop off the edges to make little smaller. After you have done edge isolation we do the process called as ARC deposition.

So, then you do ARC deposition on top of it. So, anti reflection coating is deposited and then we make front contact which can be screen printed and then you make what we call as back contact. So, this is sort of that you can say a procedure for making a silicon solar cell and then you do firing.

So, back contact printing you can say here; firing to just make sure that a contacts stabilizes and it is solidifies and after that you do testing encapsulation and testing encapsulation and testing. So, this is the complete flow chart of the way silicon is made ah.

(Refer Slide Time: 26:41)



So, if you have seen in one of the earlier lectures that what does it look like after once you make the contacts. On the backside, of course, you will have completely printed surface with aluminium all over the place the front side looks like this. So, these are screen printed contacts, but it will have a contacts on top.

So, you have a; you have a thicker contact on top to connect all of these layers together, but these are the fine contacts running across the length or breadth of silicon; so this is what is silicon manufacturing all about.

ŜŬਜ਼♥″/·∠·⋞·⋟⋞″₿/∎∎∎∎∎∎∎■ Summary - Si Solas Cell Mono - Design Strategies - Manufacturing - Sigle Cyptals:- >2540- M - 12-3, -Sigle Cyptals:- >2540- M - 15-18%. Poly Si - >222%, M - 15-18%.

(Refer Slide Time: 27:15)

And so, just to summarize for silicon, we have looked at silicon solar cells in quite detail for both mono and poly. We also looked at design strategies to improve the silicon quality to improve the performance, we also looked at the manufacturing in some detail not very detailed, but just a flow chart of how silicon solar cell is made. Currently silicon solar cell stands silicon is the dominant technology in the market. With single crystal silicon having about more than 25 percent efficiency and poly silicon having efficiency of more than 22 percent.

The modules are of about 19-20 percent and in this case the modules would be about 15-16 percent; this is 4 modules let me check the numbers. So, a modules would be a lower than what you gets about 20 percent, you will get 4 single crystal about 15-16 maybe 17 percent. Further you can say 15 to 18 percent for the poly silicon and these two other dominant technologies right now in the world.

So, what we will do in the next lecture is we will talk about a little bit about gallium arsenide just to look at a basic structure of gallium arsenide solar cell and then we move on to second generation technologies which is thin film solar cells.

Thank you.