

Introduction to Semiconductor Devices
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Lecture – 4.3
Energy Band diagram of PN Junction

This document is intended to accompany the lecture videos of the course “Introduction to Semiconductor Devices” offered by Dr. Naresh Emani on the NPTEL platform. It has been our effort to remove ambiguities and make the document readable. However, there may be some inadvertent errors. The reader is advised to refer to the original lecture video if he/she needs any clarification.

(Refer Slide Time: 00:15)

The slide is titled "Recap" and features a blue header. On the left, a schematic of a PN junction shows an N-region with donor concentration N_d and a P-region with acceptor concentration N_a . Below this, a charge neutrality equation is written: $N_a x_p = N_d x_n$, with a handwritten note "Charge Neutrality". To the right, three plots are shown: the top plot shows the charge density $\rho(x)$ in C/cm^3 as a step function; the middle plot shows the electric field $E(x)$ in V/cm as a triangular profile; and the bottom plot shows the potential $V(x)$ in V as a parabolic profile. A handwritten note $\frac{dE}{dx} = \frac{\rho}{\epsilon_s}$ is next to the charge density plot. At the bottom right, a presenter wearing a headset is visible. The NPTEL logo is in the top right corner, and "EE @ IIT Hyderabad" is at the bottom.

- $N_a x_p = N_d x_n$ - Charge Neutrality
- $E_0 = -\frac{qN_d}{\epsilon_{Si}} x_p = -\frac{qN_a}{\epsilon_{Si}} x_n$
- $V_{bi} = \frac{q}{2\epsilon_{Si}} (N_d x_n^2 + N_a x_p^2)$

Recap:

We discussed the basic electrostatics of PN junctions. If P type and N type semiconductors are in contact, because of the concentration gradients of holes and electrons respectively, there will be a diffusion of holes and electrons which will leave behind a positively charged space charge here in the N type semiconductor and a negatively charged space charge in the P type semiconductor and named the edge of the depletion regions as x_n and x_p respectively.

Based on Gauss law ($\nabla \cdot D = \rho_v$) as in Eq. (1), the electric field has to be a linear function in x . It has to show a triangular profile (as shown in the above screenshot). And potential is the integral of the electric field.

$$\nabla \cdot E = \frac{dE}{dx} = \frac{\rho_v}{\epsilon_{si}} \quad (1)$$

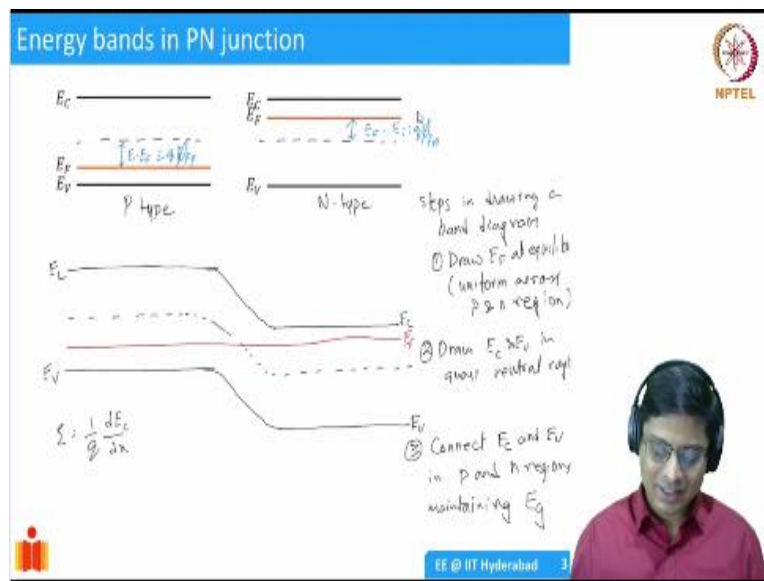
$$E = \frac{dV}{dx} \quad (2)$$

Charge neutrality condition is given by

$$x_n N_d = x_p N_a \quad (3)$$

Where in N_a – carrier concentration in the acceptor region (P type), and N_d – carrier concentration in the donor region (N type). When the holes and electrons diffuse, the area on the p side should be equal to the area on the n side. Using the Eq. (2) & (3) obtain the electric field and the potential. Note that the electric field follows triangular profile and the potential follows quadratic function of x.

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Energy bands in PN junction:

In a PN junction, we have a P type region and a N type region with corresponding doping and has fermi energy (E_F) each. There will be a constant intrinsic fermi energy for both the semiconductors (E_i). The difference in the fermi energy $E_F - E_i$ depends on the fermi potential.

$$E_{Fn} - E_i = q\phi_{Fn} \quad (4)$$

$$E_i - E_{Fp} = q\phi_{Fp} \quad (5)$$

At equilibrium, the Fermi level has to be constant and there is no other electric field applied.

Steps in drawing a band diagram:

1. Draw E_i at equilibrium which is uniform across P and N regions. Draw the E_C and E_V Quasi neutral regions that are away from the junction (as shown in the lecture slides).
2. In N region, draw the Fermi level E_F closer to E_C and draw E_V maintaining bandgap. In P region, the Fermi level has to be close to the valence band E_V .
3. Connect the E_C and E_V on both the regions maintaining E_g (material property), thus the band structure looks like in the lecture slide.

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The image shows a slide titled "Energy band diagram of a PN junction" with handwritten notes and a video feed of a lecturer. The slide includes a diagram of energy bands (E_C , E_V , E_i) across a PN junction, with regions labeled 1, 2, 3, and 4. Handwritten notes explain the quasi-neutral region, electric field, and carrier behavior. A small graph shows the potential seen by a hole.

Energy band diagram of a PN junction

① Quasi neutral region, $\mathcal{E}=0$
 \therefore independent in E_C

② Electric field -ve

③ Electron energy

④ Holes in p-type region have lower energy than n-type

⑤ Potential seen by a hole?

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Energy band diagram of a PN junction:

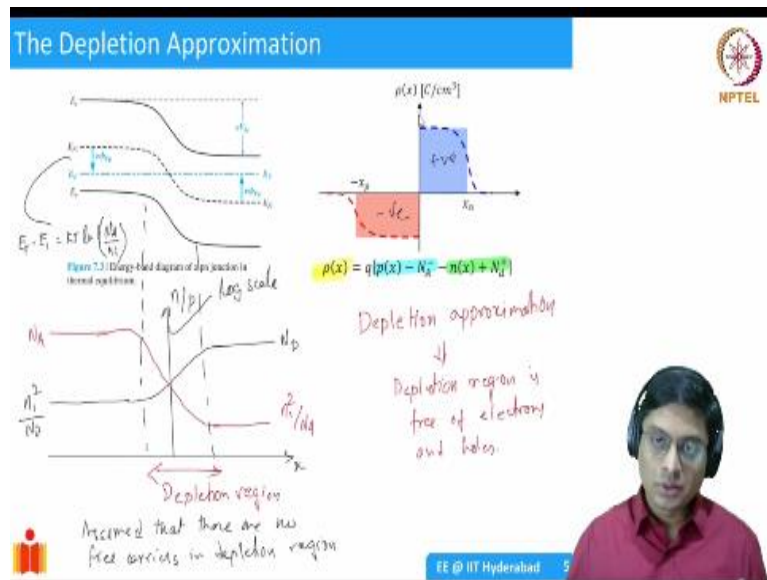
At thermal equilibrium, the energy band diagram of a PN junction is divided into mainly 4 regions.

- a. Region 1: Quasi-neutral region. The electric field is zero in this region as there is no gradient in E_C or E_V .
- b. Region 2: Depletion region, the electric field is negative as going in $-ve$ x-direction. As we go towards higher energies, electron energy increases and if lower energies, hole energy increases.
- c. Region 3, 4: N-type & P-type semiconductor regions. Since the region 4 is at higher energy, there are no transitions from region 3 to region 4. In region 4, holes in P type

region have lower energy than N type and holes stay in the P-type region. Hence there is an energy barrier and not able to go into higher energy regions.

Across the junction, the height of the barrier is known as built in potential (V_{bi}).

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Depletion Approximation:

In the depletion approximation, assume that, the depletion region is free of electrons & holes and the built in potential ' V_{bi} ' is calculated across it.

$$E_{ip} - E_F = q\phi_{Fp} = kT \ln \frac{N_a}{n_i} \quad (6)$$

$$E_F - E_{in} = q\phi_{Fn} = kT \ln \frac{N_d}{n_i} \quad (7)$$

The carrier concentration field (electron and hole) profiles are shown in the lecture slide (log scale n/p vs position x). On the P-side region, the majority carriers are holes equal to the acceptor doping concentration N_a and the minority carriers are electrons and equal to n_i^2/N_a . Whereas on the N-side region, the majority carriers are electrons and equal to the donor doping concentration N_d and the minority carriers are holes and equal to n_i^2/N_d . In the depletion region or towards the junction, the distance between E_F and E_i is reducing and sweeping, thus there is a smooth variation across the junction.

Note: Since we assume depletion approximation, the values obtained are very close to the approximated values, but not exact, as we have to consider total charge into account. Because it is difficult to solve the equations analytically, if we don't consider approximation. A demo has been shown in the other lecture.

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The slide titled "The Built-in Potential" contains the following content:

- Diagram:** Energy band diagram showing the conduction band (E_c), valence band (E_v), and Fermi level (E_f) across a PN junction. The built-in potential V_{bi} is indicated as the potential difference across the depletion region.
- Equations:**

$$V_{bi} = \phi_{FP} + \phi_{FN} = \frac{kT}{q} \ln \left(\frac{N_a N_d}{n_i^2} \right)$$

$$N_a = 10^{15} \text{ cm}^{-3}, N_d = 10^{16} \text{ cm}^{-3}$$

$$V_{bi} \approx 60 \text{ mV} \times \log \left(\frac{10^{31}}{10^{20}} \right) = 0.66 \text{ V}$$
- Additional Notes:**
 - $V_{bi} < E_g/q$
 - Si - 1.1V GaAs - 1.42V
 - Doping typically $< 10^{20} \text{ cm}^{-3}$

$$qV_{bi} = E_{Cp} - E_{Cn} = E_{ip} - E_{in} = E_{ip} - E_F + E_F - E_{in} \quad (8)$$

$$V_{bi} = \frac{kT}{q} \ln \frac{N_a N_d}{n_i^2} \quad (9)$$

At room temperature for a silicon PN junction with $n_i \sim 1.5 \times 10^{10} \text{ cm}^{-3}$, $V_{bi} \sim 60 \text{ mV} * \ln \frac{N_a N_d}{n_i^2}$

and assume $N_a = 10^{15} \text{ cm}^{-3}$, $N_d = 10^{16} \text{ cm}^{-3}$, then $V_{bi} \sim 0.66 \text{ V}$.

In case of identical semiconductors, the E_F is continuous and there is no built in potential because there is no junction formed.

The maximum built in potential in any PN junction is equal to the bandgap of the material. This can be obtained by changing the E_F closer to the E_v (E_c) on P(N) side. For example, in any Silicon based PN junction, the maximum built in voltage is 1.1 V.

$$qV_{bi} \leq E_g \quad (10)$$

The maximum doping density can be in the orders of 10^{20} cm^{-3} , the rate of Fermi level approaching the band maxima or minima is slowed down. (The reason is the carrier concentrations appear in the log term and there is a small change in the E_F). We assumed that dopants, donors and acceptors are completely ionised so far in this lecture, in this course. If we

go for higher concentrations, the donors and acceptors are not fully ionised. Only some of them are ionised and the ionisation fraction reduces as you go to higher concentrations.

The density of silicon atoms is 5×10^{22} . If you introduce 10^{20} dopants, essentially, we have one dopant in about 500 atoms which is a lot of doping density and effectively the properties of silicon itself are changed. So, for those reasons, we typically do not prefer to have very high doping.

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The Built-in Potential

Objective: Calculate the built-in potential barrier in a pn junction.

Consider a silicon pn junction at $T = 300$ K with doping concentrations of $N_A = 2.5 \times 10^{19} \text{ cm}^{-3}$ and $N_D = 10^{16} \text{ cm}^{-3}$.

EXAMPLE 7.1

$$V_{bi} = 60 \text{ mV} \times \left(\ln \frac{2.5 \times 10^{19}}{2.21 \times 10^{16}} \right)$$
$$\approx 60 \text{ mV} \times 12 = 0.72 \text{ V}$$

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For example, from the textbook problem (refer to example 7.1) calculate the approximate built in potential in the PN junction, which is approximately 0.72 V.

In the next module, will discuss about how to calculate the depletion rates and electric fields.