

Power Management Integrated Circuits
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Lecture – 34

**Transformer Model of a Buck Converter, Conduction Efficiency,
Efficiency of an LDO versus Efficiency of a Switching Regulator**

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In order to maintain V_{out} , D must be increased to compensate for V_{loss} .

$$V_{out} = D' V_{dd}$$
$$D' = D + \Delta D$$
$$D V_{dd} = D' V_{dd} - V_{loss}$$
$$D V_{dd} = D V_{dd} + \Delta D V_{dd} - V_{loss}$$
$$\Delta D V_{dd} = V_{loss} \Rightarrow \Delta D = \frac{V_{loss}}{V_{dd}}$$


So, we looked at how you calculate the losses, basically the voltage loss and that should be nothing but change in the duty cycle multiplied by your input supply voltage, which is V_{dd} .

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Diagram of an ideal transformer model for a buck converter. The primary side has voltage V_{DD} and current I_{DD} . The secondary side has voltage V_o and current I_o . The turns ratio is $1:D$.

$V_o = D \cdot V_{DD}$

for ideal transformer
 $P_o = P_{DD}$

$V_{DD} I_{DD} = V_o I_o$

$I_{DD} = D \cdot I_o$



So, buck converter can also be modelled as a transformer. So, if this is I_{DD} and output current is your I_{out} , so, for ideal transformer P_{out} equal to P_{DD} , correct, which means; V_{dd} .

So, we know V_{out} is D times V_{dd} , so what is I_{DD} ? D times I_{out} ok. So, your output voltage is multiplied by duty cycle, but when you look at the current then, input current is multiplied by the load current, which is your output, which means and D is you know it is mostly less than one. So, input current will always be less than load current and that is how you get very high efficiency.

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for load transformer

$$P_o = P_{o0}$$
$$V_{dd} I_{dd} = V_o \cdot I_o$$
$$I_{dd} = D \cdot I_o$$

$V_o = D \cdot V_{dd}$ is only true under no loss condition

$I_{dd} = D \cdot I_o$ is true for all conditions.



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So, this V_{out} equal to D times V_{dd} is only true under no-loss condition which we already looked at ok. Otherwise D times V_{dd} minus V_{loss} , but this I_{dd} equal to D times V_{out} is true for, true for all conditions ok. So, it does not matter whatever the actual duty cycle or, so, your current will automatically get scaled, but because it is a current, you are looking at a current; so and but when you look at the voltage, you drop the voltage in the resistance this power stage losses and that is why your voltage is not the actual voltage, but it will get ... , but current will always be the same irrespective of whether you are looking at a no loss duty cycle or a duty cycle under the losses.

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Efficiency of a switching converter

$$\eta = \frac{P_{out}}{P_{in}} = \frac{V_o \cdot I_o}{V_{dd} \cdot I_{dd}}$$

$\eta = \frac{D_{lossless}}{D}$ True only for conduction losses.

$D_{lossless} \rightarrow \frac{1}{D}$

So now how do you define the efficiency? Efficiency of P_{out} over P_{in} and this is one for ideal condition and less than one for, in reality when you have losses in the power stage.

So, this can be written as, $V_{out} \cdot I_{out}$ over $V_{dd} \cdot I_{dd}$. So, what is this?

$D_{lossless}$, D no loss and what is this? this portion let me change the color. So, this is your D no loss and what about this? $1/D$, which is the D actual which means I can define efficiency as, $D_{no\ loss}$ over D actual. How do you find? So, which means you do not really need to measure the power. You just look at the duty cycle you can find the efficiency, but this is only true for conduction efficiency, conduction efficiency means you are only considering the conduction losses which are occurring in the resistance $i^2 r$ losses. But if you consider the switching losses, then this formula would not be valid.

So, this is true, only for conduction losses. So, how can I find the lossless D , what is the loss actually here, $i^2 r$? So, if you make i 0 loss will be 0. So, under no load condition you get the no loss duty cycle and when you apply the load, you get the lossy duty cycle so, you can easily measure it. So, just by changing the load you can measure both $D_{no\ loss}$ and D and take the ratio of that you will get and you know, that $D_{no\ loss}$ will always be less than D because D has to increase in order to compensate for the losses. So, your efficiency will always be less than 1 or less than 100% ok.

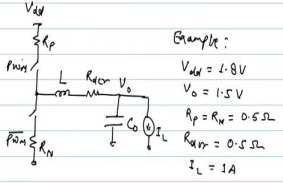
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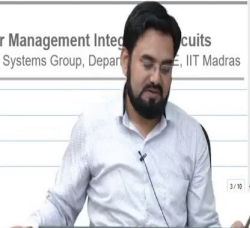
D True only for conduction losses.

Example:

$V_{dd} = 1.8V$
 $V_o = 1.5V$
 $R_p = R_n = 0.5\Omega$
 $R_{dc} = 0.5\Omega$
 $I_L = 1A$



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So, now let us go back to your power stage. So, this is so, let us take an example where V_{dd} is 1.8 volt V_o is 1.5 volt and R_p is let us say. So, for simplicity I am considering both R_p and R_n same, so the total loss will be your R resistance actually because $1 - D$ and plus D so, V_{dd} will cancel out. So, leftover will be, but if you have a R_p and R_n are different then you have to take that $1 - D$ expression into account, because in that case your losses will change. Equal to 0.5 ohm and your R_{dc} are equal to 0.5 ohm and I_L is this; I_L equal to 1 amp ok.

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$$\begin{aligned}
 V_o &= D \cdot V_{dd} - V_{loss} \\
 V_{loss} &= I_L \left(D \cdot R_p + (1-D) R_n + R_{dcr} \right) \\
 &= 1A \left(0.45 + 0.05 \right) = 100mV \\
 1.5 &= D(1.8) - 100mV \\
 \Rightarrow D &= \frac{1.6}{1.8} = 0.88 \\
 D_{no-loss} &= \frac{1.5}{1.8} = 0.833 \\
 \Rightarrow \Delta D &= 0.055 \approx 5.5\% \\
 I_{dd} &= 0.88 \times 1A = 0.88A \\
 P_{dd} &= 1.8 \times 0.88 = 1.6W \\
 P_o &= 1.5W \\
 \eta &= \frac{1.5}{1.6} = 93.75\%
 \end{aligned}$$


So, what is V out? V out is nothing, but D times V_{dd} minus V loss, and what is V loss? I_L multiplied by R_p times D , D times R_p plus 1 minus D R_n plus R_{dcr} equal to I_L is 1 amp. So, this will become 0.5 correct plus 0.5 , so this is nothing but.

1 volt, no sorry 0.05 sorry; no 0.5 is too high. So, 50 milli Ohm correct. So, this is 100 milli volt, V out is already known 1.5 volt 1.8 minus 100 milli volt this implies D equal to.

1.4 over, how much is this? Sorry, yeah, 1.6 over one point, how much is this? 0.833 . This implies ΔD is 0.033 or 3.3% . So, you get 3% change in the duty cycle in order to compensate for the losses. And what about the efficiency? So, let us calculate both ways and see if you get the same, so one way you just you know the input power output power ok. What does the input current I_{dd} ? So, your I_{dd} is. Multiplied by 1 amp which is ok, P_{dd} 1.8 multiplied by 0.88 , how much is this?

Student: 1.5 volt.

1.8 , 1.6 ?

P out 1.5 watt efficiency is, 1.5 over 1.6 , 93.75% . Ok if I am looking at percentage.

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The slide contains the following handwritten text:

$\eta = \frac{D_{no\ loss}}{D} = \frac{0.833}{0.88} \approx 93.75\%$

Efficiency of LDO

$\eta = \frac{1.5}{1.8} = 83.33\%$

No. assuming

$V_o = 0.9V$

The slide also features logos for NPTEL, IISc, and IIT Madras, along with the text "Level 1 Power management integrated circuits" and "Integrated Circuits and Systems Group, Department of EE, IIT Madras". A lecturer is visible in the bottom right corner of the slide frame.

Now, from D no loss over D 0.833 over 0.88, how much is this? 94.6, no actually it should be same. We took an approximation there that is why, 93.75%. So, I am just put a approximation here ok, so, in both the cases you get almost 94% efficiency. So, you can calculate either way if you know the power numbers, then you can just do P out by P in. If you do not know the power number you do not have a way to measure the power then and, but you can measure the duty cycle then you can just look at the duty cycle by removing the load and then applying the load and just take the ratio of that you will get the efficiency, under only conduction losses we are ignoring the switching losses here ok.

So let us see, if the same 1.5 volt was generated from LDO. So, what will be the efficiency 1.5 over 1.8, which is 83.

83% so, almost 10% decreasing the efficiency, which is not that bad. Now assume everything remains the same, but V out is 0.9 volt, which is just half of your supply ok, so, your losses will remain same because resistance is not changing load is not changing. So, from the same formula, only this factor will change 1.5 will become 0.9.

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$$\eta = \frac{D_{4161}}{D} = \frac{0.833}{0.88} \approx 93.75\%$$

Efficiency of LDO

$$\eta = \frac{1.5}{1.8} = 83.33\%$$

no assumption

$$V_o = 0.9V$$

$$0.9 = D(1.8) - 150mV$$

$$D = \frac{1}{1.8} = 0.555$$

$$\eta = \frac{0.5}{0.555} \approx 90\%$$


So, 0.9 equal to D times 1.8 minus 100 milli volt. So, D is 1 over 1.8, how much is this? 55.55? So, or 0.555, we know under no loss it will be 50%. So, you are seeing 5% difference, so, efficiency is how much 0.5 over 0.555, 90% correct? What will happen in case of LDO?

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for LDO

$$\eta = 50\%$$



So, and now if you remember the efficiency curve, if this is your V out and this is your efficiency what was happening, when V out was increasing LDO efficiency was increasing, but switching regulator was keeping very high. So, that is why you get almost flat efficiency,

I mean you will see some differences because your output power is changing ok, but losses are not changing. So, there will be some differences in the power number, but ok. But in case of LDO what happens? Your dropout voltage is reducing then V_{out} is increasing.

So when your drop out voltage is very low then this efficiency, maybe quite similar to your switching regulator or it may be in some cases even better if your dropout voltage is too low. So, let us say 50 milli volt also you are looking at dropout voltage. So, we know here we are losing 100 milliwatt in switching regulator because of this D_{cr} and switch resistance. So, which means if your drop out is 100 milli volt that is the V loss? So, the V loss is same in to in the two cases then efficiency would be same. Now if your drop out voltage is less the 100 millivolt your LDO efficiency will be better than your switching convertor efficiency ok. So, that is the reason, we prefer LDO only here for low drop out and switching regulator for smaller V_{out} by V_{in} ratio or larger dropout.

Yeah, it will, but the efficiency of switching regulator will improve, but when you hit a very light load then a switching losses will start dominating and your switching converter efficiency will also a start dropping actually in that case. But in overall if you look at the average efficiency switching regulator will have a better if you look at the wider, wide wider load rang or wider V_{out} by V_{in} ratio in both the cases your switching regulator performance will be better compared to your LDO.