

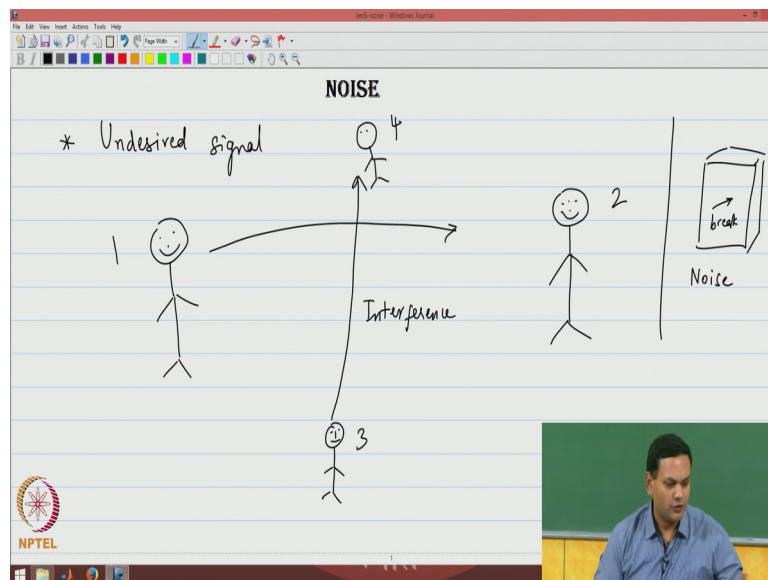
Analog Integrated Circuits
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Lecture - 06

Noise

In today's class, we are going to look at noise. Now, what do we mean by noise? Normally, noise refers to any type of undesired signal.

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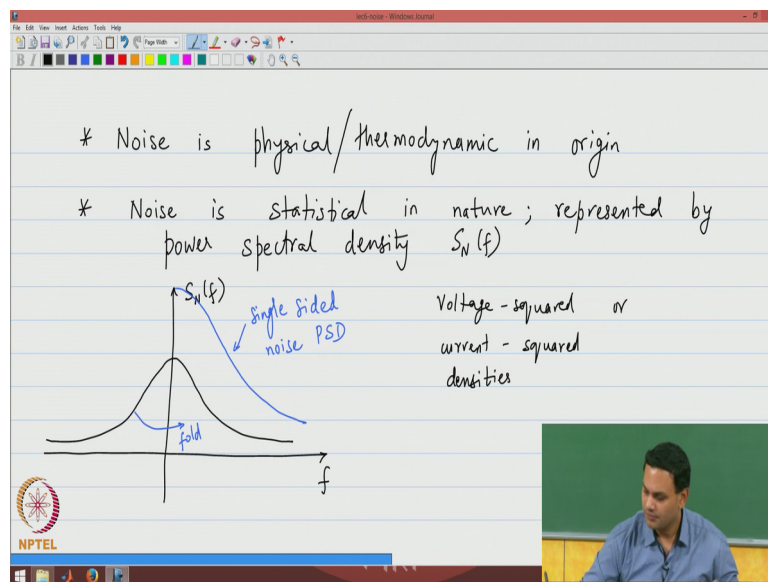
But, there are several different types of undesired signals. So, we will take the example of a person talking let us say you have a person talking to somebody across the room. So, you could have many different things; for example, you could actually have several people talking in the same room, maybe people are talking across the room in this direction also and somebody else is shouting to a fourth person.

So, let us say person 1 is trying to talk to person 2 and person 3 is trying to talk to person 4 and let us say person 3 is shouting across the room to person 4, so that person 2 is not able to listen to person 1. So, this type of signal would normally not be called noise, but it would be called an interferer, because it is you know the source of the undesired signal and the undesired

signal has very specific properties maybe it is another person trying to communicate with you know somebody else.

So, this would normally be called interference, but let us say just outside the room somebody starts using let us say jackhammer or something like that to break some structure. So, outside this room somebody is trying to break or let us say breaking a wall. Somebody is actually breaking this wall with a continuous din. In that case, there is no signal being communicated. This sound would be called noise because this is not somebody trying to communicate with anybody else it is an undesired effect of something some other activity happening.

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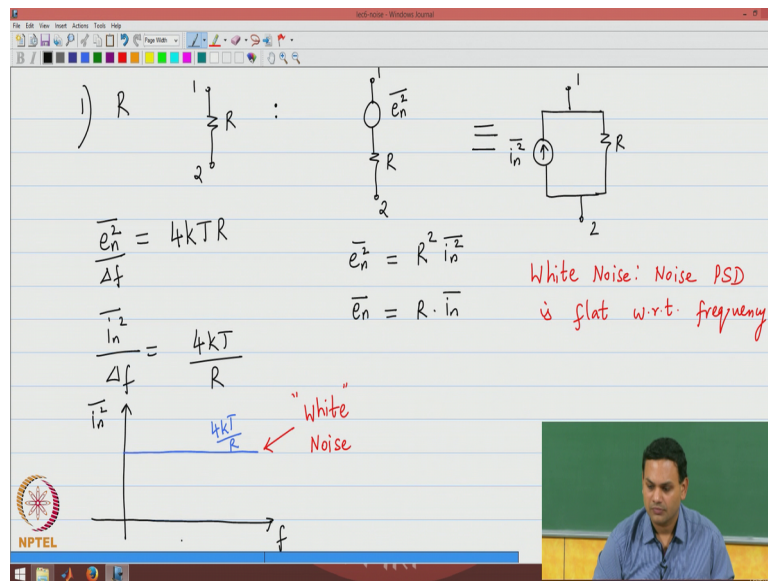
So, for the purposes of this case, we will point out that when we talk about noise; noise is physical or thermodynamic in origin and what that means, is; that means, that noise is not or noise could be dependent on physical properties such as temperature and we will see this in a few minutes. But, more importantly noise is then statistical in nature and therefore, represented by a power spectral density.

Let us call this $\sum S_N$ of f . So, if I were to draw a power spectral density for noise. It may have some representation I will show some behavior like this. Now, as it turns out when we work with circuits we normally look at the one-sided representation in the frequency domain and therefore, what we will do is, we will take the signal content in the negative frequency and

fold it over 2 positive frequencies which means my noise will now get doubled. So, this would be the single sided representation of the noise power spectral density. Now, in the case of circuits when we say power we normally represented as voltage squared or current squared densities.

Now, let us start looking at taking some simple circuits and we will start looking at the noise associated with those circuits.

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The simplest element that we will consider is a resistor. Let us say it has a value R , the noise associated with the resistor R can be represented in 2 ways; one is in terms of the voltage, the other is in terms of the current. If I represent it as voltage, let the 2 nodes be 1 and 2, I will represent it as a voltage in series with the resistance R and I am going to call this e_n^2 which is the mean squared voltage in series with the resistance and an equivalent representation is a current which is represented as a mean squared current i_n^2 between the same 2 terminals, 1 and 2.

And because you are talking about mean squared you can go between the Thevenin representation and the Norton representation and in terms of Thevenin representation you can say that the dependence between the Thevenin and the Norton equivalents is in the following way; e_n^2 is R^2 times i_n^2 or if you are talking about the root mean

squared RMS quantities which I will represent as e_n and i_n respectively. This is the relationship between the 2 quantities.

What about the value? Now, the value of e_n and i_n are themselves physical in origin and the expression is given in the following way; e_n squared which is the mean squared voltage density is $4kTR$ and therefore, the mean squared current density is $4kT$ by R . What does this look like in the frequency domain if I were to plot these 2 quantities? Let me plot e_n squared and again I am going to this is the one sided value. So, if I were to plot i_n squared as a function of frequency clearly because it has a constant power spectral density, the value is $4kT$ by R and it does not change with frequency.

Such a source of noise or such a type of noise is called white noise. White noise is something is a noise that has a constant power spectral density with respect to frequency.

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2) $L, C, M \rightarrow$ No noise

3) $\begin{matrix} D \\ \downarrow \\ \overline{i_{dn}^2} \\ \downarrow \\ \text{MOSFET} \\ \downarrow \\ S \end{matrix}$

a) OFF: No noise $\{I_D = 0\}$

b) Triode: $g_{ds} = \frac{\partial I_D}{\partial V_{DS}}$

$$g_{ds} = \frac{\partial}{\partial V_{DS}} \left[\mu_n C_{ox} \left(\frac{W}{L} \right) \cdot \left\{ (V_{GS} - V_T) \cdot V_{DS} - \frac{V_{DS}^2}{2} \right\} \right]$$

$$g_{ds} = \mu_n C_{ox} \left(\frac{W}{L} \right) (V_{GS} - V_T - V_{DS})$$

$$\frac{\overline{i_{dn}^2}}{\Delta f} = 4kT \cdot g_{ds}$$

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The second type of element that we will talk about are inductors and capacitors and it turns out that inductors and capacitors do not produce noise they do not have any noise associated with them and that of course, includes transformers also. I will represent that as M . So, inductors capacitors transformers do not have any noise associated with them.

However, if you have non ideal inductors; for example, if you represent the series resistance of the coil used to create the inductance by a resistance R , this will have thermal noise.

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1) R : $\begin{matrix} 1 \\ | \\ R \\ | \\ 2 \end{matrix}$: $\begin{matrix} 1 \\ | \\ e_n^2 \\ | \\ R \\ | \\ 2 \end{matrix}$ \equiv $\begin{matrix} 1 \\ | \\ i_n^2 \\ | \\ R \\ | \\ 2 \end{matrix}$ Thermal Noise of Resistor

$\frac{e_n^2}{\Delta f} = 4kTR$ $e_n^2 = R^2 i_n^2$ White Noise: Noise PSD is flat w.r.t. frequency

$\frac{i_n^2}{\Delta f} = \frac{4kT}{R}$ $e_n = R \cdot i_n$

$\frac{4kT}{R}$ "White Noise"

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This will have resistor thermal noise, but this inductance will have no noise.

Next, we will look at a MOSFET. The MOSFET of course, has 3 terminals; drain, gate and source. Since the gate current is 0, there is no noise associated with the gate current, but there is noise associated with the drain current and I will represent that as i_n , the whole squared.

Now, clearly the MOSFET has 3 regions of operation and the noise of the MOSFET is different in each region. When the MOSFET is off for example, when the gate source voltage is smaller than the threshold voltage, that device will have no noise because it does not conduct. In the triode region, that device has conductance g_{ds} that is given by $\frac{dI_D}{dV_{DS}}$. So, g_{ds} in the triode region is given by $\frac{dI_D}{dV_{DS}}$ of the current expression. The current expression is $\mu_n C_{OX} \frac{W}{L} (V_{GS} - V_T) V_{DS}$ in the triode region and therefore, it turns out that the thermal noise associated with the device in the triode region is given by the squared current density is given by $4kT g_{ds}$.

So, we have to take the first derivative with respect to the drain source voltage and this g_{ds} is given by $\mu_n C_{OX} \frac{W}{L} (V_{GS} - V_T)$ in the triode region and therefore, it turns out that the thermal noise associated with the device in the triode region is given by the squared current density is given by $4kT g_{ds}$.

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c) Saturation region: $\frac{\overline{i_{dn}^2}}{\Delta f} = 4kT \cdot \left(\frac{2}{3}\right) \cdot g_m = \frac{8kT}{3} g_m$

Thermal noise of MOSFET is White Noise

4) MOSFET flicker noise or $1/f$ noise

In saturation region:

$$\frac{\overline{i_{dn}^2}}{\Delta f} = \frac{K_{1f}}{W \cdot L \cdot C_{ox}} \cdot \frac{g_m^2}{f}$$

* flicker noise is "coloured" noise

The diagram shows a MOSFET with gate (G), drain (D), and source (S) terminals. A downward arrow from the drain terminal is labeled $\overline{i_{dn}^2}$.

The third region of operation of the device is the saturation region. In this region the device thermal noise happens to be dependent on the trans-conductance rather than g_{ds} and this is given by $4kT$ times factor two-thirds times g_m or $\frac{8kT}{3}$ times g_m .

This is the thermal noise of the device in the saturation region.

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2) L, C, M \rightarrow No noise

3) Thermal Noise

a) OFF: No noise $\{I_D = 0\}$

b) Triode: $g_{ds} = \frac{\partial I_D}{\partial V_{DS}}$

$$g_{ds} = \frac{\partial}{\partial V_{DS}} \left[\mu_n C_{ox} \left(\frac{W}{L}\right) \cdot \left\{ (V_{GS} - V_T) \cdot V_{DS} - \frac{V_{DS}^2}{2} \right\} \right]$$

$$g_{ds} = \mu_n C_{ox} \left(\frac{W}{L}\right) (V_{GS} - V_T - V_{DS})$$

$$\frac{\overline{i_{dn}^2}}{\Delta f} = 4kT \cdot g_{ds}$$

The diagram shows a MOSFET with gate (G), drain (D), and source (S) terminals. A downward arrow from the drain terminal is labeled $\overline{i_{dn}^2}$.

Sofar, we have been looking at the thermal noise of the MOSFET. It so happens that the MOSFET also has a different type of noise. So, please note, that the thermal noise is flat in nature with respect to frequency. They all have constant power spectral densities and therefore, they are white noise sources.

The next type of MOSFET noise that we are going to look at is called flicker noise. So, when the MOSFET is in the saturation region the MOSFET has one more source of noise and I am going to refer to that as $1/f$ noise. This is also called $1/f^2$ noise because the power spectral density is inversely related to frequency. In the saturation region the device has a $1/f$ noise that is related in the following way, the $1/f$ noise current density is given by some constant k_f over $W \times L \times C_{ox} \times g_m^2$ over f .

So, there are several things to note for the flicker noise; number one, flicker noise is colored noise, that is, the noise spectral density the current squared mean squared spectral density does vary with frequency.

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* flicker noise is normally present when the transistor has bias current (ie in saturation region)

* $1/f$: flicker noise is dominant at very low frequencies

* $1/f$ noise : k_f/f is process dependent

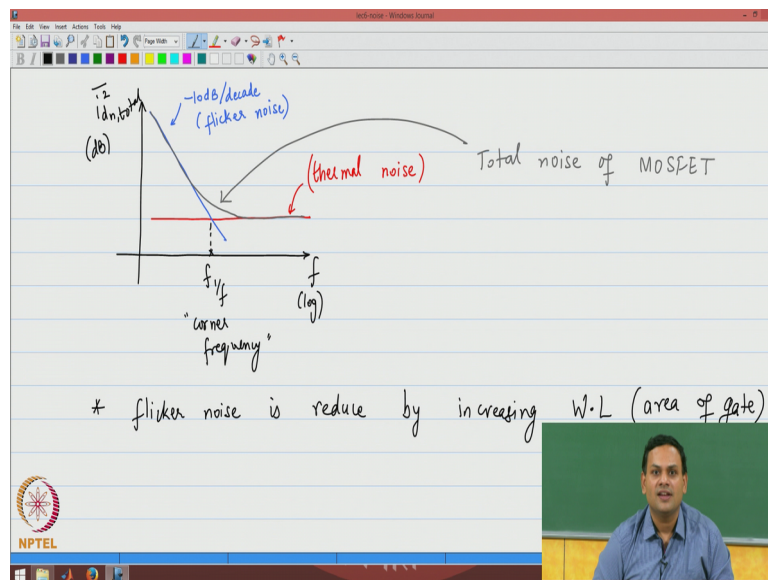
The diagram illustrates a cross-section of a MOSFET. It shows a central channel region with a gate (G) on top. The channel is flanked by drain (D) and source (S) regions, both labeled as n+. The substrate is labeled as p-sub. The NPTEL logo is visible in the bottom left corner of the slide, and a small video inset of a speaker is in the bottom right corner.

Number 2; flicker noise is normally present when the transistor has a bias current, in other words, in the saturation region. You do not expect to see flicker noise in the triode region where normally the transistor is used as a resistor. Number 3; because of $1/f$ dependence you expect flicker noise to be dominant at very low frequencies. We are in a minute we are going to draw the spectral content. Finally, this flicker noise or $1/f$ noise, it has a

process dependent parameter which is called K_1 over f and therefore, different processes could have different amounts of flicker noise.

Now, the reason this happens is because flicker noise is related to unwanted traps in the silicon to silicon dioxide boundary inside the MOSFET at the gate oxide to the channel interface. So, when you have when you build the MOSFET, you may have unwanted traps in this region. In the interface between the substrate the channel region and the oxide and when you have them you could have charge carriers periodically being trapped in a random manner and released in a random manner again. And that causes flicker noise and therefore, if you are able to build a process which has lower density of traps you might very well have lower flicker noise for that process.

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Having said this, let us now draw the noise of the MOSFET. Let us draw the total noise of the MOSFET. I will first show the flicker noise; let us assume that this is the, I am going to plot the total with the individual contributions also shown. So, I will first show the flicker noise in blue and I am going to show it in a log-log scale. In fact, even better is, if I show it in a decibel scale. So, if I do this, $1/f$ noise we will have a power spectral density which looks like this. Since, I am drawing it in a decibel scale I will see that the magnitude of this noise drops at a rate of minus 10 dB per decade change in frequency.

The blue color shows the flicker noise. In red color I am going to show thermal noise of the device. Note that; thermal noise is white noise and it has a flat power spectral density and therefore, if you look at the total noise at very low frequencies the noise is dominated by the noise from flicker noise and at very high frequencies the noise is dominated by thermal noise.

So, normally you would expect that flicker noise is dominant at very low frequencies and thermal noise is dominant at very high frequencies. Now, how do you decide at what frequency each one is dominant that completely depends on the values of g_m that is the thermal noise level and k and g_m and $W L C_{OX}$, so that, you would have a resultant noise corner frequency which is called f_c or corner frequency. This corner frequency is completely dependent on the values of g_m , k , $W L C_{OX}$ etcetera.

Finally, one last thing to note; you can reduce flicker noise by increasing the area of the gate. So, the gate area of the MOSFET is $W \times L$ and if you increase that the absolute value of flicker noise is reduced because flicker noise squared noise density is inversely proportional to that $W L C_{OX}$.

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In today's lecture we will look at one final topic for just a couple of minutes. Let us say, now that I have learnt about sources of noise, let us say I have some 2 port network. That consists of R , L , C and MOSFETs. Clearly, this 2 port network is going to generate noise. So, I will call it a noisy 2-port. I want to find some way to represent the noise of this 2 port.

Now, clearly I can do it in 2 ways. I can represent, I can take all the noise sources and if I want to represent it in a system manner I need to represent it with respect to either the input port or the output port. Just like at a very high level just as we would represent the system parameters by the z , y , g , h parameters. We can represent the noise the total noise produced by the 2 port either at the input or at the output.

Now, if you represent it at the output, it turns out this is not a fair way of comparing different 2 ports. The reason we want to represent the noise at the input and the output is to compare 2 different networks and see if one is noisier than the other. For example; I have 2 amplifiers, with gains A_1 and A_2 . I want to find out in a particular application whether I should use A_1 or A_2 with respect to noise. I need to find some way to represent this. If A_1 was much larger than A_2 , if I look simply at the output, it is very possible that V_{O1}^2 is much larger than V_{O2}^2 .

So, in other words the output noise of A_1 may look much larger than that of A_2 , because if I keep increasing the gain of the circuit, the noise levels will look larger and larger. The important thing to note is we are really interested in signal to noise ratio. If you increase the gain A_1 the signal at the output of A_1 is also larger. So, we want to find out of fair way of representing it and the fair way of representing the noise of a 2-port is at the input. So, we convert it to a noiseless 2-port with a representation at the input and in general, they are represented by 2 sources at the input; a voltage source in series with the input and a current source in shunt with the input.

So, you need both e_n^2 and i_n^2 , in general, to represent 2-ports with all range of input resistances and all range of source resistances. So, in general any 2-port will have a e_n^2 voltage noise source in series with the input and a current noise source in shunt.

In the next lecture, we will look at some example circuits, MOSFETs resistors where we will calculate the input referred noise current and voltage sources.