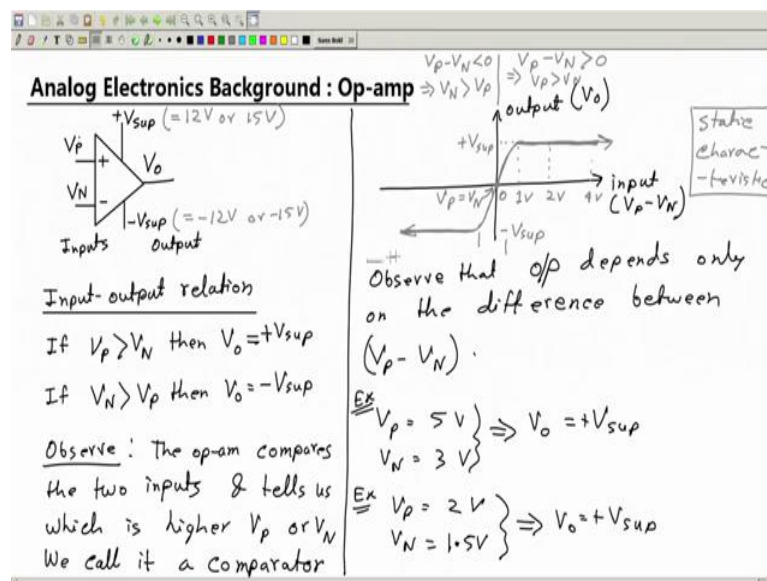


Electrical Measurement And Electronic Instruments
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Lecture - 49
Background: Operational Amplifiers - I

Hello and very welcome to this next week and previous week we have studied digital electronics.

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Today, this week we will study, analog electronics and op-amps ok. So, op-amp is often very frightening, very scaring to a many student, which was also true to some accident to me but I think if you study it carefully this is one of the easiest equipment, easiest element in electrical circuits ok.

What is an op-amp? Op-amp is nothing but an electrical equipment, which has 2 inputs and 1 output ok. So, we you know (Refer Time: 01:14) we draw it with a triangle ok. And, it has two inputs we denote them as plus and minus and it has one output ok. So, these are inputs and this is the output ok.

So, we a denote the two inputs with plus and minus symbol and we call them as a inverting and non-inverting inputs minus for inverting plus for non-inverting inputs ok, you can also call for simplicity plus input minus input ok.

So, this is what happened this? Now, what we need to know; we need to know how it works? Ok. So, what is the relationship between the input and output ok. So, input output relation and it is very simple. So, if I call this input let me call this V_P P for positive voltage positive and call this V_N N for negative. So, these are the 2 inputs, I am giving them in V_P and V_N and this equal V_O O for output ok.

If $V_P > V_N$ $V_O = + V_{supply}$

If $V_N > V_P$ $V_O = - V_{supply}$

So it has 2 inputs, 1 output, 2 power supplies among other things. So, these are the main things it has two power supplies if these two voltages are plus V_{supply} and minus V_{supply} , then if V_P is greater than V_N output is positive, if V_P is less than V_N output is negative. So, you observe the op-amp compares the two inputs two inputs and tells us which are which one is had and tells us which is higher V_P or V_N this is so, simple ok.

So, that is why we call it we call an op-amp also as an comparator ok. Because, it compares the two inputs and tells us which is higher V_N higher V_P higher or V_N higher as simple as that ok. So, now, when you know this, this simple thing let me show you a bit complicated representation of this simple fact, which is a pictorial graphical representation of the simple fact. So, what I want to do, I want to draw a graph a picture representing input versus output ok. So, I will draw input versus output.

Output is nothing but V_O but I am in trouble because we have two inputs V_P and V_N , but I have one x axis, one input axis how do I put two; two inputs V_P and V_N , I do not have a 3D graph I do not want to make it that complicated but what I will do just see observe that, output depends only on the difference between V_P and V_N call it V_P minus V_N .

It does not depend on the value of V_P or V_N separately, it depends only on the difference. If the difference is say positive that is V_P is higher than the output is positive and if this difference is negative; that means, output; that means, V_P is lower than V_N then output will be negative ok.

So, it is possible that $V_P = 5$ volt V_N is equal to 3 volt, then the this is implied V_O equal to V_P is higher. So, V_O will be equal to V_{supply} ok; example. Similarly another example

you take V_P is equal to say here 2 volt and V_N is equal to say 1.5 volt, then will so, V_o will be equal to plus V supply.

It does not depend on the actual value of V_P or V_N . It depends only on the difference right; it does not depend on the actual value of V_P or V_N individually it depends on the difference. Therefore, why do you not put the difference as the input instead of putting V_P or V_N separately right?

So, I will use this now you observe that on this side right side on this side $(V_P - V_N) > 0$ ok. So, on this side right side we have $(V_P - V_N) > 0$ and on this side we have $(V_P - V_N) < 0$ this implies $V_P > V_N$ and, this implies $V_N > V_P$.

Now, what does the rules, say if V_P is higher output will be positive ok. And, if we so, here we will have a positive output whose value will be equal to plus V supply and on it does not depend on the difference actually, the difference can be V_P so, say here V_P minus V_N is 1 volt, then also the output is plus V supply. So, here at this point the V_P minus V_N is 2 volt, then also the output is same plus V supply here say here this value is 4 volt so; that means, V_P is 4 volt more than V_N , then also the output is plus V supply.

So, therefore, we have this as straight horizontal line. It does not change ok; and, so, normally if say for example, normally we have this V supply is equal to 12 volt or may be 15 volt these are the normal values, then in this case we will have minus V supply equal to minus 12 volt or minus 15 volt, then this value is actually 12 volt or 15 volt.

Now, the difference can be 1 volt 2 volt 4 volt anything, but the output will be same as 12 volt constant, it is not going to change ok, but on this side left side V_P is less than V_N . So, output now should be negative ok. And, it will be negative and constant no matter how large or small this differences is, no matter how more the V_N is compare to V_P it can be more by 1 volt. It can be more by 2 volt what is output will always be negative.

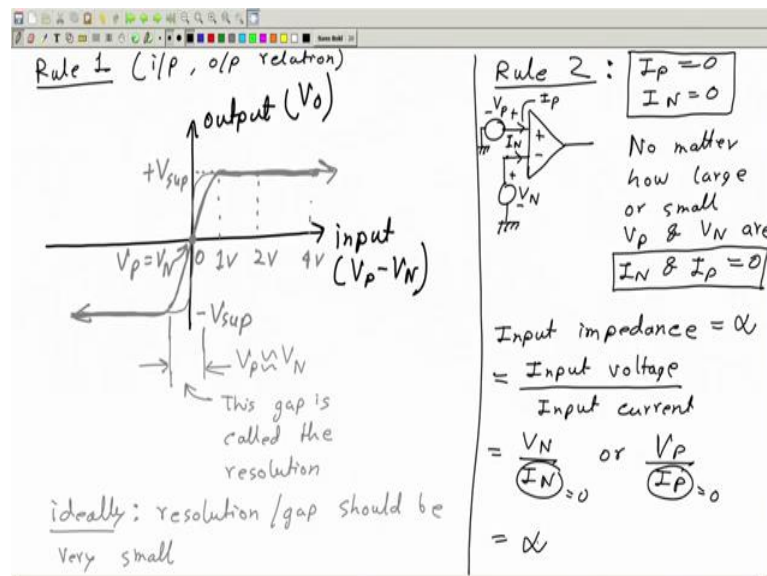
So, this is how it is constantly negative equal to minus V supply and here you can put an arrow to indicates that, it goes on like this flat horizontal also called you can call this saturation ok, it does not increase or decrease any further ideally. So, this is the graphical behavior. The (Refer Time: 11:30) fact just 2-line fact that if V_P is higher output is positive; if V_N is higher output is negative. The small fact can also be represented it

graphically pictorially with this diagram and this diagram we called we call it static characteristic.

Why static? Ok. Just, I have to say in one line because the output in the input output relationship is true strictly true, if the input or output is not changing with time, then only this this relationship is strictly true but if input and output is changing with time, particularly you were changing very fast, then the input output relationship does not follow this pattern or this rule ok. So, that is why we call it static characteristic ok, roughly speaking ok.

Now, you may ask the question what happens if $V_P = V_N$; that means, add this point ok. So, what happens here? If, add of V_P minus V_N is equal to 0. That means V_P is equal to V_N at this point. The answer is at this point or close to the region of this point or V_P is very close to V_N , the output changes slowly from the negative to the positive value like this and at this moment I do not bother much whether this is a straight line or a curved line at this moment I do not care. The important fact that I say is that, near this region where I mean this region ok, where V_P is close to V_N output changes gradually from negative to positive let me just copy this diagram.

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So let me just zoom into this part, this part only ok. Here, where V_P is approximately equal to V_N , then the output changes gradually from minus to plus value. This and this gap where the output goes from negative to positive is called the resolution of an op-amp; this

gap is called the resolution. Ideally this gap the resolution or the gap should be very small and ideally 0 for an ideal op-amp we will say that this is equal to 0 so that means, the pattern the input output characteristics should be like this.

Almost, vertical line like this this is the ideal characteristic and for a particular op-amp of course, it will not 0, this and this the larger the gap is we the we will say the op-amp is bad (Refer Time: 15:08) smaller the gap is we will say the op-amp is better why, because this is the region were the op-amp is confused.

What is the task of an op-amp? The task of an op-amp is compare to 2 inputs until which input is higher, how does it tell by making the output either positive or negative. If, V_P is positive then it tells that, sorry if V_P is higher, then it tells that then by making output positive but if V_N is higher, then it tells that by making output negative, but in this region where V_P is close to V_N op-amp is confused, which is higher they are very closed.

So, that is why it does not make the output to positive completely positive or completely negative, it makes the output somewhat in between ok, it is confused it is not very sure.

So, the output is not completely positive not completely negative. So, output is somewhere in between depending on how large V_P is compared to V_N , how large V_N is compare to V_P . So, this is the region where op-amp is confused. So, this smaller the region is we say the op-amp is better ok. That is it; that is the main thing one should know about the op-amp this is the first rule that one should know about the op-amp.

Now, the second important rule that one should know about the op-amp is that call it rule 2 ok. So, this is Rule 1, then this is nothing, but input, output relation and Rule 2 says that, this is my op-amp plus minus these are the 2 inputs. Now, I can connect this to input to any voltage source, this is also 20 voltage source like this, this way rule 2 says, this current call it I_N and this current call it I_P ok. The current that goes through the input terminal positive input terminal P for positive and this current I_N , through the negative input order. The Rule 2 says, I_P is equal to 0 for an ideal op-amp and I_N is also equal to 0 for an ideal op-amp.

No matter, how large or small this voltages are this voltage is V_N , this voltage is V_P . No matter, how large V_N and V_P is I_N and I_P is going to be 0, which means no current can go in into the op-amp through it is input terminal, either of this input terminals. Similarly,

no current can come out from these input terminals ok. So, no matter how large or small V_P and V_N are I_N and I_P are equal to 0, practically it is not 0 this is for theoretical op-amps ideal op-amps, practically it is not 0 but it is very small maybe microamperes; you can neglect it is so, small that for most of the calculations you can neglect it ok.

$$\text{Input impedance} = \frac{\text{input voltage}}{\text{input current}} = \frac{V_N}{I_N} \text{ or } \frac{V_P}{I_P}$$

The denominator this is equal to 0, this is equal to 0 or very small and this is a finite number numerator is a finite number therefore, this is equal to infinite. So, this is the second rule about an op-amp ok.

So, these are the 2 rules about an op-amp than that one should remember and with only this rules one can solve almost any problem involving op-amp. One can analyze any op-amp base circuits using only this 2 rules, what are this those these 2 rules number 1, the input output characteristics shown here, which if I say normally output is positive if V_P is higher, output is negative if V_P is lower rule number 1 and rule number 2; no current can go into the op-amp through the input terminal; no current can come out from the input terminal that is all.

So, let us continue from this point onwards ok.

Thank you.