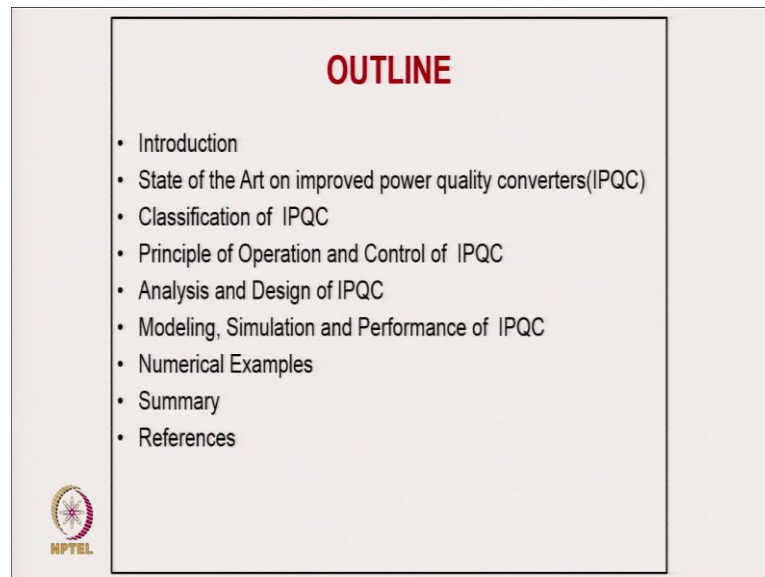


**Power Quality**  
**Prof. Bhim Singh**  
**Department of Electrical Engineering**  
**Indian Institute of Technology, Delhi**

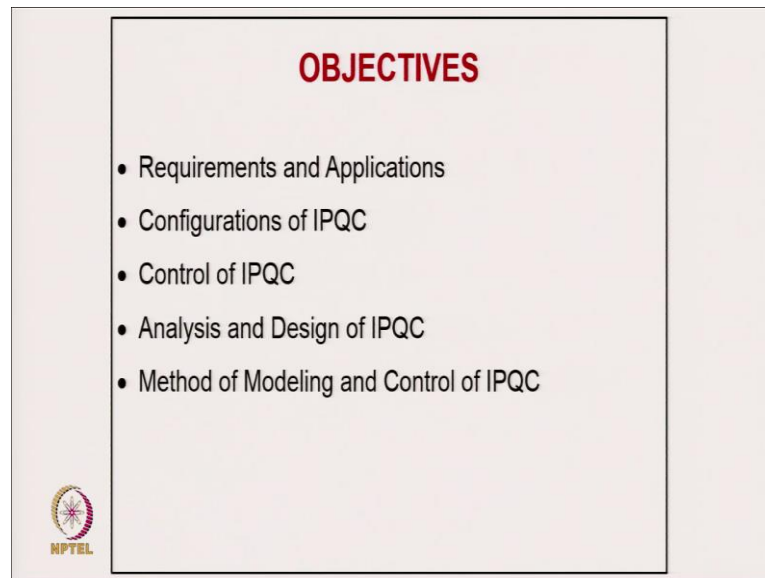
**Module - 01**  
**Lecture - 30**  
**Improved Power Quality Converters- AC-DC Boost Converters**

(Refer Slide Time: 00:29)




Welcome to the course on Power Quality. We will discuss today improved power quality converters AC to DC Boost Converter. Coming to outline part of it, starting with the introduction, state of art on improved power quality converters, we will discuss about classification, then principle of operation and control of improved power quality converters, then analysis and design of improved power quality converter, and modelling simulation and performance of improved power quality converter. We will have some numerical examples followed by summary and references.

(Refer Slide Time: 00:55)



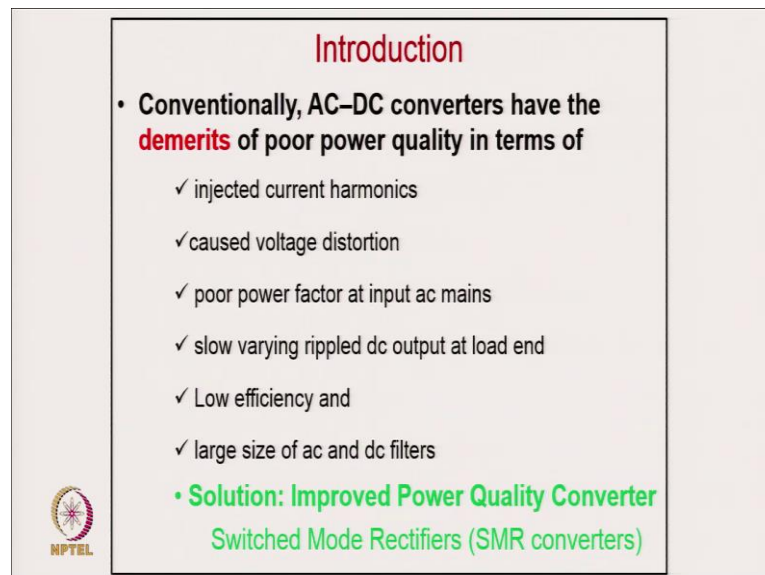
**OBJECTIVES**

- Requirements and Applications
- Configurations of IPQC
- Control of IPQC
- Analysis and Design of IPQC
- Method of Modeling and Control of IPQC

 NPTEL


First, we would like to discuss about the objectives of this lecture which mainly include the requirement and applications of the improved power quality converters (IPQCs), configuration of IPQCs, control of IPQCs, analysis and design IPQCs, and method of modelling and control of IPQCs.

(Refer Slide Time: 01:19)



**Introduction**

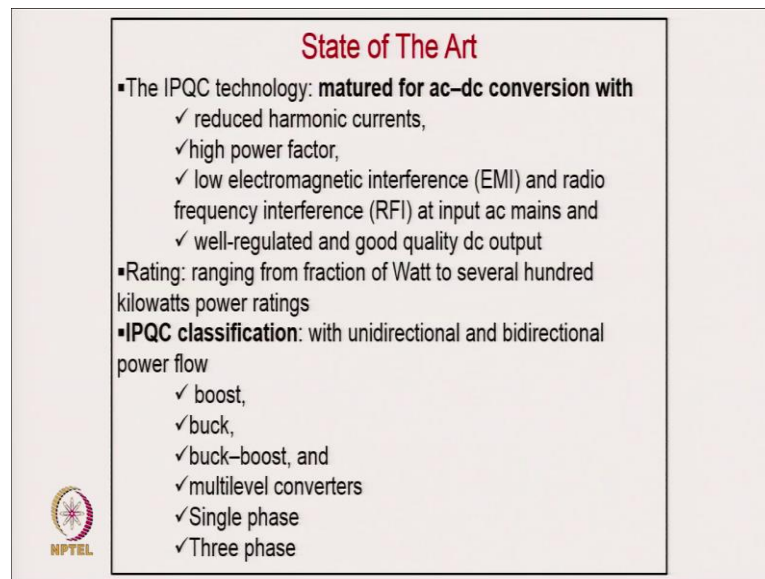
- Conventionally, AC–DC converters have the **demerits** of poor power quality in terms of
  - ✓ injected current harmonics
  - ✓ caused voltage distortion
  - ✓ poor power factor at input ac mains
  - ✓ slow varying rippled dc output at load end
  - ✓ Low efficiency and
  - ✓ large size of ac and dc filters
- **Solution: Improved Power Quality Converter**  
Switched Mode Rectifiers (SMR converters)

 NPTEL

Well, conventional AC-DC converters have demerits of poor power quality in terms of injected current harmonics, which causes the voltage distortion at the point of common coupling where they are connected and also caused poor power factor at the input AC


mains, Further, the slow varying ripple in the DC output at load end, low efficiency, and large size of AC and DC filters are other major shortcoming of conventional AC-DC converters. The solution to these problems are improved power quality converters which also known as switched mode rectifier or also in short we call it switched mode SMR converters.

(Refer Slide Time: 02:03)



**State of The Art**


- The IPQC technology: **matured for ac-dc conversion with**
  - ✓ reduced harmonic currents,
  - ✓ high power factor,
  - ✓ low electromagnetic interference (EMI) and radio frequency interference (RFI) at input ac mains and
  - ✓ well-regulated and good quality dc output
- Rating: ranging from fraction of Watt to several hundred kilowatts power ratings
- IPQC classification:** with unidirectional and bidirectional power flow
  - ✓ boost,
  - ✓ buck,
  - ✓ buck-boost, and
  - ✓ multilevel converters
  - ✓ Single phase
  - ✓ Three phase



Coming to the state of art, the improved power quality converter technology is matured for AC-DC conversion with reduce harmonic currents, high power factor, low electromagnetic interference and radio frequency interference at input mains with well-regulated DC output. The rating of these converters varies from fraction of watt to several 100-kW.

Further, the improved power quality converters are classified into different category and these are the typical classification with unidirectional and bidirectional power flow like boost converters buck converter, buck boost converter, multi-level converters. Of course, the another classification is based on type of supply like single phase and three phase supply.


(Refer Slide Time: 02:55)



- For Example: unidirectional boost converter is used in **SMPSs, low-rating ASDs in fans, air conditioners, etc.,**
- Other category of IPQC for high-voltage and high-power applications are
  - ✓ multilevel converters
  - ✓ multipulse converters

For example, the unidirectional boost converter is used in switched mode power supplies, low power adjustable speed drive in fans, air conditioners, welding machines, and in other home appliances like in washing machines, ceiling fans, etc. The other category of improved power quality converters for high voltage and high power applications are like multi-level converters and multi-pulse converters.

(Refer Slide Time: 03:27)



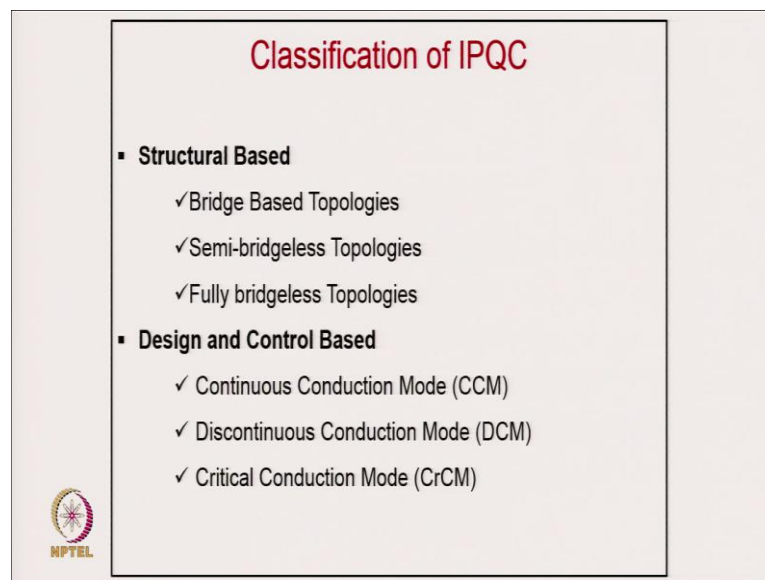
### Classification of IPQC

- **Topology Based**
  - ✓ boost,
  - ✓ buck,
  - ✓ buck-boost,
  - ✓ multilevel,
  - ✓ Unidirectional and bidirectional voltage,
  - ✓ current, and power flow
- **Converter Based**
  - ✓ Voltage source converters
  - ✓ Current-source converters
  - ✓ Step-up choppers
  - ✓ Step-down choppers,
- **Supply Based:** Single and Three phase supply system

Now coming to the classification of the improved power quality converters (IPQCs), based on topology the IPQCs are classified into boost converter, buck converter, buck boost converters, and multilevel converters.

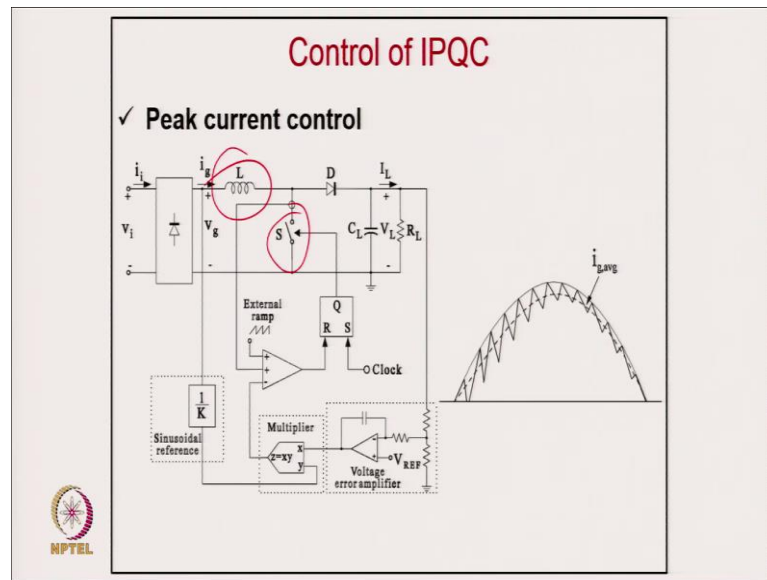
Well, another classification is based on the type of converters, i.e., voltage source converters or current source converters. Similarly, another classification is based on type of supply like single phase and three phase supply.

(Refer Slide Time: 04:04)



Based on structure, the IPQCs can be like a bridge-based topology or semi bridgeless topology or fully bridgeless topology. Further, based on the design and control, the IPQCs can be designed and controlled in continuous conduction mode, discontinuous conduction mode or critical conduction mode.

(Refer Slide Time: 04:45)



Starting with a single-phase boost IPQC, it consists of a simple diode rectifier followed by an inductor and the switch and diode and filter capacitor. The very purpose of this is to get a regulated output DC voltage with improved power quality at AC mains. In boost type IPQCs, the output voltage always remains greater than the peak of input AC voltage. It is also known as boost power factor corrected converter and extensively used in plenty of applications because the major advantage of such converters is that, you get regulated DC voltage irrespective of varying AC mains voltage.

As far as the control of IPQCs is concerned, several types of control approaches are discussed here. Starting with the peak current control, the concept of peak current control is to control the output voltage across the load, while controlling peak current through active semiconductor switch. Here, the output voltage is feedback and we have a reference voltage at which the output voltage needs to be regulated. Then, we have an error amplifier, which is normally a PI controller, to reduce the error between reference and feedback voltage. The output of error amplifier is considered as the amplitude of supply current, which is multiplied by the rectified voltage to obtain reference current waveform. Now, the reference current is compared with sensed switch peak current and the error is then compared with the external ramp. Finally, the R S digital flip flop provides the required gating signal to the semiconductor switch.

(Refer Slide Time: 08:58)

### Control of IPQC


✓ **Peak current control**

**Advantages:**

- Constant switching frequency
- Only the switch current must be sensed and this can be accomplished by a current transformer, thus avoiding the losses due to the sensing resistor.
- No need of current error amplifier and its compensation network.
- Possibility of a true switch current limiting.

**Disadvantages:**

- Presence of subharmonic oscillations at duty cycles greater than 50%, so a compensation ramp is needed.
- Input current distortion which increases at high line voltages and light load and is worsened by the presence of the compensation ramp.
- Control more sensitive to commutation noises.


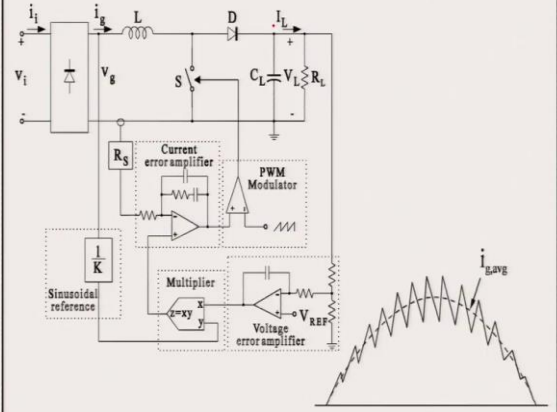


These are the advantages and disadvantages of the peak current control.

(Refer Slide Time: 09:55)

### Control of IPQC

✓ **Average current control**



Well, the another control for the boost power factor corrected converter is the average current control. In this, the control action is based on the inductor average current and the control loop remain identical to previous control.

(Refer Slide Time: 10:43)

### Control of IPQC


✓ **Average current control**

**Advantages:**

- Constant switching frequency
- No need of compensation ramp;
- Control is less sensitive to commutation noises, due to current filtering;
- Better input current waveforms than for the peak current control since, near the zero crossing of the line voltage, the duty cycle is close to one, so reducing the dead angle in the input current.

**Disadvantages:**

- Inductor current must be sensed;- a current error amplifier is needed and its compensation
- Network design must take into account the different converter operating points during the line cycle


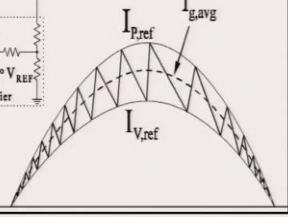
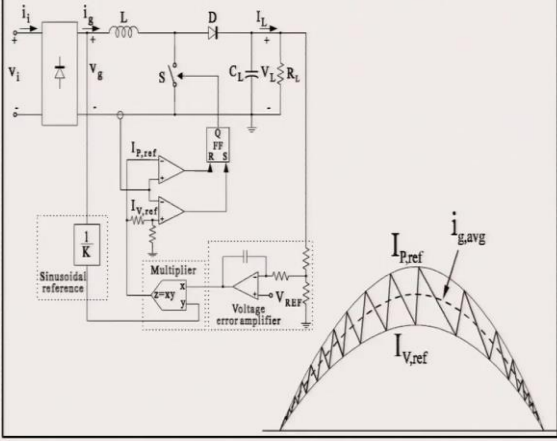


These are the advantages and disadvantages of the average current control.

(Refer Slide Time: 11:18)

### Control of IPQC

✓ **Hysteresis control**



The third control is the hysteresis control. The concept of hysteresis is that the supply current should be maintained within the defined hysteresis band. In this the hysteresis band is realized by two comparators and the current is maintained within that hysteresis band. The control architecture remains identical to the previous control.



(Refer Slide Time: 11:52)

### Control of IPQC


✓ **Hysteresis control**

**Advantages:**

- No need of compensation ramp
- Low distorted input current waveforms

**Disadvantages:**

- Variable switching frequency
- Inductor current must be sensed
- control sensitive to commutation noises


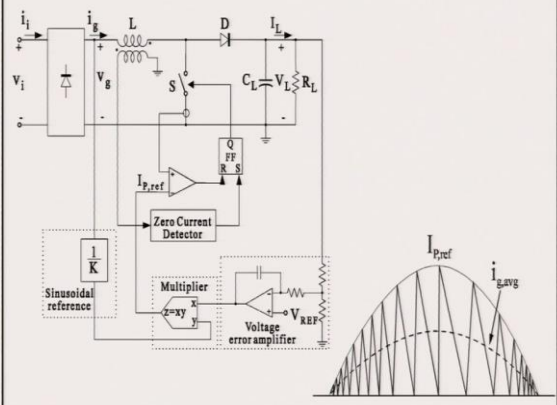


These are the advantages and disadvantages of the hysteresis current control.

(Refer Slide Time: 12:05)

### Control of IPQC

✓ **Borderline control**



Another control is the borderline control, which is also known as the critical conduction mode. In this control, the current is reduced to zero just at the end of a switching cycle. Thus, the converter operates at the boundary of discontinuous current mode conduction and continuous current mode conduction, and therefore, utilizes the benefits of both controls.

(Refer Slide Time: 13:06)

### Control of IPQC


✓ **Borderline control**

**Advantages:**

- No need of a compensation ramp
- No need of a current error amplifier
- For controllers using switch current sensing, switch current limitation can be introduced

**Disadvantages:**

- Variable switching frequency
- Inductor voltage must be sensed in order to detect the zeroing of the inductor current
- For controllers in which the switch current is sensed, control is sensitive to commutation noises


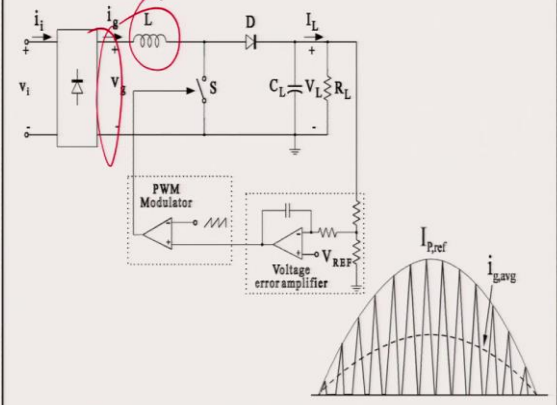


These are the advantages and disadvantages of the discontinuous current control.

(Refer Slide Time: 13:31)

### Control of IPQC

✓ **Discontinuous current control**



Well, the another control is the discontinuous current control. This control is very interesting in a sense that you do not need the sensor on AC side and it realizes inherent power factor at AC mains. Another major benefit of discontinuous current control is the reduced size of magnetic component. However, the peak current in such control is much higher as compared to other control methods.

(Refer Slide Time: 15:13)

## Control of IPQC


✓ **Discontinuous current control**

**Advantages:**

- Constant switching frequency
- No need of current sensing
- Simple PWM control

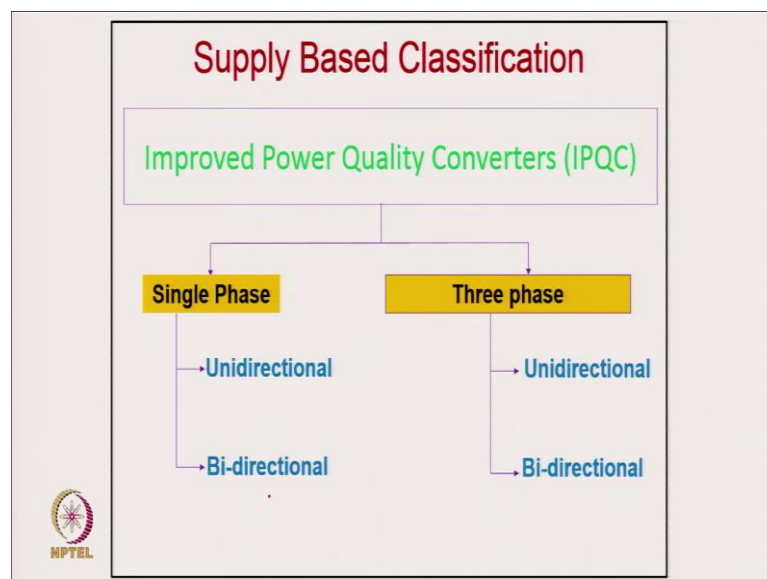
**Disadvantages:**

- Higher devices current stress than for borderline control
- Input current distortion with boost topology



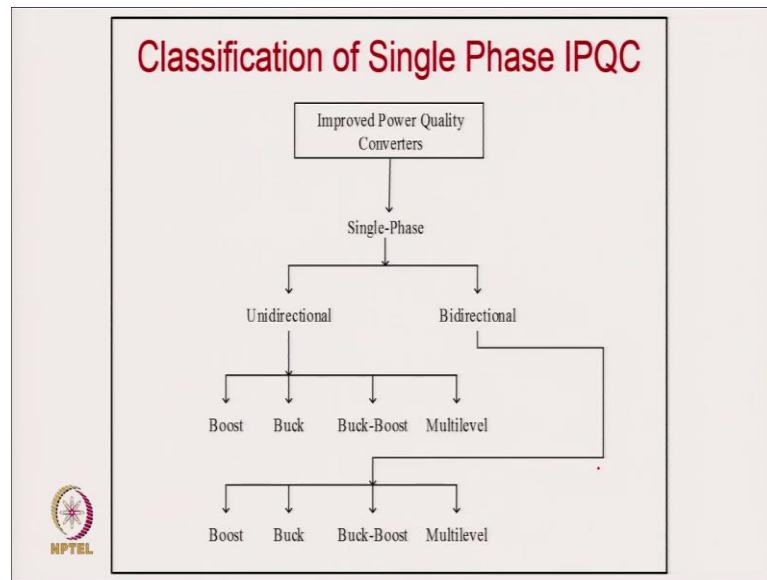
Well the advantage of this discontinuous current is constant switching frequency, no need of current sensing and simple control. Disadvantage high higher device current stress than the borderline control and input current distortion with boost topology or so.

(Refer Slide Time: 15:27)



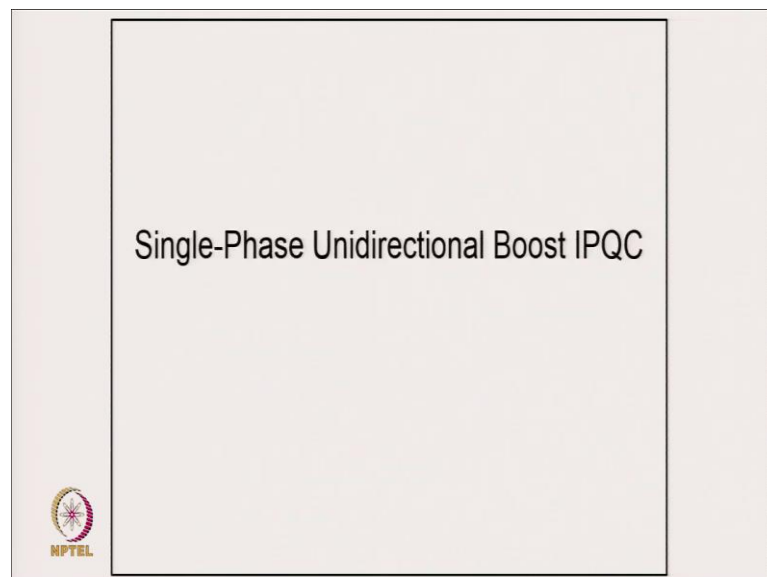
Coming to supply based classification of improved power quality converters, the IPQCs can be classified in a single phase or a three phase. In which, we can have a unidirectional power flow or bidirectional power flow in single phase as well as we can have in three phase also unidirectional power flow and bidirectional power flow.

(Refer Slide Time: 15:47)

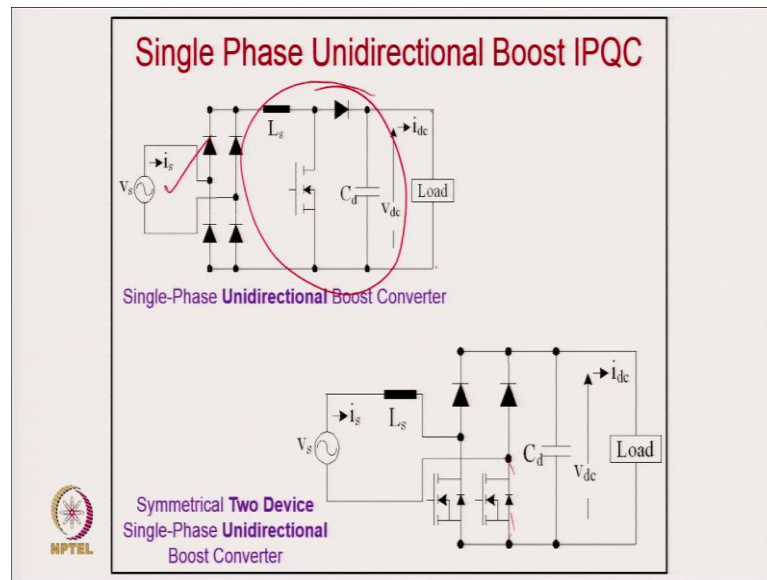


And coming to like a further of classification of single phase we can have unidirectional and bidirectional, and in unidirectional we can further have a boost, buck, buck-boost and multilevel. Further, in the bidirectional category, of course, we can have boost, buck, buck-boost and multi-level converters. So, this is the classification, in which the single phase improved power quality converter can be divided into 8 categories.

(Refer Slide Time: 17:01)

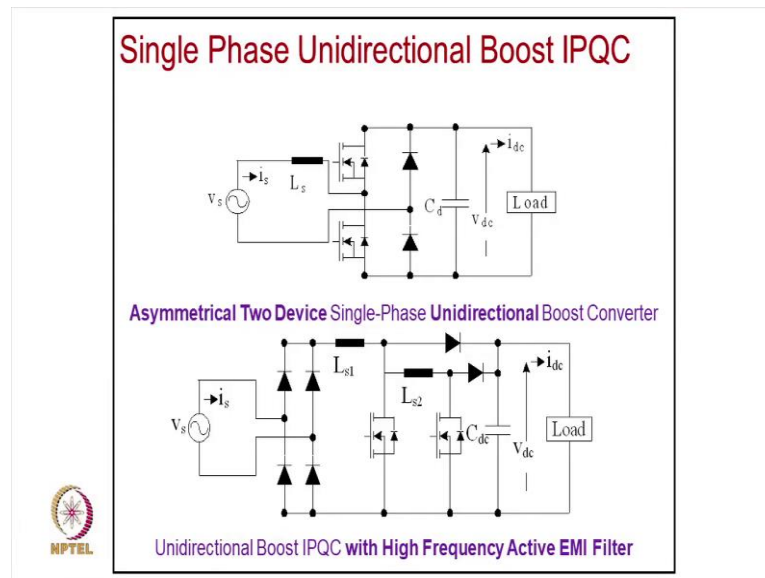


(Refer Slide Time: 17:02)



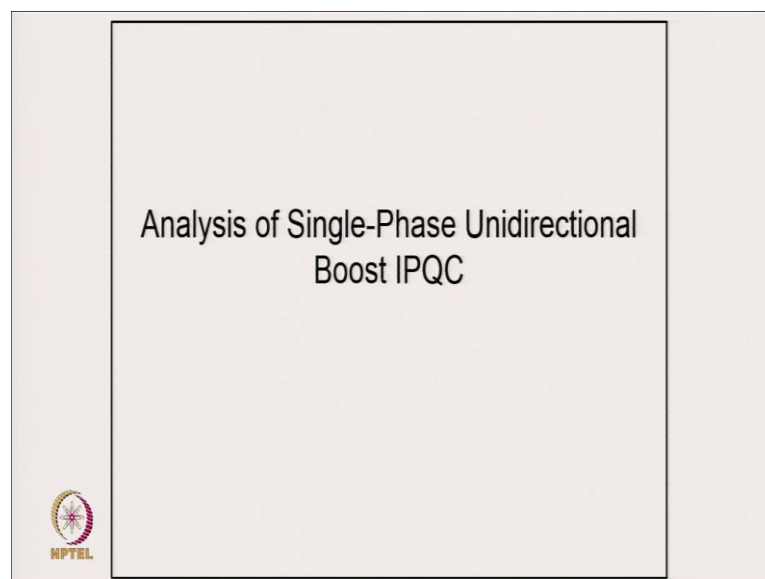
Now coming to the single phase unidirectional boost improved power quality converter. In boost converter with proper design and proper control, we can get regulated DC output voltage and power factor correction at the input supply. That means, supply current remain in phase with the voltage and THD also gets reduced quite low maybe less than 5 percent. As we have already discussed its various control. So, if you are designing it in DCM, the benefit is that, you are able to reduce the size of the input inductor. Further, another circuit is presented, which has two devices and bridgeless unidirectional structure. The benefit of bridgeless topology is that, the inductor is moved to AC side, which makes inductor design quite simple because now it is an AC inductor. The another benefit is lower device count and associated conduction losses, as the current is flowing through only two devices here, as compared to three devices of non-bridgeless circuits. The third major benefit is that, it can give a good quality at AC mains, even up to quite low voltage. So, this we call it a bridgeless boost converter or two devices single phase unidirectional boost converter.

(Refer Slide Time: 19:34)

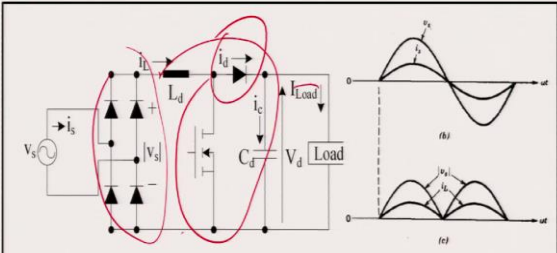


The third topology of course, you can have a asymmetric configuration for typically the same boost converter. The unidirectional boost IPQC with high frequency active EMI filter is an interesting topology, this boost converter that have two cells of single phase unidirectional boost converter. In these two cells, 90 % energy or power is at low frequency. So, we design one cell at typically low frequency and an another with the high frequency to handle 10 % remaining power.

(Refer Slide Time: 20:28)



(Refer Slide Time: 20:53)



Because of a fairly large capacitance, the voltage can be initially assumed to be that is,

$$V_d(t) = V_d$$

Therefore, the output power is

$$p_d(t) = V_d i_d(t)$$

Where  $i_d(t) = I_{load} + i_c(t)$

So, this is the basic analysis and design of boost IPQCs.

(Refer Slide Time: 21:50)

This allows the assumption that  $p_{in}(t) = p_d(t)$  on an instantaneous basis. Therefore,

$$i_d(t) = I_{load} + i_c(t) = \frac{V_s I_s}{V_d} \cos 2\omega t$$

where the average value of  $i_d$  is  $I_d = I_{load} = \frac{V_s I_s}{V_d}$

And the current through the capacitor is

$$i_c(t) = -\frac{V_s I_s}{V_d} \cos 2\omega t = -I_d \cos 2\omega t$$

Even though this analysis is carried out by assuming the voltage across the capacitor to be ripple-free dc, the ripple in  $v_d$  can be estimated from

$$v_{d,ripple}(t) \approx \frac{1}{C_d} \int i_c dt = -\frac{I_d}{2\omega C_d} \sin 2\omega t$$

The output diode current ( $I_d$ ) can be written like a load plus capacitor current. The capacitor supposed to take the all ripple and here the lowest frequency ripple will be the second harmonic ripples. The size of capacitor depends on how much ripple you can permit in the output; you can call it depends on your fundamental frequency because it is supposed to absorb second harmonics. So, with the given output load current, voltage ripples and supply frequency, you can get the value of  $C_d$ .

The capacitor value does not depend on switching frequency it depends on how much ripple you can allow and how much is the load current. If you allow the ripple more, the capacitor value will low, and if load current is more the capacitor will be more because it directly proportional to that.

(Refer Slide Time: 23:51)


**Switching frequency:** The following equations can be written during the on interval  $t_{on}$  and the off interval  $t_{off}$  of the switch

$$t_{on} = \frac{L_d I_{rip}}{|V_s|}$$

Where the switching frequency  $f$  is given as  $t_{on} = \frac{L_d I_{rip}}{|V_s|}$


In a constant-frequency control scheme,

$$f_s = \frac{1}{t_{on} + t_{off}} = \frac{(V_d - |V_s|)|V_s|}{L_d I_{rip} V_d}$$

$$I_{rip,max} = \frac{V_d}{4L_d f_s}$$


The plot of the normalized  $I_{rip}$  as a function of  $|V_s|/V_d$ , noting that in a step-up converter  $|V_s|/V_d$  must be less than or equal to 1. The maximum ripple current is given as,

$$I_{rip} = \frac{(V_d - |V_s|)|V_s|}{L_d f_s V_d} \quad \text{when } |V_s| = \frac{1}{2} V_d$$



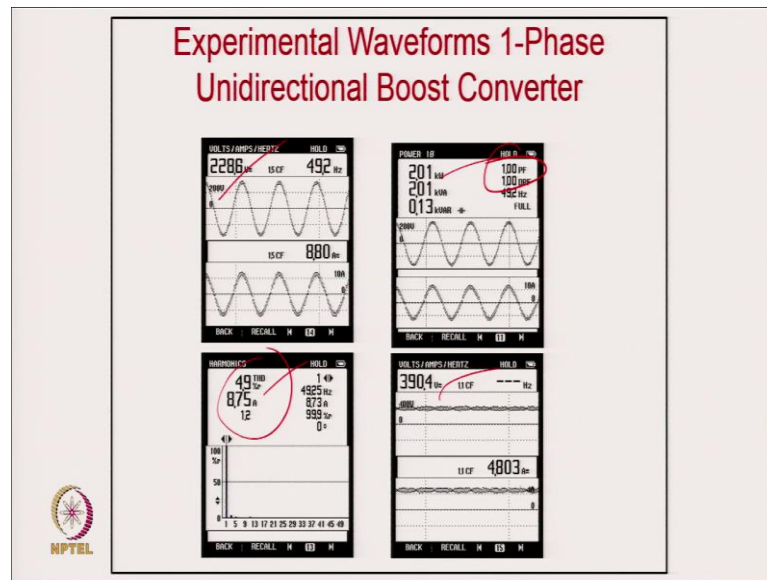
Now, this is the design of boost inductor. The maximum value of inductor ripple current depends on DC link voltage and switching frequency. From the given relation, we can find out, taking the inductor and ripple current, the inductor depend certainly on DC link voltage.

So, if you have a low DC link voltage you will require a higher value inductor and if you have a higher switching frequency inductor will reduce, and if you allow higher ripple current then also inductor will reduce.

With this, we are able to get the design of boost IPQC converter.

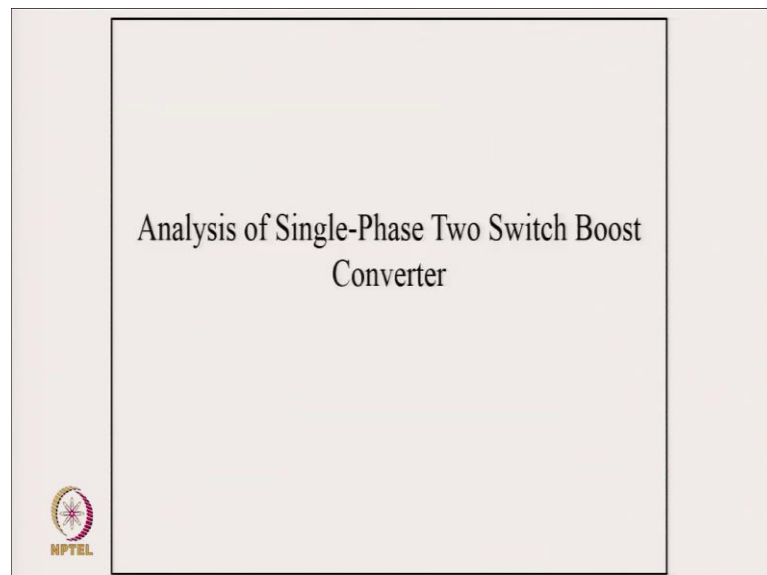


(Refer Slide Time: 26:37)



Of course, we have also develop boost IPQC converter and these are the typical experimental result corresponding to 230 V supply voltage. Where, for the design of 2 kW power and output voltage of 390 V, the THD is only 4.9 % and the power factor is unity.

(Refer Slide Time: 27:07)



(Refer Slide Time: 27:12)

### Single-Phase Two Switch Boost Converter

**Design Specifications:-** Power Level – 1500 W, Supply Voltage 120 V/60 Hz, Output Voltage – 200 V, Allowable Output Voltage Ripples = 5 %, Switching Frequency – 36.5 kHz, Desired Input Inductor Current Ripples – 10 %.

Now, coming to the design example of single phase two switch boost IPQC. The design specifications are given as follows.

(Refer Slide Time: 28:12)

### Design Example

The bulk output filter capacitor may be determined by setting the output ripple constraint. By allowing a 5% output voltage ripple and considering the ripple frequency to be twice the line frequency, we get

$$V_{\text{ripple}} = (0.05)V_{\text{out}}(\text{pu}) = (0.05)(1.67) = 0.083$$

$$V_{\text{ripple}} = 0.083 * 120 = 10V$$

$$C_d = \frac{I_{\text{DC}}}{n\omega\Delta V} = \frac{I_{\text{DC}}}{2(2\pi f)\Delta V}$$

Where  $I_2$  is the twice the line frequency current.  
Equating instantaneous input power to output dc power

$$I_{\text{DC}} = \frac{120 * 12.5}{200} = 7.5A$$

Therefore

$$C_d = \frac{7.5}{2(2 * \pi * 60 * 10)} = 994.72\mu F$$

We chose  $C_{dc} = 1300\mu F$  to assure a stiffer dc voltage

(Refer Slide Time: 28:50)

**Design Example**


The input inductor,  $L_s$ , may be determined knowing the switching frequency is 36.5 kHz. To obtain a 10% input current ripple we find  $L_s$  by

$$I_{36.5\text{kHz}} = (0.1)(I_{60\text{Hz}}) = (0.1)(1\text{pu}) = 0.1\text{pu}$$
$$X_L = \omega L = \frac{V}{I}$$
$$X_{L_n} = n \omega L = \frac{V_n}{I_n} = n X_L$$

Letting  $n = f_s/f = 36.5\text{ kHz}/60\text{ Hz}$  and assuming  $V_n = 1.0\text{ pu}$ .


$$X_L = 0.15781\Omega$$
$$L = \frac{X_L}{\omega} = 419\ \mu\text{H}$$

We chose an available 560 $\mu\text{H}$  inductor.

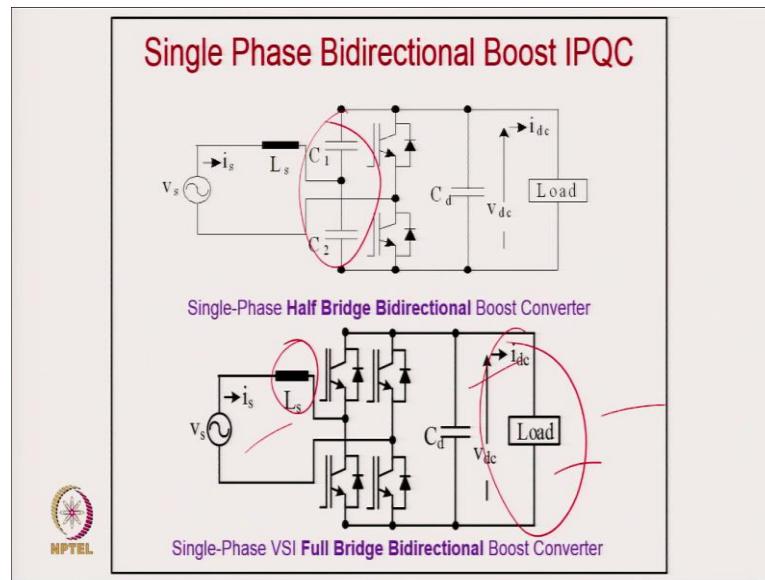


(Refer Slide Time: 29:38)

Single-Phase Bidirectional Boost IPQC

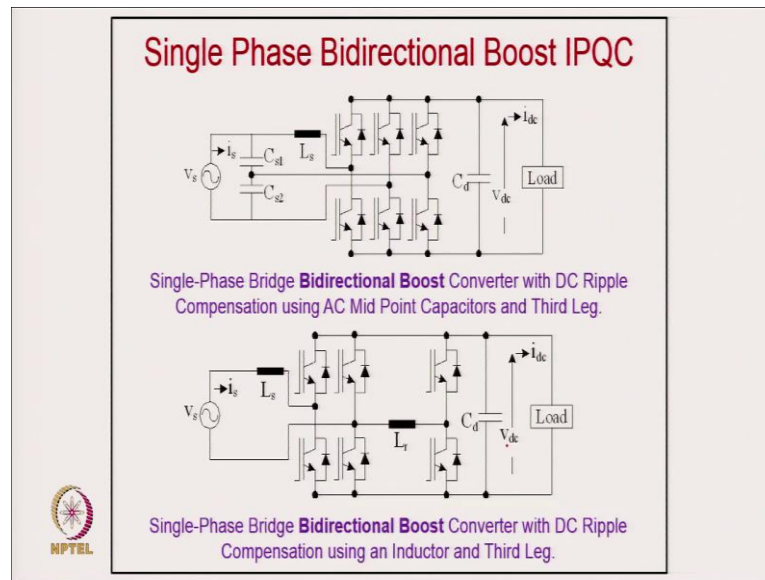


(Refer Slide Time: 29:48)



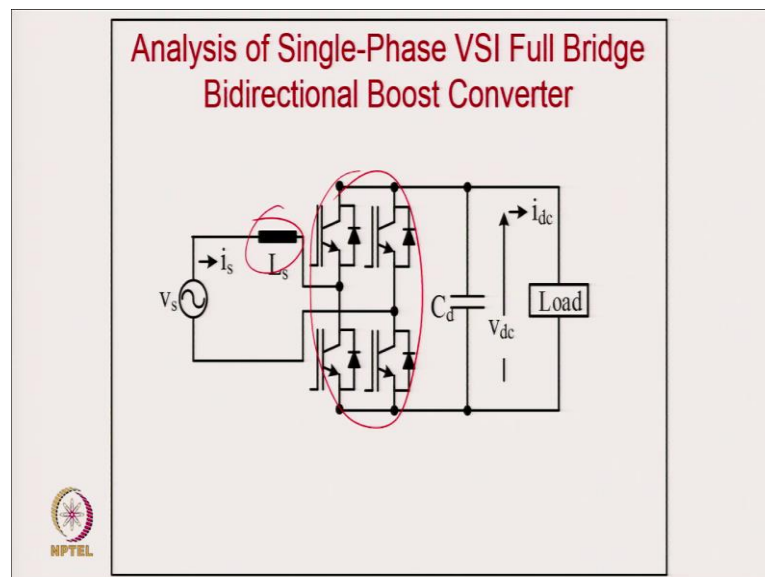
Previously, the unidirectional boost IPQCs of two kinds were given with example as well as the design concept. Now, coming to the single phase bidirectional boost converter. The first circuit of bidirectional boost converter has two devices and two midpoint output capacitors. The disadvantage with this is that, the entire current have to flow through output capacitor, so the size of the capacitor will be large. And the output voltage have to be high as compared to full bridge type configuration. The full bride type boost converter is quite versatile converter, as you can have 4 quadrant operation of P and Q on supply side. So, this converter used very extensively for many applications such as for solar grid interface, i.e. the DC side maybe the solar system with the MPPT and you can push the power to the grid.

(Refer Slide Time: 31:39)



Or you can extend bidirectional full bridge boost converter configuration for reducing the ripples on DC link, by considering one more leg for reducing the ripple, as demonstrated here.

(Refer Slide Time: 31:50)

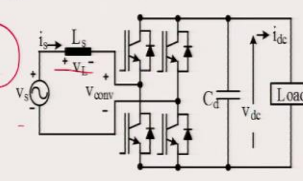


The another benefit of single phase bridge converter is that, this can operate with the unipolar switching and the benefit of unipolar switching is that you are switching the devices at some particular frequency like 10 kilo hertz, but the ripple in the inductor will

be of 20 kilo hertz or you can say you can reduce the switching ripples to approximately half in this phase configuration.

(Refer Slide Time: 32:18)

**Single Phase Bidirectional IPQC**



Where

$$V_s = V_{conv} + V_L$$

$$V_L = L_s \frac{di_s}{dt}$$

Assuming  $V_s$  to be sinusoidal, the fundamental frequency components of  $V_{conv}$  and  $i_s$  can be expressed as phasors  $V_{conv1}$  and  $I_{s1}$  respectively. Choosing  $V_s$  arbitrarily as

$$V_s = V_{conv1} + V_{L1}$$

Where

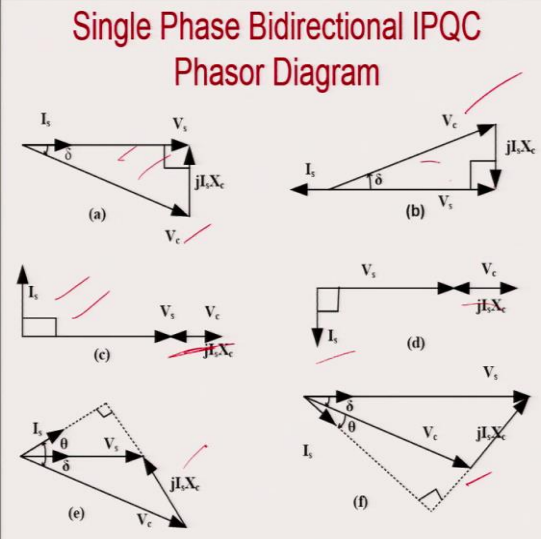
$$V_{L1} = j\omega L_s I_{s1}$$

The reference phasor  $V_s = V_s e^{j0}$ , at the line frequency  $\omega = 2\pi f$

Now, this is the design part of bidirectional full bridge boost converter.

(Refer Slide Time: 33:14)

**Single Phase Bidirectional IPQC Phasor Diagram**



Now these are the phasor corresponding to different operating condition. So, let us say, the boost converter is working as rectifier and power is feeding to the converter, then considering unity power factor operation, the supply voltage and supply current remain in phase. And for this condition (as given in Fig. a), the phasor sum of converter

fundamental voltage and inductor voltage, will become virtually the supply voltage. Fig. b shows the phasor corresponding to when current is out of the phase it means, you are feeding a power back to the grid.

If boost converter work as a condenser, i.e., active power will be zero and only reactive power will be there. Then you can have a current at leading (as shown in Fig. c), and you can say  $V_s$  is there, but you will be adding the inductor voltage also in the same supply phase. Similarly, you can subtract that (as shown in Fig. d), corresponding to your inductive reactive power demand. So, the boost converter can work as a pure inductor it can work as a pure capacitor and in between both as shown in phasor diagrams of Fig. e and Fig. f.

(Refer Slide Time: 35:15)

• The real and reactive power are given as

$$P = V_s I_{s1} \cos \theta = \frac{V_s^2}{\omega L_s} \left( \frac{V_{conv1} \sin \delta}{V_s} \right)$$

$$Q = V_s I_{s1} \sin \theta = \frac{V_s^2}{\omega L_s} \left( 1 - \frac{V_{conv1} \cos \delta}{V_s} \right)$$

• The fundamental current is given as

$$I_{s1} = \frac{V_s - V_{conv1}}{j\omega L_s}$$

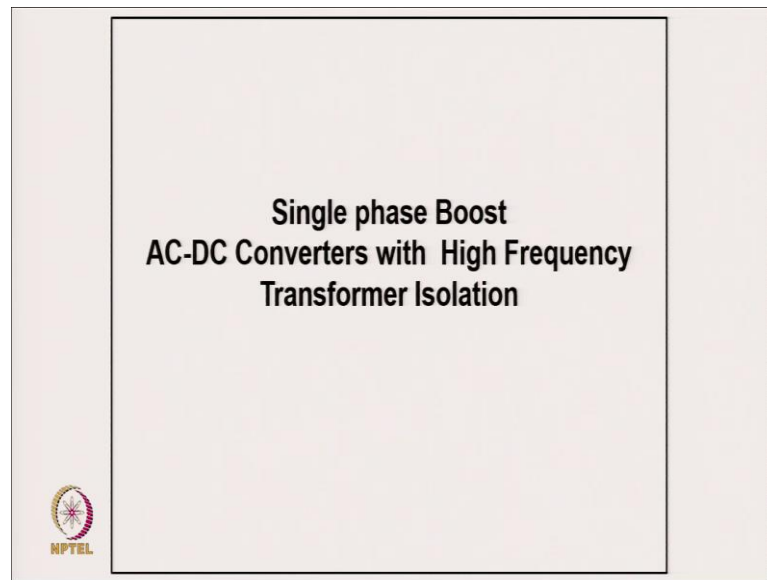
• For rectification and inversion at unity power factor,

$$V_{conv1} = [V_s^2 + (\omega L_s I_{s1})^2]^{1/2}$$

NPTEL

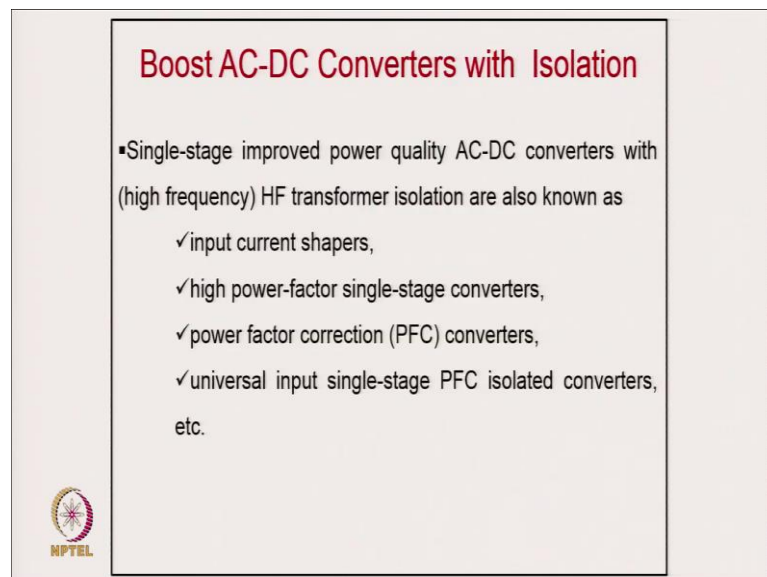
So, with this, we can have expressions of active power, reactive power, fundamental supply current and fundamental converter voltage. As demonstrated, the active and reactive power flow can be controlled either by controlling converter fundamental converter voltage or by controlling the angle delta.

(Refer Slide Time: 38:07)



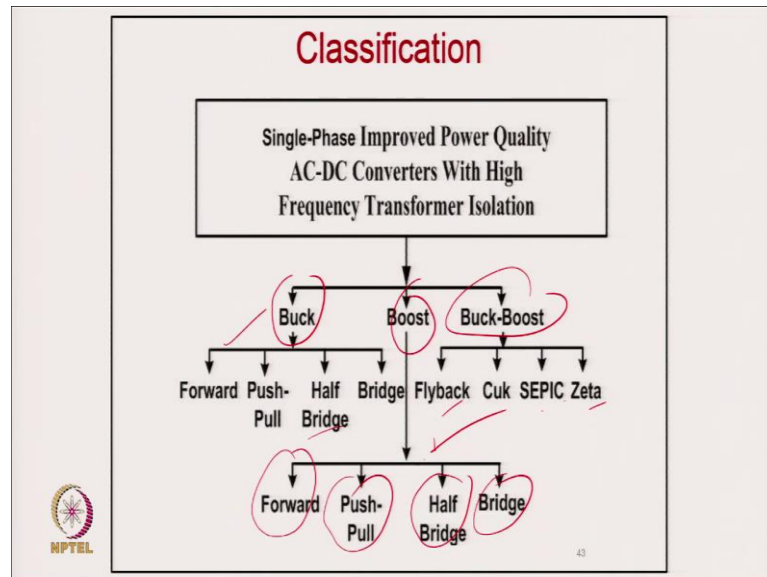
Now coming to like other topology of single phase boost AC-DC converter with the high frequency transformer isolation, they are vary extensively use in many applications, about which we will discuss latter.

(Refer Slide Time: 38:21)



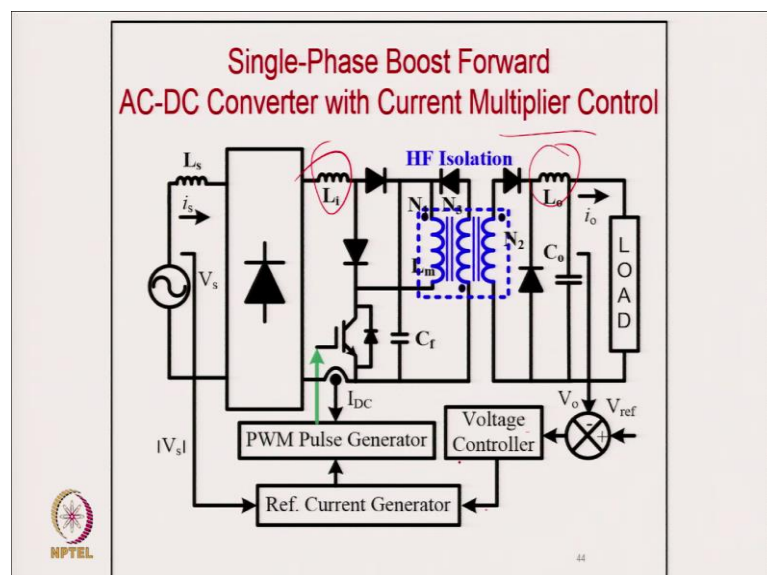
(Refer Slide Time: 38:43)





If you talk about classification with high frequency transformer isolation you can have buck you can have a boost or you can have buck boost. So, we will talk about boost here. Like, you can use, either forward or push pull configuration, half bridge configuration or bridge configuration and if you feed by current fed source, they will work as a boost converter. But if you feed directly with voltage source, then they will work as a buck converter.

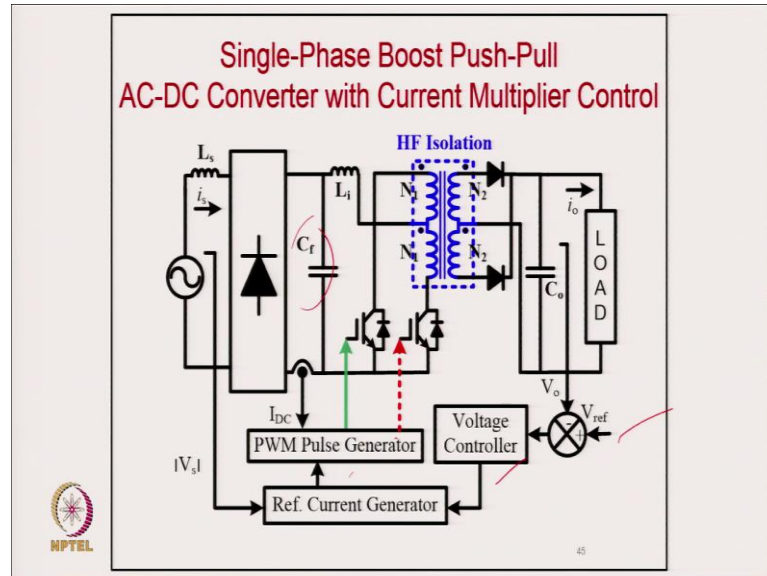
(Refer Slide Time: 39:36)



Now, coming to single phase boost isolated forward converter, so, the circuit diagram with current multiplier control, is given as follows. Current multiplier approach in the

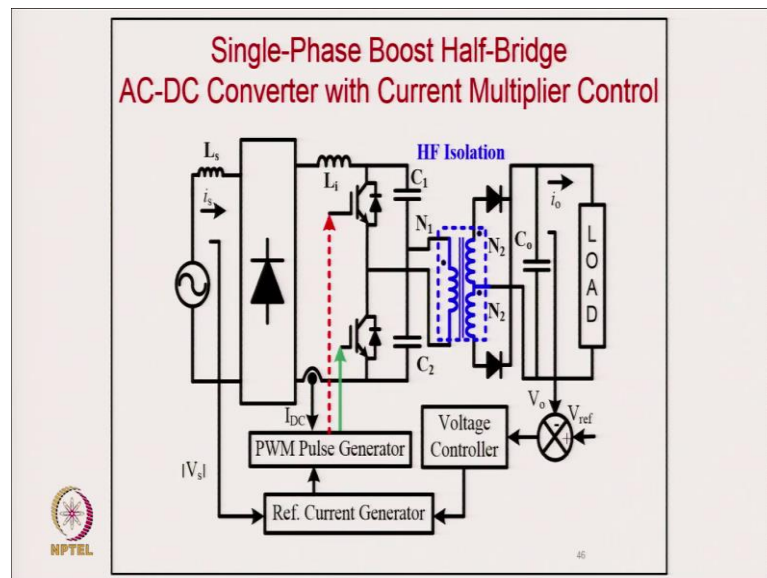
sense, we are sensing the voltage and input current as we have discuss in the PFC control part, you have to sense the voltage end current for current multiplier approach.

(Refer Slide Time: 40:50)



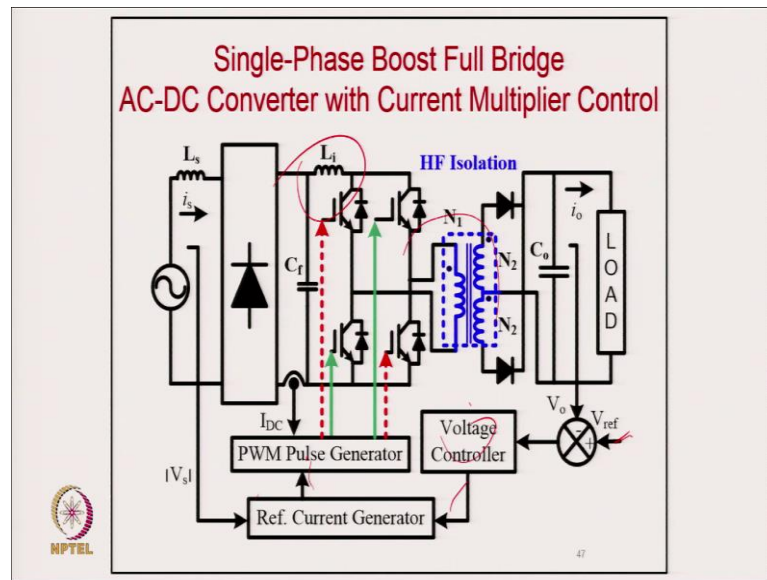
And, this is a boost push pull converter with current multiplier approach.

(Refer Slide Time: 41:41)



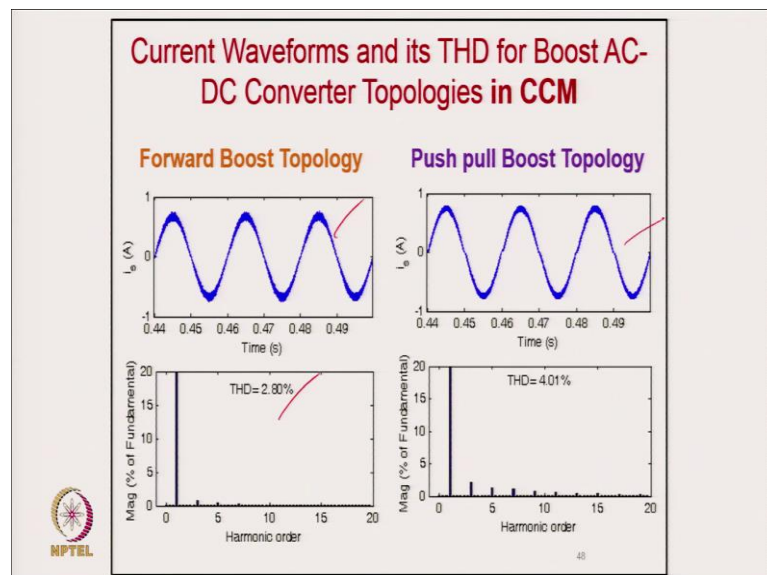
Similarly, for the half bridge, I mean we can switch on both the devices which stored the energy in the inductor and when you switch off one the energy get transfers to the secondary side.

(Refer Slide Time: 42:03)



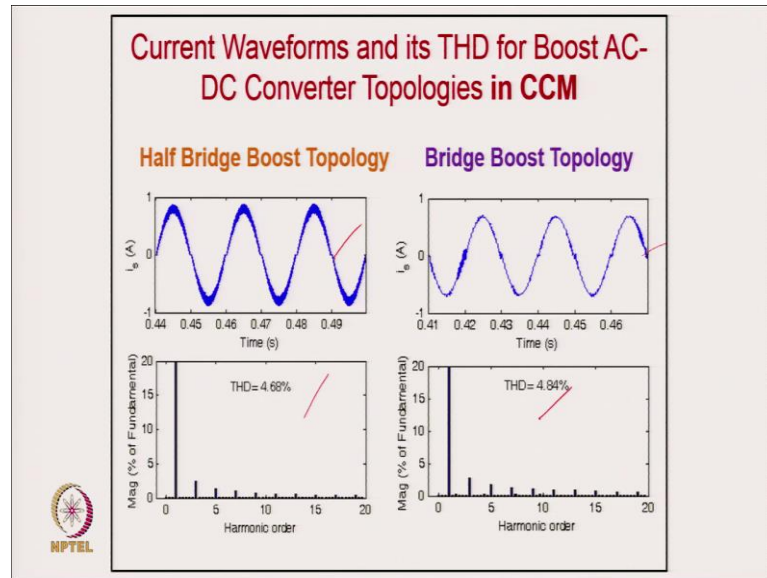
So, that was a half bridge. Now, we come to full bridge type isolated boost converter, this is normally used for high power rating. So, here we can switch on either one leg or both legs simultaneously, when we store the energy; and when we want to transfer, so, we can switch on diagonally. So, the transformer action is there and we can operate in the same manner.

(Refer Slide Time: 42:48)

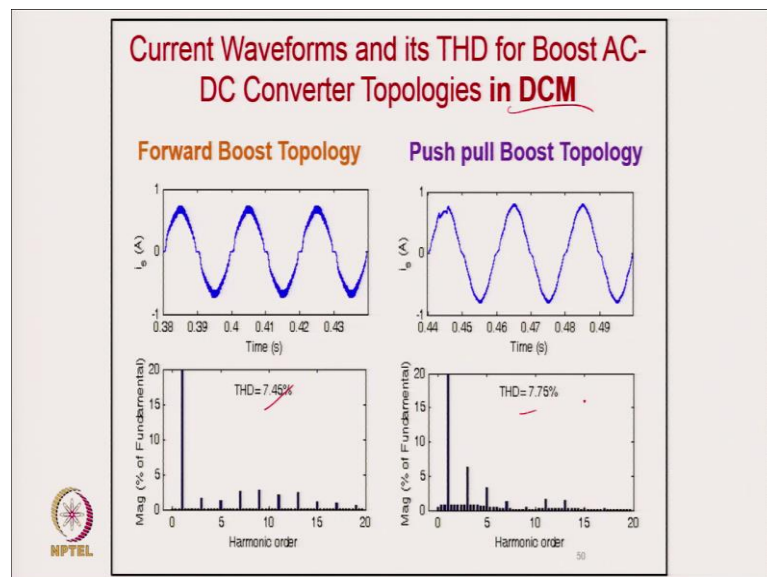


Well, coming to design and simulation. In the forward topology, under CCM operation, you can get the current very closed to sine wave with current THD of 2.8 % and for push pull this is the typical current THD is 4 %.

(Refer Slide Time: 43:04)

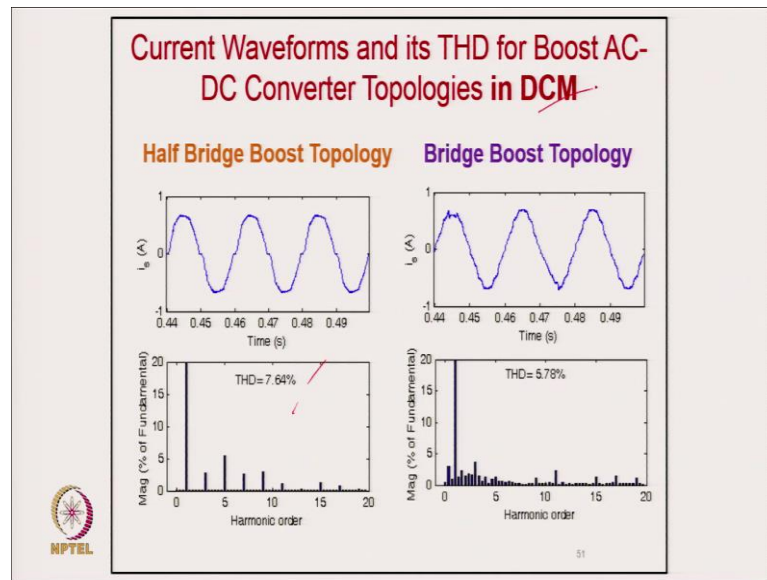


(Refer Slide Time: 43:18)



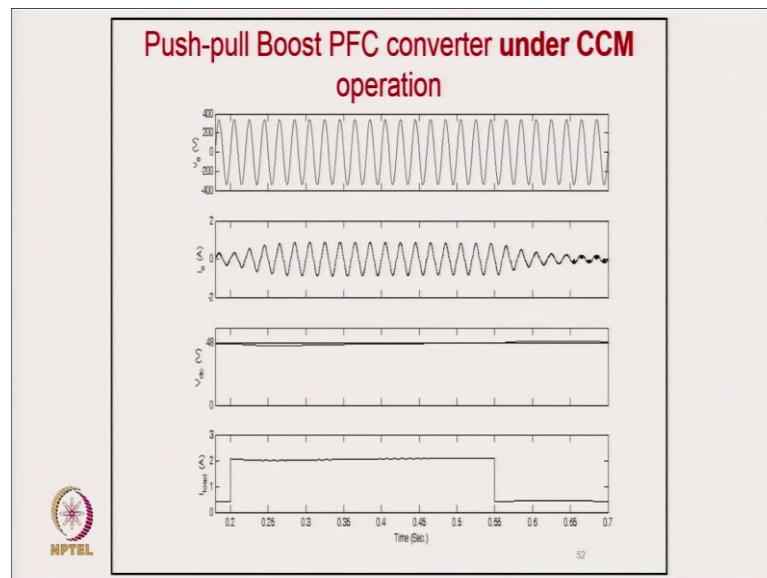
And then for the half bridge the current THD is found nearly 4.68 % and for full bridge, it is 4.84 %. So, all isolated boost IPQCS are able to get good power factor correction and low THD supply current during CCM operation. If you are operating converters in discontinuous mode, the THD slightly increases.

(Refer Slide Time: 43:33)



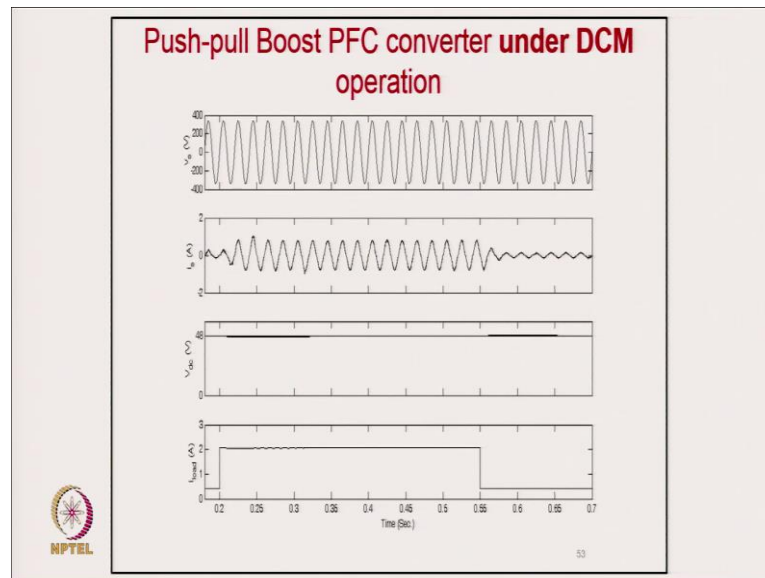
This is the performance analysis for push pull and for half bridge based isolated boost IPQCs

(Refer Slide Time: 43:46)



And this is the dynamic performance of push pull based isolated boost IPQCs in CCM mode. As you can see, on increasing and decreasing of load, the supply current remain in phase with the supply voltage, and DC link voltage remain regulated. The input current is increasing because output power is increased because of increased load on the DC side.

(Refer Slide Time: 44:06)



And this is the case corresponding to DCM operation of push pull based isolated boost IPQCs.

(Refer Slide Time: 44:31)

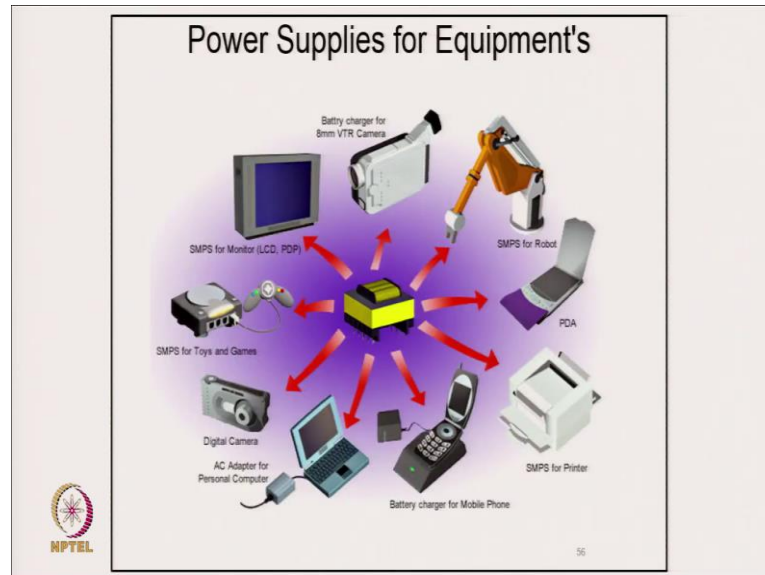
- 
- The slide is titled "Applications" and lists several uses for the converter. The NPTEL logo is in the bottom left corner, and the number 55 is in the bottom right corner.
- DC Power Supplies
  - Telecommunication Power Supply
  - Improved Power Factor ballast
  - Power Supplies for equipments like computers, medical equipments, printers, scanners etc
  - Drives Applications with Power Factor Improvement at AC side
  - Electrical Welding
  - Lighting such as ballasts, CFL etc.

Now, coming to the applications where these converters are used like in a DC power supply, telecommunication power supply, improved power quality power factor ballast like lighting system, power supply for equipment like computer medical equipment printers, scanners and drive applications for power factor improvement at AC side electric welding, high frequency welding virtually and lighting such as CFL, LED



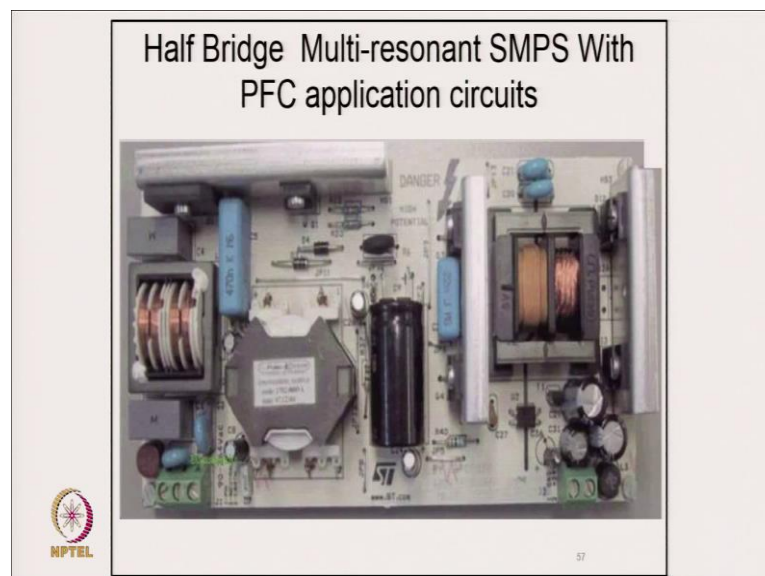
lighting. In all these applications, we can use these PFC power factor correction converters. Of course, in some cases we use isolated and in some cases we need non isolated like computer supply we use the isolated one.

(Refer Slide Time: 45:05)



So, these are the power supply equipment, you can think about, where we are using the boost converters.

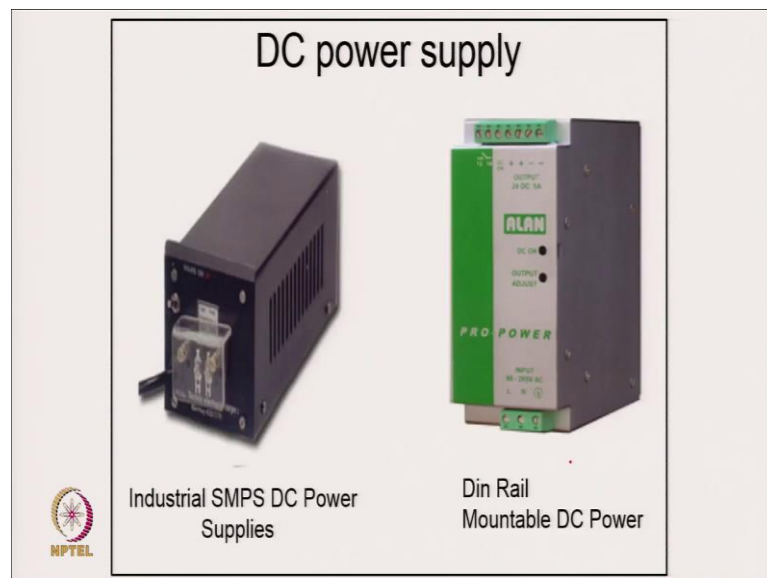
(Refer Slide Time: 45:49)



(Refer Slide Time: 45:58)



(Refer Slide Time: 46:06)





(Refer Slide Time: 46:14)



(Refer Slide Time: 46:19)



(Refer Slide Time: 46:30)

### Electrical Welding



Hybrid laser-arc-welding



Electrical Welding Machines

 62

(Refer Slide Time: 46:41)

### Lighting such as ballasts, CFL



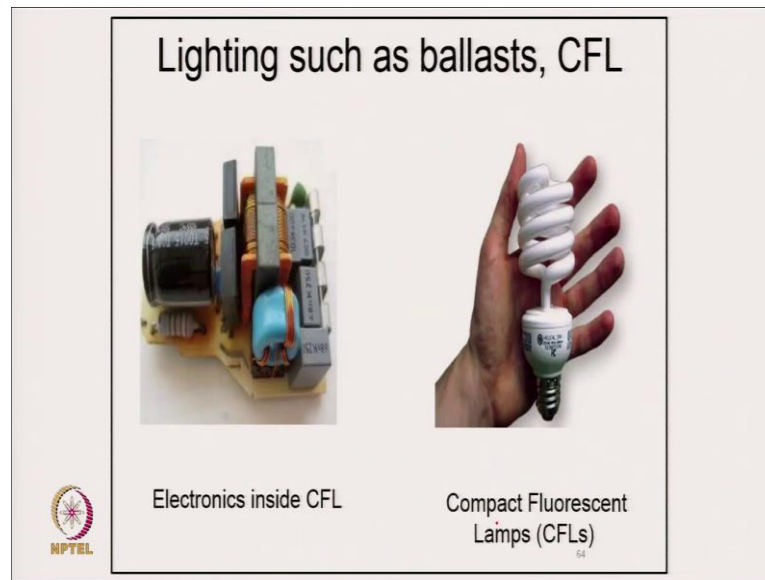
High Power  
Electronic Ballasts For CFL



The tube in the  
CFL uses SMPS

 63

(Refer Slide Time: 46:47)

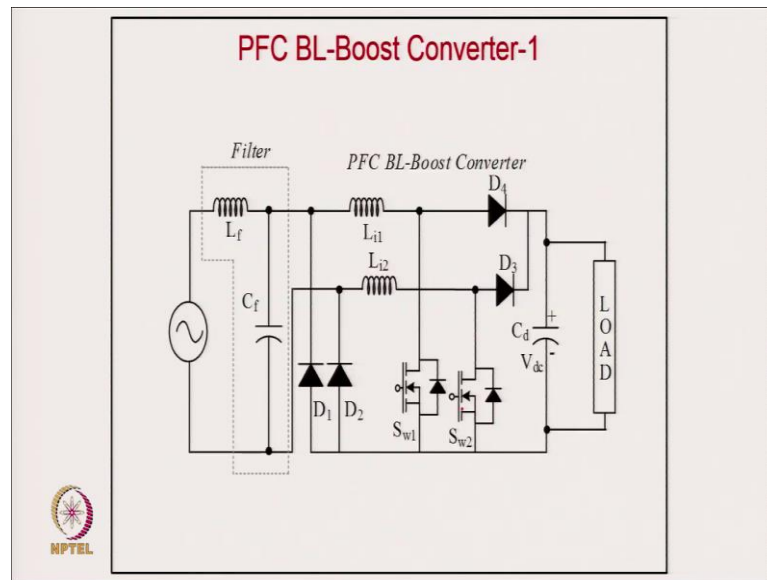


(Refer Slide Time: 46:51)

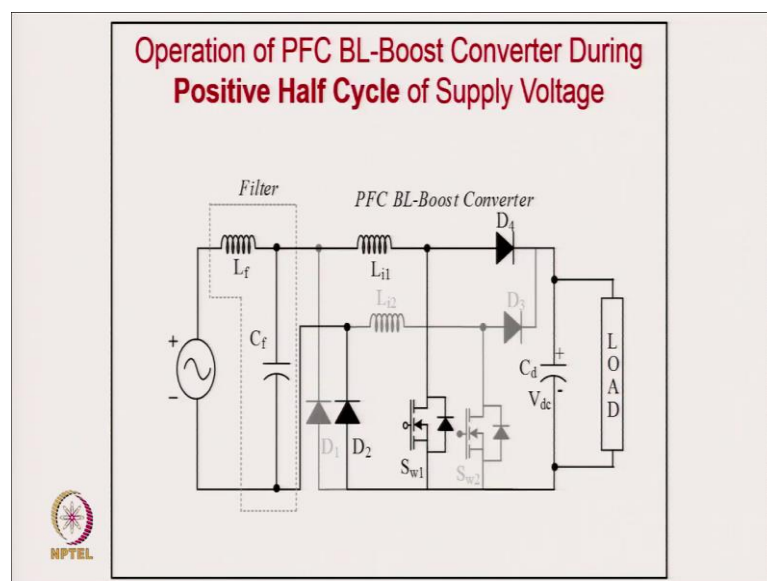


So, now coming to like analysis design and simulation of performance of PFC power factor correction bridgeless boost converter.

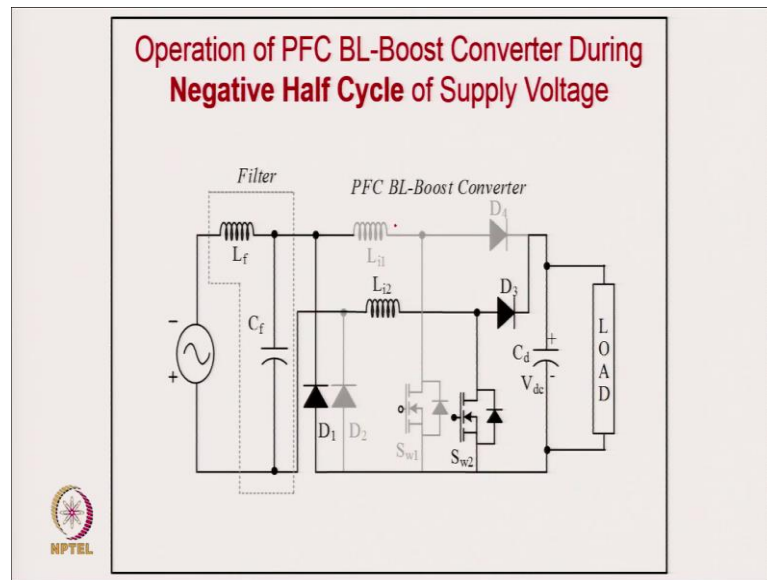
(Refer Slide Time: 46:59)



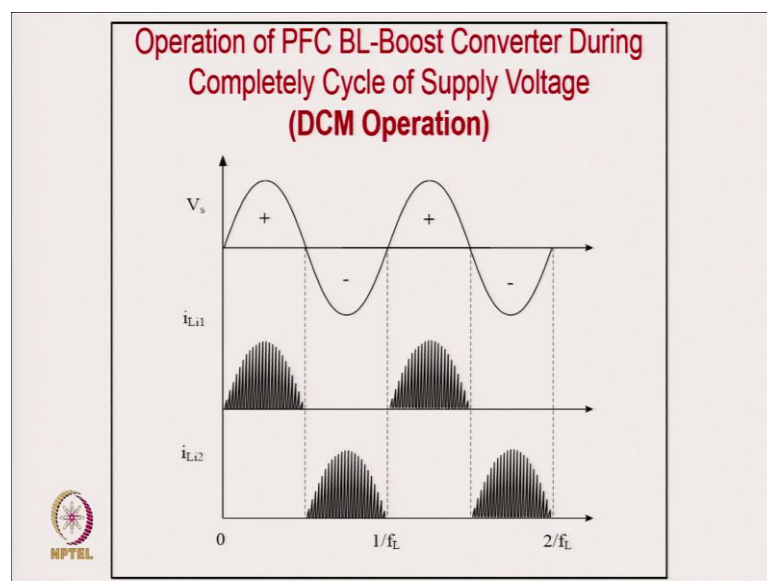
(Refer Slide Time: 47:18)



(Refer Slide Time: 47:28)



(Refer Slide Time: 47:31)



And this is typically the bridgeless boost converter, you can clearly see, here we have eliminated the bridge. So, one positive half cycle one boost converter work in another negative half cycle another converter will work.

(Refer Slide Time: 47:37)

### Design of a PFC BL-Boost Converter

The average input voltage appearing to input of converter

$$V_{in} = \frac{2\sqrt{2}V_s}{\pi}$$

The relation governing the voltage conversion ratio for a boost converter is given as,

$$V_o = V_{in} / (1 - D)$$


Inductor Operating in CCM

$$L_i = \frac{V_{in} \cdot D}{f_s \Delta I_{Li}}$$

Inductor Operating in DCM

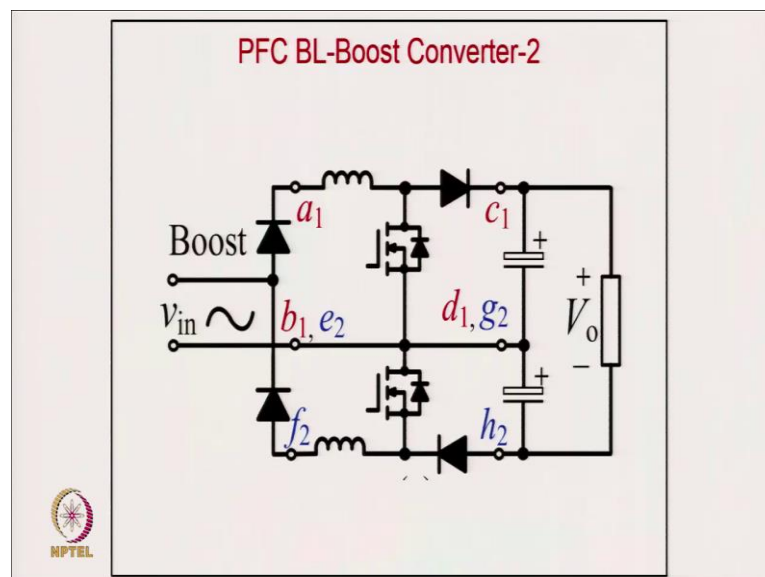
$$L_i \ll L_{ic} = \frac{V_{in} \cdot D}{f_s (2I_{in})}$$

DC Link Capacitor Design

$$C_d = \frac{I_d}{2\omega \Delta V_{dc}}$$


And you can design this converter for both CCM and DCM operation by utilizing following equations.

(Refer Slide Time: 47:53)



And this is the another version of PFC power factor correction bridgeless boost converter. Of course, this converter uses two devices, but the you can understand in some application we require two equal voltage at output with power factor correction like in a switch reluctance motor (SRM) drive of four phase you require midpoint converter. So, this converter is suitable for such applications.