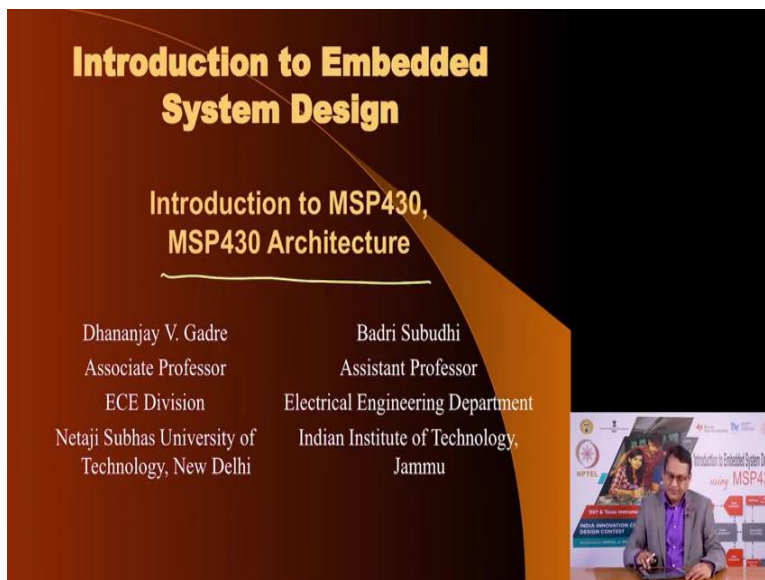


Introduction to Embedded System Design
Professor Dhananjay V. Gadre (NSUT) & Badri Subudhi
Indian Institute of Technology, Jammu
Lecture 11
Introduction to MSP430

Hello, Welcome to a new lecture on this online course on Introduction to Embedded System Design. I am your instructor, Dhananjay Gadre. Today, we are going to start our discussions on the most important building block of out of our six box model, which is the embedded computer block. And today we are starting real discussions on MSP430 microcontroller. MSP430 is a entry level microcontroller from Texas Instruments. It is one of their oldest offerings as far as microcontrollers are concerned.


(Refer Slide Time: 1:00)



The slide features a dark background with a curved orange and black graphic on the right side. The main title is 'Introduction to Embedded System Design' in yellow and white. Below it, the subtitle is 'Introduction to MSP430, MSP430 Architecture' in white. The slide lists two instructors: Dhananjay V. Gadre, Associate Professor at ECE Division, Netaji Subhas University of Technology, New Delhi; and Badri Subudhi, Assistant Professor at Electrical Engineering Department, Indian Institute of Technology, Jammu. A small inset image in the bottom right corner shows a man sitting at a desk with a computer, with a banner for 'Introduction to Embedded System Design using MSP430' and 'JETA INNOVATION CHALLENGE CONTEST' visible behind him.

So, here is the we are going to talk about the introduction to MSP430 and subsequently we will cover the architecture.

(Refer Slide Time: 1:09)



Introduction to MSP430

- MSP430 is a microcontroller family from Texas Instruments.
- It is one of the simplest microcontroller families from TI.
- 'MSP' stands for Mixed Signal Processor.
- It is a 16-bit processor (AB = 16 bit or 20 bit and DB = 16 bit) designed for low power applications.
- It can be used for
 - ✓ General purpose sensing and measurement
 - ✓ Capacitive touch sensing
 - Ultrasonic sensing

The slide includes a small inset video in the bottom right corner showing a presenter in a grey suit and purple shirt sitting at a desk with a laptop. Behind him is a presentation board with the title 'Introduction to Embedded System Design using MSP430' and logos for 'MSP430' and 'MSP430'.

MSP, as I mentioned MSP430, is a microcontroller from Texas Instruments, it is one of the simple microcontroller families from TI. MSP stands for mixed signal processor. This is to indicate that the microcontroller has a CPU and another digital building blocks. And apart from that, it has support for analogue circuits and that is the reason why it is called mixed signal. It is a 16 bit processor, so if we go back to our discussion on various classification methodologies that we have that we have discussed.

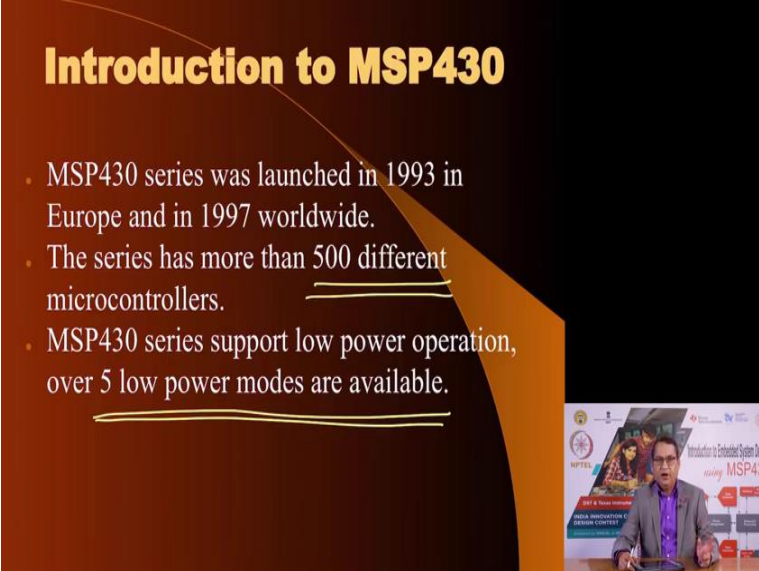
From the point of view of bit handling capacity, it is a 16 bit processor, as you see here the 16 bit data path which connects to the memory which is integrated on the chip. The amount of memory and peripherals and various types of memory, in fact, can be accessed using a address bus, which is for most cases 16 bit address bus which means it has 2¹⁶ rest to power 16 address locations, memory locations. And in some cases, when you want to have more memory than is possible with 2¹⁶ rest to power 16 combinations, it also uses a 20 bit address bus. The data bus is 16 bits. This processor has been designed specifically for low power applications.

The intended application areas of general-purpose sensing and measurement, so you may create instruments where you sense various physical parameters, measure them, process them and provide an appropriate output. It has a concept called capacitive touch sensing you may correlate

with that very easily, because these days smartphones come with the touch and the kind of touch that is used on smartphones these days is a capacitive touch.

And so almost all the pins are a lot of pins of MSP430 microcontroller have a capability to be sensed using the capacity of touch sensing. It can also be used for ultrasonic sensing, ultrasonics are frequencies about 20 kilohertz. So, it has special support for creating waveforms so that you can operate in the ultrasonic range.

(Refer Slide Time: 3:41)



Introduction to MSP430

- MSP430 series was launched in 1993 in Europe and in 1997 worldwide.
- The series has more than 500 different microcontrollers.
- MSP430 series support low power operation, over 5 low power modes are available.

The slide features a dark background with a light-colored curved shape on the right side. The text is white and yellow. A small inset video in the bottom right corner shows a man in a suit presenting at a desk with various logos and text in the background.

This series MSP430 was initially launched in 1993 and in 1997, it was released to the entire world. Like we mentioned, typically a microcontroller family would have a lot of offerings so that an end application could use an appropriate device, which is optimal for that application. And so MSP430 has more than 500 different microcontrollers.

These include from the point of view of capabilities, what kind of hardware support a microcontroller has, the amount of memory it has, and also the physical footprint, small chip and big chips and so on. MSP430 series supports a low power operation. And to support that low power operation, it has 5 low power modes.

You would recall an earlier discussion where we talked of operating modes of a microcontroller and we are broadly used three terms active mode, sleep and power down. The sleep mode of

operation here refers to these five modes of low power operation and will see the details of these operations.

(Refer Slide Time: 4:57)

The image shows a slide titled "MSP430 FAMILY" with a table of microcontroller specifications. In the background, a presenter is visible at a desk with a laptop. The table is as follows:

| MSP430 SERIES | MSP430 FAMILY | FREQUENCY | MEMORY | GPIO |
|--------------------------|---------------|-----------|--|-------|
| CAPACITIVE SENSING MCUs | FR25x/FR26x | 16MHz | FRAM: UP TO 16KB ✓ SRAM: UP TO 4KB ✓ | 15-19 |
| VALUE LINE SENSING MCUs | FR2XX/FR4X | 16MHz | FRAM: UP TO 16KB ✓ SRAM: UP TO 4 KB ✓ | 16-64 |
| | G2X/I2X | 16MHz | FLASH: UP TO 56KB ✓ SRAM: UP TO 4KB ✓ | 4-32 |
| PERFORMANCE SENSING MCUs | FR5X/FR6X | 16MHz | FRAM: UP TO 256KB ✓ SRAM: UP TO 8KB ✓ | 17-83 |
| | F5X/F6X | 25MHz | FLASH: 512KB ✓ SRAM: UP TO 67 KB ✓ | 29-90 |
| OTHER MSP430 MCUs | F2X/F4X | 16MHz | FLASH: UP TO 120KB ✓ SRAM: UP TO 8KB ✓ | 14-80 |
| | F1X | 16MHz | FLASH: UP TO 120KB ✓ SRAM: UP TO 10KB ✓ | 10-48 |

Here is a snapshot which captures almost all the microcontrollers available in the MSP430 family, you have the capacitive sensing MCU here this one.

And it has the nomenclature uses these numbers, the maximum frequency operation of members of this family is 16 megahertz and it has FRAM for the program memory and you can have up to 16 kilobytes of programmed memory. And SRAM for storing data, you can have up to four kilobytes. And the number of input output general-purpose input output pins that are available in this segment, you can have from 15 to 19 GPIO pins.

Then, we have what they call as value line microcontroller units, which means one can expect them to be very inexpensive. They have two series for that, one is a FR2xx and the other one is G2 series. The first series again can be operated up to 16 megahertz. It has FRAM up to 16 kilobytes and SRAM up to 4 kilobytes. And the number of GPIO pins as you see, is much larger from 16 to 64 pins you can have so you can expect that the packaging for this line of microcontrollers would be large and the G series and incidentally, we are going to use in our experimentation we are going to use G series of microcontrollers.

They can also operate up to 16 megahertz. They do not have FRAMs, instead, they have flash memory and you can have up to 56 kilobytes of flash memory and up to 4 kilobytes of SRAM. And the number of input output pins available in this series you can have from 4 pins to 32 pins. Then, we have what they call as performance sensing MCUs. And you have FR5 and F5x series. The first one has a maximum frequency up to, again, 16 megahertz. It offers flash memory. It offers FRAM for program memory up to 256 kilobytes.

And you see the moment this number is above 64k that means it would be using address bus, which is the 20 bit address bus. It has SRAM up to 8 kilobytes and the number of IO pins is large from 17 to 83 pins you can have. The second series the 5F6X operates up to 25 megahertz and it has flash memory, not FRAM for the program memory. And you can have up to 512 kilobytes it has SRAM up to 67 kilobytes. So, it is a huge amount of ram that is available and the number of input output pins is again large 29 to 90 pins you can have.

And then you have what is called as other MSP430 MCUs. They have two series F2 F4X and F1X and the first one, both of them actually have a 16 megahertz clock, both of them offer flash memory. And a number of IO pins is for the first one 14 to 80 and the other one is 10 to 48. And the SRAM in the first case is 8 kilobytes and here you have slightly more at 10 kilobytes. So, this single table in a way captures the kind of variety that is available in the MSP430 family of microcontrollers.

(Refer Slide Time: 8:54)

MSP430F2618ATZQWT-B

Processor Family
 CC = Embedded RF Radio
 MSP = Mixed Signal Processor
 XMS = Experimental Silicon

430 MCU Platform

Device Type

| | |
|--|--|
| <p>Memory Type C = ROM F = Flash FR = FRAM Q = Flash (Value Line) L = No non-volatile memory</p> | <p>Specialized Application FG = Flash Medical OG = ROM Medical FE = Flash Energy Meter FUI = Flash Electronic Flow Meters AFE = Analog Front End BT = Pre-programmed with Bluetooth BO = Contactless Power</p> |
|--|--|

Generation


| | |
|--|---|
| 1 series = up to 8 MHz 2 series = up to 16 MHz 3 series = Legacy OTP 4 series = up to 16 MHz w/ LCD | 5 series = up to 25 MHz 6 series = up to 25 MHz w/ LCD 0 = Low Voltage Series |
|--|---|

Family

Series and Device Number

Optional: A = revision

Optional: Temperature range
 S = 0°C to 50°C
 I = -40°C to 85°C
 T = -40°C to 105°C



Generation

| | |
|--|---|
| 1 series = up to 8 MHz 2 series = up to 16 MHz 3 series = Legacy OTP 4 series = up to 16 MHz w/ LCD | 5 series = up to 25 MHz 6 series = up to 25 MHz w/ LCD 0 = Low Voltage Series |
|--|---|

Family

Series and Device Number


Optional: A = revision

Optional: Temperature range
 S = 0°C to 50°C
 I = -40°C to 85°C
 T = -40°C to 105°C

Packaging
www.ti.com/backpackings

Optional: Distribution format
 T = Small Reel (7-in)
 R = Large Reel (11-in)
 No markings = Tube or Tray

Optional: Additional features
 *Q1 = Automotive qualified
 *EP = Enhanced product (-40°C to 125°C)
 *HT = Extreme temp parts (55°C to 150°C)



MSP430F2618ATZQWT-EP

Processor Family
 CC - Embedded RF Radio
 MSP - Mixed Signal Processor
 MSP - Experimental Micro

Device Type

| Memory Type | Specialized Application |
|----------------------------|------------------------------------|
| C = FRAM | F1 = Flash + Realtime |
| F = Flash | F2 = FRAM + Realtime |
| FR = FRAM | FE = Flash + Energy Harvest |
| L = Flash + Low Power | FL = Flash + Low Power + Realtime |
| L = No non-volatile memory | AFE = Analog Front End |
| | BT = Pre-programmed with Bluetooth |
| | SP = Standalone Power |

Connection
 1 series = up to 5 MHz
 2 series = up to 10 MHz
 3 series = Legacy OTP
 4 series = up to 16 MHz w/ LDD
 5 series = up to 20 MHz
 6 series = up to 20 MHz w/ LDD
 8 = Low-Voltage Device

Frequency

Package and Device Number

Optional A.C. revision

Optional Temperature range
 Q = 0°C to 50°C
 H = -40°C to 85°C
 T = -40°C to 105°C

Package
www.ti.com/links/links

Optional Distribution format
 T = Small Reel (7-12)
 H = Large Reel (13-18)
 N = Nonmarking
 T = Tube or Tray

Optional Additional features
 *Q1 = Automotive qualified
 *MSP = Embedded product (MSP430 to 120°C)
 *HT = Extreme temperature (MSP430 to 150°C)

www.ti.com

Let us see some of the more features of this family. Before that, here is a nomenclature. Let me see if I can zoom it. So, you would see these microcontrollers would be available starting with this number MSP and then 430, because there is a 430 platform the next number or numeral used is which indicates the kind of memory that is available. So, if it is F that means it is flash, if it is G, then also it is flash. If it is F.R that means the microcontroller would have a FRAM memory and so on. And then the other two next digit tells you what kind of frequency operation it offers and so on.

At the bottom it also has a variety in terms of the temperature range for which these microcontrollers are guaranteed to operate. And we have three temperature ranges. The standard one is 0 to 50 degrees. Then you have industrial range, which is minus 40 to 85 degrees, and then the extended range, which is minus 40 degrees to 100 plus 105 degrees centigrade. And of course, the packaging, the overall packaging is in the form of small or large reels or you could get these ICs in the cube form also.

And this is important information when you are doing mask manufacturing certain manufacturing equipment requires the components to be housed in a reel certain other manufacturing techniques, require them to be in a tube or even tray and so on. So, this sort of gives you an idea of how to interpret the complete part number for this series of microcontrollers. When you go to the Texas Instruments website, which is incidentally

www.ti.com, you could search for MSP430 microcontrollers and you would get to know more details about this.

(Refer Slide Time: 10:58)

MSP430G2x53 Features

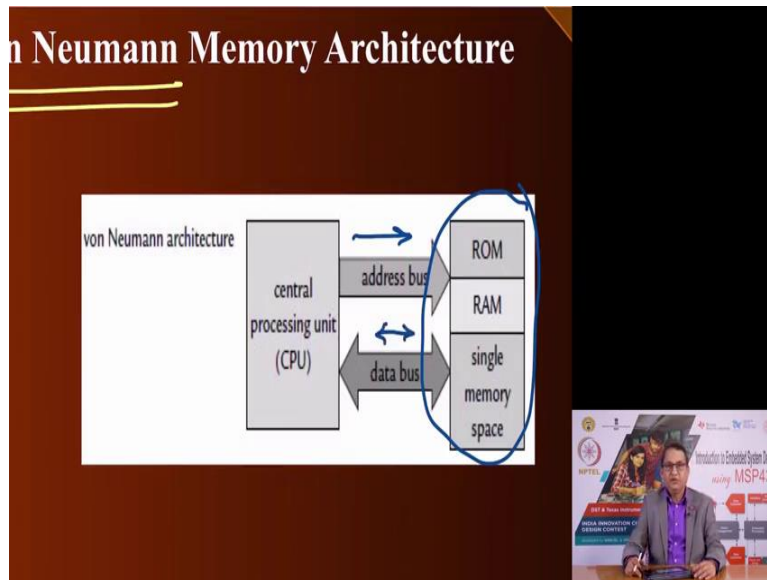
- 16-BIT RISC Architecture
- Von Neumann Memory Architecture

The diagram illustrates the Von Neumann architecture. It shows a central processing unit (CPU) on the left, connected to ROM, RAM, and a single memory space on the right. The CPU is connected to the ROM and RAM via an address bus, and to the single memory space via a data bus. The address bus is shown as a single line with arrows pointing from the CPU to the memory components. The data bus is shown as a double line with arrows pointing from the memory components to the CPU.

A person is sitting at a desk with a laptop, presenting a slide about the MSP430 microcontroller. The slide is titled 'MSP430G2x53 Features' and lists '16-BIT RISC Architecture' and 'Von Neumann Memory Architecture'. The person is wearing a grey jacket and a purple shirt. The background of the slide is dark with a large orange arrow pointing from the top left towards the bottom right.

So, the important feature is that it is a 16 bit risk architecture, it is a risk architecture. So, risk indicates that the instruction set architecture is of the risk variety reduced instructions at computer and from the memory interface point of view, it is a Von Neumann memory architecture. Here, it what this indicates is that the program memory, the data memory and the registers they are all mapped into a single memory map, as you see here.

(Refer Slide Time: 11:39)



This is the memory map, and in a single memory map, using a single set of address bus here and a single set of data bus, you are able to access the ROM, which is the program memory, the RAM and the registers and so on and so forth. So, this is to highlight the nature of MSP430 microcontroller family.

(Refer Slide Time: 12:06)

MSP430G2x53 Features

- Low Supply-Voltage Range: 1.8 V to 3.6 V TTL
- Ultra-Low Power Consumption
 - Active Mode: 230 μ A at 1 MHz, 2.2 V
 - Standby Mode (LPM): 0.5 μ A
 - Off Mode (RAM Retention): 0.1 μ A
- Five Power Saving Modes (LPM0 -LPM4)
- Ultra-Fast Wake-Up From Standby Mode in Less Than 1 μ s

These are the features, the supply voltage unlike the microcontrollers of previous era, where they would operate at standard 5 volts.

You may also remember that TTL ICs transistor transistor logic ICs operated at 5 volts but as the frequency operation increased, continuing to use a higher supply voltage led to higher power dissipation and one way to reduce power dissipation would be to reduce the supply voltage. And so MSP430, since it is it calls itself a low power microcontroller family. Therefore, one way to reduce the power dissipation is to reduce the power supply voltage. And here you can operate it in a wide range of voltage between 1.8 volts to 3.6 volts.

This makes it very easy to operate from a battery of various types and we will see all kinds of batteries we could use. The low power consumption numbers are mentioned here in the next part. In the active mode, it only consumes 0.23 milli amperes to 30 micro amperes of current. And that happens when you operate it at one megahertz clock and the supply voltage is 2.2 volt if you operate it in a what is called a standby mode, which is one of the low power modes that is available, it consumes a mere 0.5 micro ampere of current.

And you can imagine that if you have a coin cell or a button cell, even on the single button cell for how long this microcontroller can continue to operate, provided it has large periods of time when it is in the standby mode. But once in a while it would wake up and do real work and then go back to the standby mode. If you want to turn the internal operations off and just retain the contents of the RAM, then the power consumption will go down to 100 nano amperes. That is 0.1 micro amperes that is a really low current.

It offers a variety of low power operating modes, five low power operating modes that can be selected by the user through appropriate program, executing certain instructions. And you can go from low power mode 0 to low power mode 4. While you are in one of these low power modes. And if you want to go get back to the active mode, you do not want to waste time doing that switching.

And in this case you can get back to active mode in less than one microsecond. So, these are very important and salient features of MSP430 microcontroller family.

(Refer Slide Time: 14:51)

MSP430G2x53 Features

- 62.5ns Instruction Cycle Time (register to register operation)
- Basic Clock Module Configurations
 - Internal Frequencies up to 16 MHz with four calibrated frequencies - 1MHz, 8Mhz, 12Mhz, 16Mhz.
 - Internal Very-Low-Power Low Frequency (LF) Oscillator.
 - 32-kHz Crystal as external clock source ←
 - External Digital Clock Source

32768Hz

The slide also features a small inset image of a presenter in a video call window, with a background showing logos for Intel, TI, and other companies, and text including 'Introduction to Embedded System Design using MSP430' and 'DATA MANAGEMENT AND MEMORY CONTROL'.

The you can, as you noticed in that earlier slide that the dominant frequency operation is 16 megahertz, so if you operated your microcontroller at 16 megahertz, the clock period will be 62.5 nanoseconds.

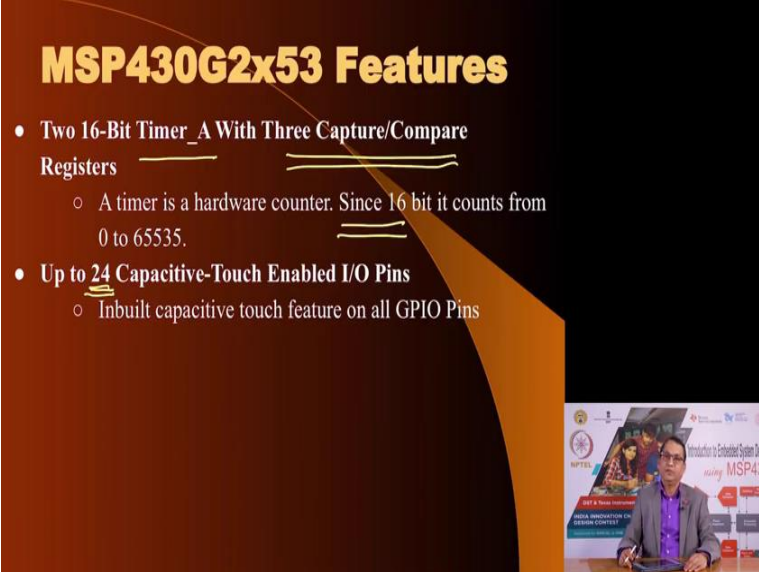
And in one clock cycle, you can execute instructions which would transfer data from a register to register. Of course, other instructions would take more clock cycles and therefore they would be taking more time. But this is the fastest instruction that you can execute on MSP430 microcontroller. The internal clock generator, which could take inputs from external pins, we will see which are those pins or you could utilize internal RC oscillators offer a wide variety of options.

One of them is you can have calibrated internal frequencies up to 16 megahertz, and you can choose whether you want to operate at 1 megahertz or you can want to operate at 8 or 12 or 16 megahertz. It also has internal, very low frequency oscillator, up to 12 kilohertz. And this is a very, very small frequency. And in case you do not have much computational activities to perform, you could switch yourself into low frequency, which will reduce your power dissipation.

You can use an external 32 kilohertz. 32 is exactly the 32768 hertz that I had mentioned. This kind of RTC crystal could be used as an external clock source, or you can have externally

generated digital clock source. You can apply to MSP430 as a source of clock, which could be internally utilised. And so and you can change the clock frequency. What we had mentioned, one of the salient features of contemporary microcontrollers is the ability to scale the clock frequency of operation. MSP430 has that feature.

(Refer Slide Time: 16:56)



MSP430G2x53 Features

- Two 16-Bit Timer_A With Three Capture/Compare Registers
 - A timer is a hardware counter. Since 16 bit it counts from 0 to 65535.
- Up to 24 Capacitive-Touch Enabled I/O Pins
 - Inbuilt capacitive touch feature on all GPIO Pins

Now for the input output it offers you two timers they are referred to as timer A and it has three captured compare units. These are requirements of a timer that if you want to say how much time has elapsed, how much time has elapsed, has no meaning. But the way it is interpreted, it is that you when the clock clocks the counter, it starts incrementing and when that value is equal to certain compare register value, then you say this much time has elapsed.

So, it has three compare capture registers with which you can do variety of things. The counter these timers are 16 bit and therefore they can count from 0 to 65535. That is the dynamic range of these timers and counters. It also have up to 24 pins, which have capacitive touch enabled capabilities, and all the GPIO pins could be utilized for that, this is a very important feature of MSP430 microcontrollers.

(Refer Slide Time: 18:07)

MSP430G2x53 Features

- Universal Serial Communication Interface (USCI)
 - UART →
 - IrDA Encoder and Decoder →
 - Synchronous SPI (Serial Peripheral Interface) ✓
 - I2C (Inter IC Communication) ✓
- On-Chip Comparator ✓
- Analog-to-Digital Cycle Time (A/D) Conversion

The slide also features a schematic diagram of a comparator with two input pins and a triangular symbol, and a small inset video frame showing a presenter in front of a screen displaying 'MSP430'.

For serial communication, it offers a wide variety of communication protocols again, looking from the point of view of one of the building blocks, one of the six building blocks that we discussed, and there was this blog regarding communication channels. And so this microcontroller offers built in communication protocols and the communication interface is called universal serial communication interface. It has these options you can have a UYART, which is the universal asynchronous receiver and transmitter.

You could also utilize it for infrared communication, bidirectional infrared communication IRDA. It could be used for serial peripheral interface, what is called as SPI bus. And if you remember from those discussions SPI and I square C that is inter IC communication were classified as intra device communication that is within your gadget that you are building that embedded system that you are building. If you want the microcontroller to communicate with an external IC which had this or this protocol, you could use the built in protocol built in communication capability of MSP430 to communicate with devices on within the system.

It also has a on chip competitor that is an analogue competitor, meaning something like this. This and it is not just one single competitor, it has many, many inputs which could be routed to this or the pin so that you can do a variety of comparisons, voltages. It also has a ADC and we will see more detail about this ADC built in ADC.

(Refer Slide Time: 20:02)

MSP430G2x53 Features

- 10-Bit 200-kSPS Analog-to-Digital (A/D) Converter
 - Internal Reference (1.5 V or 2.5 V)
 - Sample and Hold with programmable sample periods
 - Eight External Input Channels

The slide features two graphs showing a red sine wave sampled by a red staircase line, illustrating the sampling process. Below the graphs is a diagram of a multiplexer with eight input channels. In the bottom right corner, there is a small inset video of a presenter in front of a screen displaying the MSP430 logo and other technical details.

The ADC is capable of 10 bits of resolution here and up to two hundred thousand samples per second. That is the kind of maximum sampling rate that you can achieve out of this ADC.

ADC also require the reference voltage and you could choose an internal voltage reference of 1.5 or 2.5 volts. Of course, if you are if you are going to utilize an internal 2.5 volts of reference voltage then your supply voltage has to be obviously higher than 2.5 volts. You could also have an external reference voltage and we will see the pin out of this microcontroller, a sample microcontroller, see that there are certain pins on which you can connect external reference voltages.

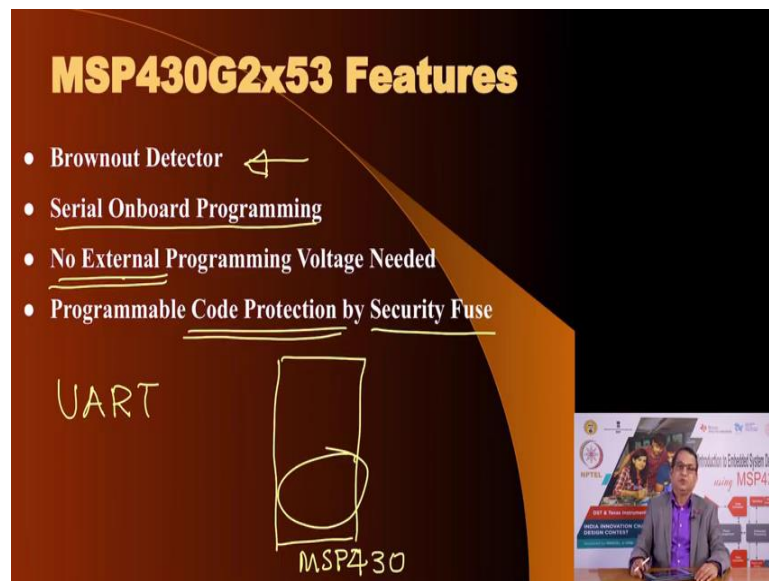
Now, ADC is required that when the conversion is in progress, that the input voltage should not change. This is a critical requirement of a ADC and MSP430 has an integrated sample and hold units where the sampling frequency, which is up to 200 kilo samples per second. But you can sample it at lower frequencies also. And it has a sample and hold for all these inputs. And you can have up to 8, you can have up to 8 ADC inputs. That does not mean that it has 8 ADCs it simply means that it has one ADC if I draw the ADC block diagram like this, it has 8 channels.

And with the multiplexer using a multi analogue multiplexer and you can select at any given time, one of the inputs will be connected to the actual ADC and a conversion will be performed.

And this is how the conversion would look like if you could sample it here, but in reality that value will remain constant till the next conversion begins here and so on.

So, away from which the way form which is shown invite will, once you discretise it will be represented in the red signal that you see in this diagram, this is the result of discrimination performed through ADC.

(Refer Slide Time: 22:19)



Now, we will discuss more features that MSP430 has and I encourage you to map these features with the salient features of modern microcontrollers that we had covered in a previous lecture.

And you can realize you would realize that MSP430 is a truly modern contemporary microcontroller. It has reset source, one of the ways of resetting I had mentioned is a brownout detector. Of course, it has use a reset. Also, we will see which pin is use for that you can program you can download the program into the memory of the microcontroller if this is your MSP430.

You can download the code in a variety of ways, one of the methods is called serial on board programming, which means it uses two pins to download program. Apart from that, you can use J tag interface for downloading the program and you can also use and in application programming method using a pre-programmed boot loader, which is available in the flash memory of the microcontroller, you could utilize it.

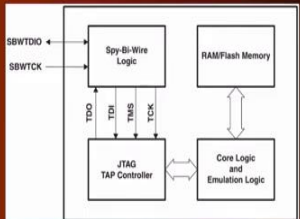
If you choose to use the in application programming that is programming with boot loader, then it uses the UART which MSP430 has. And in fact our MSP430 evaluation kit, which is the MSP430 lunchbox, uses this method. This allows you to program microcontrollers with very little external support. And we will show you variety ways of actually making your own evaluation kit. In case you do not have access to the lunchbox, we will show you how you can create your own set up.

While programming in many microcontrollers, you require some external voltage. But in the case of MSP430, no such voltage required. And so all you do is just connect some wires to your microcontroller to your desktop or laptop, and you can quickly download the program. And once the program is downloaded in the memory of the microcontroller, you can secure it by invoking code protection, using security fuse. And this will ensure that the program, which is inside the microcontroller, is secure from being copied and hacked by external you know elements. So, this is a very important feature to protect the intellectual property of your product.

(Refer Slide Time: 24:54)


MSP430G2x53 Features

- **On-Chip Emulation Logic With Spy-Bi-Wire** SWD
Spy-Bi-Wire is a serialised JTAG protocol.
The two connections are a bidirectional data output (SBWTDIO), and a clock (SBWTCK). The clocking signal is split into a period of three clock pulses, for each clock pulse the TDI, TDO and TMS signals are passed on the microcontroller via the bidirectional data output.



The diagram illustrates the internal components of the MSP430G2x53 microcontroller. It shows a central 'Spy-Bi-Wire Logic' block connected to 'RAM/Flash Memory' and 'Core Logic and Emulation Logic'. The 'JTAG TAP Controller' is also connected to the 'Core Logic and Emulation Logic'. External signals 'SBWTDIO' and 'SBWTCK' are shown entering the 'Spy-Bi-Wire Logic' block. Internal signals 'TDI', 'TMS', and 'TCK' are shown connecting the 'JTAG TAP Controller' to the 'Spy-Bi-Wire Logic'.

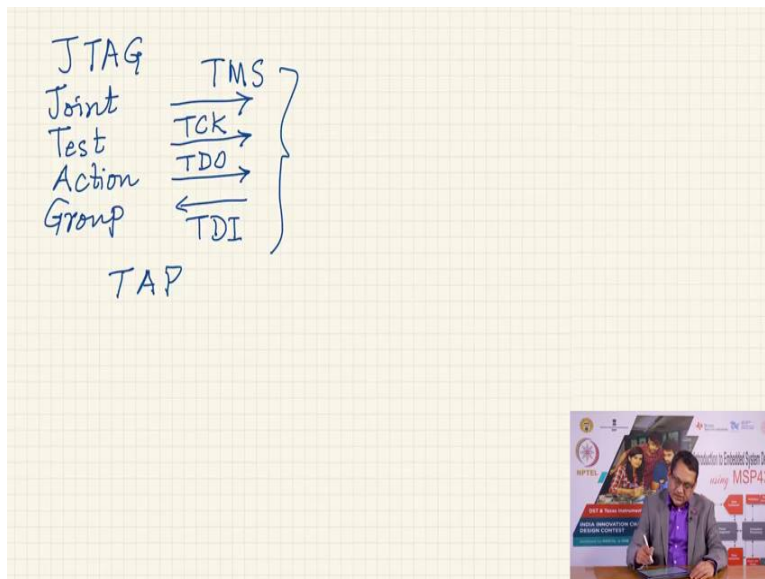
Spy-Bi-Wire Basic Concept Diagram



A small inset image shows a man in a suit sitting at a desk, likely the speaker for the presentation. Behind him is a banner for the 'MSP430' microcontroller, featuring the Texas Instruments logo and various technical details.

Let us see some more features it offers you on chip emulation, which means you can once your download your program, you would like to evaluate how it is functioning and it uses spy bi wire protocol, which is a SWD name that Texas Instrument gives to serial wire debugged, called spy bi wire, and it is actually a serialized J tag protocol.

(Refer Slide Time: 25:31)



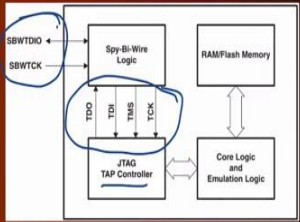
Let me explain to you J tag, because we have used this term several times a J tag is J tag interface it stands for Joint Test Action Group. It is the name of the protocol. It is a 4 wire serial protocol it has 4 pins TMS, a clock signal called TCK, a data output called TDO and a data input called TDI and this was primarily used by a manufacturers of digital ICs for testing, and it uses a concept called test access port over a period of time.

This protocol has been adopted and incorporated in microcontroller units and MSP430 has embraced it. And so you would see some of the pins of MSP430 have J tag functionality. But because it uses 4 pins for such microcontrollers where the number of available pins is less, Texas Instruments created a mechanism to use only two pins and out of those two pins to recreate the J tag interface of these 4 pins.

(Refer Slide Time: 26:59)

MSP430G2x53 Features

- On-Chip Emulation Logic With Spy-Bi-Wire SWD
Spy-Bi-Wire is a serialised JTAG protocol.
The two connections are a bidirectional data output (SBWTDIO), and a clock (SBWTCK). The clocking signal is split into a period of three clock pulses, for each clock pulse the TDI, TDO and TMS signals are passed on the microcontroller via the bidirectional data output.



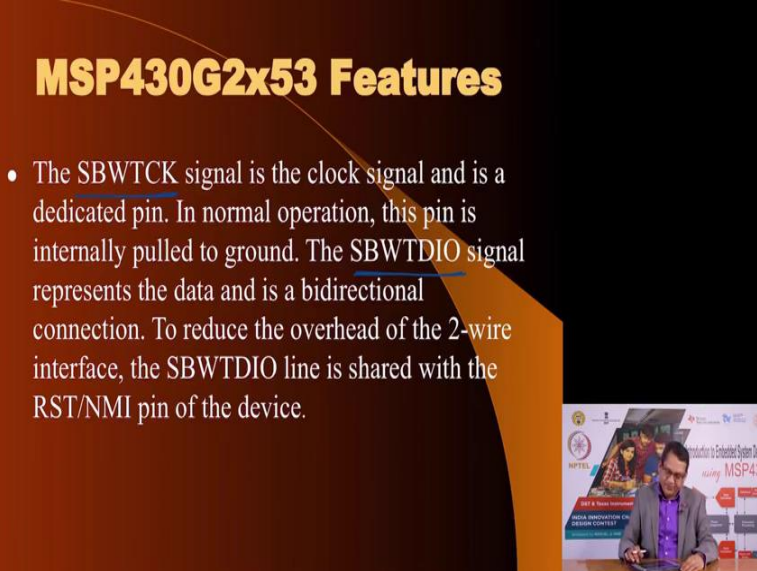
The diagram illustrates the internal architecture of the MSP430G2x53. It shows external pins SBWTDIO and SBWTCK connected to internal Spy-Bi-Wire Logic. This logic is bidirectionally connected to RAM/Flash Memory and a JTAG TAP Controller. The JTAG TAP Controller is also bidirectionally connected to Core Logic and Emulation Logic. The JTAG TAP Controller interface includes TDI, TMS, and TCK signals. The text 'Spy-Bi-Wire Basic Concept Diagram' is written to the right of the diagram.

And that is the meaning of the spy bi wire protocol as you see, using two external pins here, it connects to the internal spy bi wire logic, and from there it recreates the standard J tag interface, which allows it to connect to the core logic and memory which is utilized when you want to debug your program, that after this instruction is executed, what is the result of that instruction? What is the content of a given register? You can access that through the spy bi wire interface.

(Refer Slide Time: 27:37)

MSP430G2x53 Features

- The SBWTCK signal is the clock signal and is a dedicated pin. In normal operation, this pin is internally pulled to ground. The SBWTDIO signal represents the data and is a bidirectional connection. To reduce the overhead of the 2-wire interface, the SBWTDIO line is shared with the RST/NMI pin of the device.



This slide continues the presentation of the MSP430G2x53 features. It includes a small inset image of a presenter at a desk with a laptop, and a banner in the background that reads 'Introduction to Embedded System Design using MSP430'.

And this spy bi wire interface actually uses 2 pins, one of them is the reset pin and other is a pin called test and it has these pins are renamed SBW Clock and SBW data and using these to the internal J tag 4 wire 4 pins of J tag are created.

(Refer Slide Time: 28:05)



This is the available options, packaging options, you can have MSP43 in bit form and we are going to use a DIP dual in line package IC for our experimentation board. You can also have it in SMD varieties of the type called TSOP. And then even small TSOP If you notice, it has pins on two sides just like you have on DIP. But when you have a quad flat pack there you get pins on all the four sides. So, this leads to even smaller size of package because then you are utilizing all the sides of the IC.

So, your MSP microcontroller, MSP430 microcontroller is available with so much variety. Let us go through the pin out or what we will do is we will consider the pin out in the next lecture because we are just run out of time for this session. I will see you soon. Thank you.