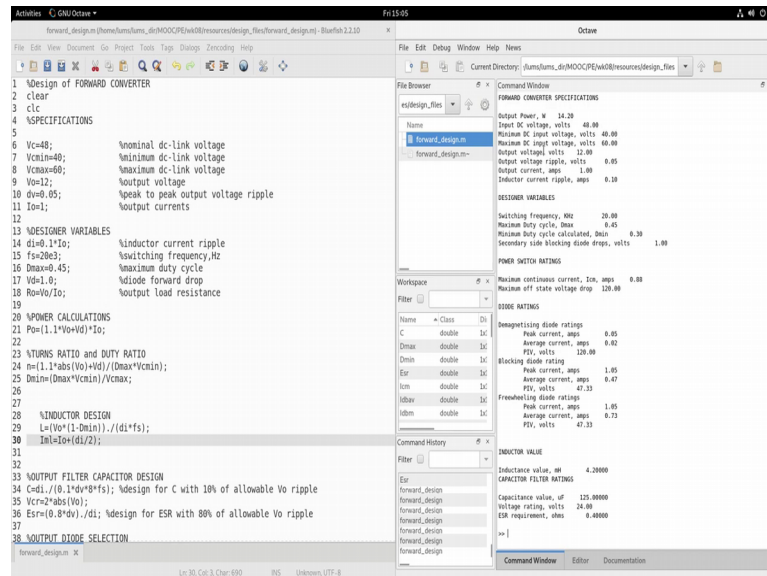


Fundamentals of Power Electronics
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Lecture - 60
Octave mfile for design

(Refer Slide Time: 00:28)



I have here on the screen on the left side, a text editor and on the right side I am having the octave workspace screen. You could either use octave or MATLAB both are compatible and on this text editor, I have here m file, forward design dot m file. This is basically an m file a text ASCII file, where I am trying to summarize, the equations in to make some kind of a semblance for design.

Now, designer first I am using clearing the screen and clearing the variables. in a systematic way let us first make the specifications. So, the input voltage specification, 48 volts nominal, DC link voltage we call that , the input VI can take a minimum value or a maximum value, you should design for worst case. So, this example takes care of variations, you have a minimum value of the DC input and the maximum value of the DC input.

V naught, let us say you want it for 12 volts. There is a delta V, there is output voltage ripple peak to peak ripple of 0.05 times 0.05 and I naught is 1 amp, this is the output current. So, designer variables delta I is 10 percent of I naught fs is 20 kilohertz, Dmax

do not go beyond 0.544 you do not go beyond 0.5. So, to be on the safer side I have kept it at 0.45 and you can calculate R0

Now, power calculation P naught is V naught 1.1 to account for winding losses plus the Vd diode drop will be the output, on the secondary side the voltage into I naught. The turns ratio, you can calculate the turns ratio, I will use D max value and Vcmin when D max occurs, input voltage is minimum and when the input voltage is maximum D is minimum. So, D minimum you can calculate using that $v_{i\ min}$ into D max is equal to $v_{i\ max}$ into Dmin and find out the value.

Inductor design like we discussed, I have here the equation and I naught plus delta i by 2 will be the peak inductor current.

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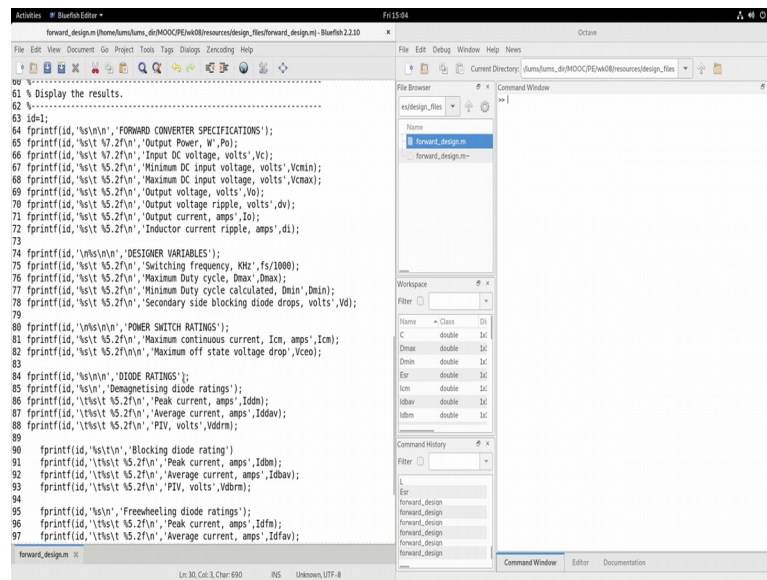
```

25 Dmin=(Dmax*Vcmin)/Vcmax;
26
27
28 %INDUCTOR DESIGN
29 L=(Vp*(1-Dmin))/(di*fs);
30 Irl=Ia+(di/2);
31
32
33 %OUTPUT FILTER CAPACITOR DESIGN
34 C=di./(0.1*dv*8*fs); %design for C with 10% of allowable Vo ripple
35 Vcr=2*abs(Vo);
36 Esr=(0.8*dv)/di; %design for Esr with 80% of allowable Vo ripple
37
38 %OUTPUT DIODE SELECTION
39 %Demagnetising winding diode
40 Idm=0.1*(n*Io)*sqrt(Dmax);
41 Idav=0.1*(n*Io)*sqrt(Dmax)*(1-Dmin)/2;
42 Vdfr=2*Vcmax;
43
44 %Secondary winding blocking diodes
45 Idm=Io+(di/2);
46 Idav=Idm*Dmax;
47 Vbr=Vcmax;
48
49 %Secondary side freewheeling diodes
50 Idf=Io+(di/2);
51 Idf=Idf*(1-Dmin);
52 Vdfr=Vcmax;
53
54 %POWER SWITCH DESIGN
55 Imag=0.1*(n*Io)*sqrt(Dmax);
56 Icm=(Io+(di/2)) + Imag;
57 Vce=2*Vcmax;
58
59
60 %
61 % Display the results.
62 %

```

Likewise, you can design the output capacitor C the voltage across the capacitor voltage rating and the Esr values also. Output, the diode selections the currents through the demagnetizing diode, average current, peak current and the voltage peak inverse voltage. Likewise the blocking diodes, peak current, average current, peak inverse voltage. As we discussed put in the formulas there and the power switch rating also can be selected in this fashion putting the equations have a look at it.

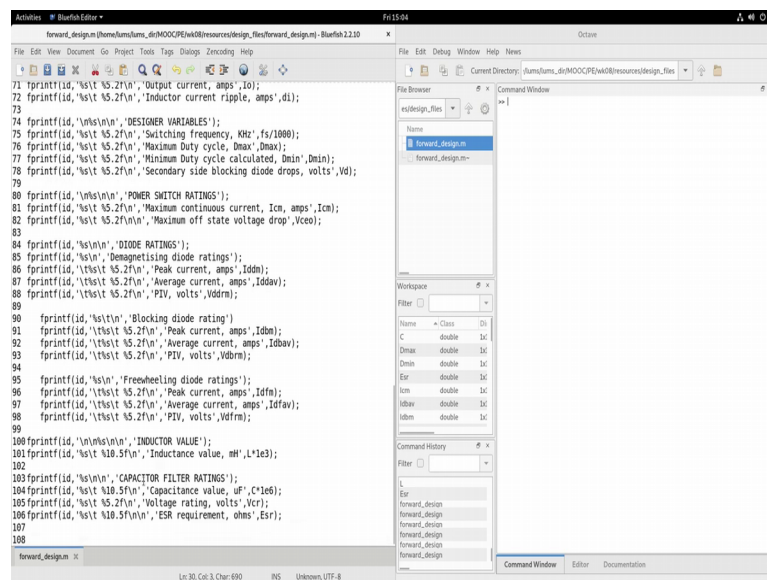
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```
forward_design.m (home\lums_din\MOOCPE\w40\resources\design_files\forward_design.m - Bluefish 2.2.10)
61 % Display the results.
62 %
63 id=;
64 fprintf(id, '%s\n', 'FORWARD CONVERTER SPECIFICATIONS');
65 fprintf(id, '%s\t %7.2f\n', 'Output Power, W, Po);
66 fprintf(id, '%s\t %7.2f\n', 'Input DC voltage, volts, Vc);
67 fprintf(id, '%s\t %5.2f\n', 'Minimum DC input voltage, volts, Vcmin);
68 fprintf(id, '%s\t %5.2f\n', 'Maximum DC input voltage, volts, Vcmax);
69 fprintf(id, '%s\t %5.2f\n', 'Output voltage, volts, Vo);
70 fprintf(id, '%s\t %5.2f\n', 'Output voltage ripple, volts, dv);
71 fprintf(id, '%s\t %5.2f\n', 'Output current, amps, Io);
72 fprintf(id, '%s\t %5.2f\n', 'Inductor current ripple, amps, di);
73
74 fprintf(id, '%s\n', 'DESIGNER VARIABLES');
75 fprintf(id, '%s\t %5.2f\n', 'Switching frequency, KHz, fs/1000);
76 fprintf(id, '%s\t %5.2f\n', 'Maximum Duty cycle, Dmax, Dmax);
77 fprintf(id, '%s\t %5.2f\n', 'Maximum Duty cycle calculated, Dmin, Dmin);
78 fprintf(id, '%s\t %5.2f\n', 'Secondary side blocking diode drops, volts, Vd);
79
80 fprintf(id, '%s\n', 'POWER SWITCH RATINGS');
81 fprintf(id, '%s\t %5.2f\n', 'Maximum continuous current, Icn, amps, Icn);
82 fprintf(id, '%s\t %5.2f\n', 'Maximum off state voltage drop, Vceo);
83
84 fprintf(id, '%s\n', 'DIODE RATINGS');
85 fprintf(id, '%s\n', 'Demagnetising diode ratings');
86 fprintf(id, '%s\t %5.2f\n', 'Peak current, amps, Idpm);
87 fprintf(id, '%s\t %5.2f\n', 'Average current, amps, Idsav);
88 fprintf(id, '%s\t %5.2f\n', 'PIV, volts, Vdprm);
89
90 fprintf(id, '%s\t\n', 'Blocking diode rating');
91 fprintf(id, '%s\t %5.2f\n', 'Peak current, amps, Idpm);
92 fprintf(id, '%s\t %5.2f\n', 'Average current, amps, Idsav);
93 fprintf(id, '%s\t %5.2f\n', 'PIV, volts, Vdprm);
94
95 fprintf(id, '%s\n', 'Freewheeling diode ratings');
96 fprintf(id, '%s\t %5.2f\n', 'Peak current, amps, Idfm);
97 fprintf(id, '%s\t %5.2f\n', 'Average current, amps, Idfav);
98
forward_design.m:Ln 30, Col 3, Char: 690  INS Unknown, UTF-8
```

After the equations we need to have one more important program section, which is the display of the results. So, I am using f print f, both MATLAB octave will permit that.

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```
forward_design.m (home\lums_din\MOOCPE\w40\resources\design_files\forward_design.m - Bluefish 2.2.10)
71 fprintf(id, '%s\t %5.2f\n', 'Output current, amps, Io);
72 fprintf(id, '%s\t %5.2f\n', 'Inductor current ripple, amps, di);
73
74 fprintf(id, '%s\n', 'DESIGNER VARIABLES');
75 fprintf(id, '%s\t %5.2f\n', 'Switching frequency, KHz, fs/1000);
76 fprintf(id, '%s\t %5.2f\n', 'Maximum Duty cycle, Dmax, Dmax);
77 fprintf(id, '%s\t %5.2f\n', 'Maximum Duty cycle calculated, Dmin, Dmin);
78 fprintf(id, '%s\t %5.2f\n', 'Secondary side blocking diode drops, volts, Vd);
79
80 fprintf(id, '%s\n', 'POWER SWITCH RATINGS');
81 fprintf(id, '%s\t %5.2f\n', 'Maximum continuous current, Icn, amps, Icn);
82 fprintf(id, '%s\t %5.2f\n', 'Maximum off state voltage drop, Vceo);
83
84 fprintf(id, '%s\n', 'DIODE RATINGS');
85 fprintf(id, '%s\n', 'Demagnetising diode ratings');
86 fprintf(id, '%s\t %5.2f\n', 'Peak current, amps, Idpm);
87 fprintf(id, '%s\t %5.2f\n', 'Average current, amps, Idsav);
88 fprintf(id, '%s\t %5.2f\n', 'PIV, volts, Vdprm);
89
90 fprintf(id, '%s\t\n', 'Blocking diode rating');
91 fprintf(id, '%s\t %5.2f\n', 'Peak current, amps, Idpm);
92 fprintf(id, '%s\t %5.2f\n', 'Average current, amps, Idsav);
93 fprintf(id, '%s\t %5.2f\n', 'PIV, volts, Vdprm);
94
95 fprintf(id, '%s\n', 'Freewheeling diode ratings');
96 fprintf(id, '%s\t %5.2f\n', 'Peak current, amps, Idfm);
97 fprintf(id, '%s\t %5.2f\n', 'Average current, amps, Idfav);
98
100 fprintf(id, '%s\n', 'INDUCTOR VALUE');
101 fprintf(id, '%s\t %10.5f\n', 'Inductance value, mH, L*1e3);
102
103 fprintf(id, '%s\n', 'CAPACITOR FILTER RATINGS');
104 fprintf(id, '%s\t %10.5f\n', 'Capacitance value, uF, C*1e6);
105 fprintf(id, '%s\t %5.2f\n', 'Voltage rating, volts, Vcr);
106 fprintf(id, '%s\t %10.5f\n', 'ESR requirement, ohms, Esr);
107
108
forward_design.m:Ln 30, Col 3, Char: 690  INS Unknown, UTF-8
```

So, FORWARD CONVERTER SPECIFICATIONS, start printing the spec, designer variables, then the switch ratings, diode ratings, blocking diode ratings, inductor value and the capacitor value use the variables and then give meaningful statements.

Now, this if you run executed, it will design the values for these set of specifications. Now, let us run that and see I am already in that resource file, resource folder. You can

see that in octave workspace forward design, let me run that. So, on typing forward design do not put dot m just without the dot m. So, you execute it, you will see that the, all the commands are executed, you get the specs, the designer variables and then the power switcher ratings, diode ratings, all are calculated and you get the entire design of the forward convertor.

We have not include the transformer here, because we have not studied the transformer. Once, we do the study of the transformer and inductor magnetics, which I will do in the next session, then you will be able to use the magnetics portion of the design also and then we will update this file to include the magnetics design. I will request that you do a similar such m file for flyback converter also and try it out.