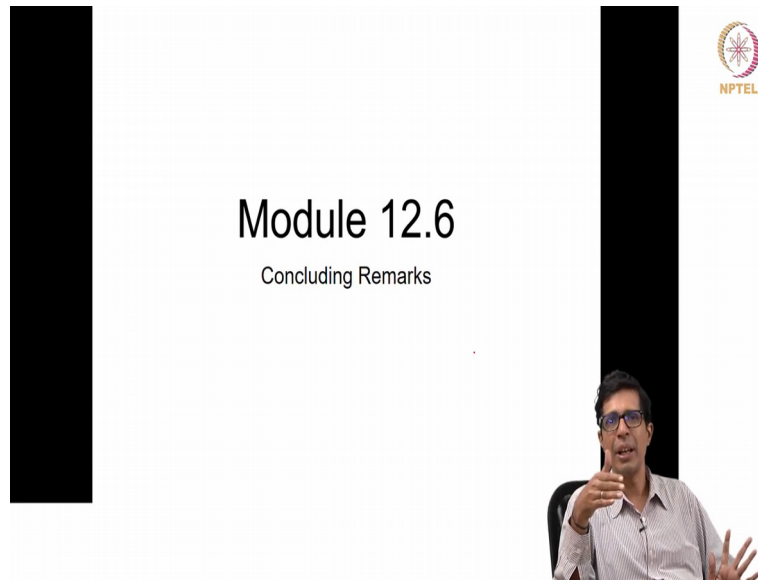**Foundations to Computer System Design**
Professor V. Kamakoti
Department of Computer Science and Engineering
Indian Institute of Technology, Madras
Module 12.6
Concluding Remarks

(Refer Slide Time: 0:25)

Welcome to module 12.6 I hope you all enjoyed this course, I hope we have done quite a bit of thing we have started off from simple hardware, we assemble… We made the hardware, we had a (())(0:31) language, we wrote an assembler, the design an assembly language, wrote an assembler then from there we define a virtual machine, we wrote a virtual machine to assembler translator then we wrote a full list compiler and so we have come to a place where we have done an entire system engineering. I am sure if you finish the projects till 12 I think it is going to be a wonderful experience for you and I hope you have done all the assignments let us keep in touch and I hope you enjoy this course, Thank you very much. Jai Hind.