

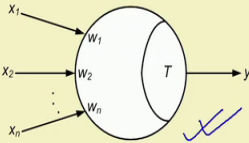
Switching Circuits and Logic Design
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Lecture – 30
Threshold Logic and Threshold Gates

So, far if you recall you had discussed various ways designing logic circuits using conventional gates like AND, OR, NOT, NAND, NOR, XOR and also some functional blocks like multiplexer decoders etcetera. In this lecture we shall be looking at a slightly different way of designing logic functions, not using the conventional gates, but using something which is a little unconventional. The title of a talk here is this lecture is Threshold Logic and Threshold Gates. So, in this lecture we shall be talking about something called threshold logic circuits and how we can implement threshold logic circuits using threshold gates.

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Basic Concepts

- Schematic of a Threshold element or Threshold gate:

- How does it work?
 - $y = 1$, if and only if $w_1x_1 + w_2x_2 + \dots + w_nx_n \geq T$
 - $y = 0$, if and only if $w_1x_1 + w_2x_2 + \dots + w_nx_n < T$

Weights
Threshold

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So, let us see the basic idea behind this so, called threshold logic ok. The first thing is that, in threshold logic the basic element that you talk about is called a threshold element or a threshold gate this is how a threshold gate looks like in its schematic form. So, you see in a threshold gate what are the basic components, if I show it as a rectangular box there are some inputs, which are applied. So, in this example have I shown that there are n inputs x_1, x_2 up to x_n and there is one output y .

Now, there are a few other parameters you can see each input assigned some weight is W_1, W_2, W_n these are called weights and, there is another parameter quality is called T , T is called a threshold so, unlike a gate which we have learned earlier and or not etcetera, where only inputs and the output matters, here in addition you have some weights and threshold. Now, how does the weights and threshold play a role? You say this threshold element, or threshold gate the weight works well the inputs x_1, x_n these are binary inputs, means they can be 0 or 1.

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Basic Concepts

- Schematic of a *Threshold element* or *Threshold gate*:
 - Inputs: x_1, x_2, \dots, x_n (Binary)
 - Weights: W_1, W_2, \dots, W_n
 - Threshold: T
 - Output: y (Binary)
- How does it work?
 - $y=1$, if and only if $W_1x_1 + W_2x_2 + \dots + W_nx_n \geq T$
 - $y=0$, if and only if $W_1x_1 + W_2x_2 + \dots + W_nx_n < T$

Handwritten notes on the slide:

- $\sum_{i=1}^n W_i x_i$
- $W_i, T \Rightarrow \text{real numbers}$
- 1
- 2
- 3
- 0.5
- 2.5
- ...

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The output y this is also a binary output, but this weights W_i and the threshold these are not binary numbers are real numbers. Real number means they can be integers, they can be fractions, they can be negative also, they can be any arbitrary numbers like for example, is weights and threshold values can be 1, they can be 2, they can be minus 3, 0.5 minus 2.5 etcetera. So, you can have any such numbers integer numbers not only integers also numbers with fractional points both positive and negative.

Now, the way the output get us decided is as follows the output why you will be equal to 1, if the weighted sum of the inputs by weighted some inputs by weighted sum means summation $W_i x_i$ equal to 1 to n .

So, you see here we have written down in the expanded form W_1, X_1, W_2, X_2 upto $W_n X_n$ if this weighted sum is greater than or equal to the threshold T , then the output will be 1, otherwise the output will be 0. This is how a basic threshold gate works. So, you

have the inputs you have the weights assigned to the input you have a threshold, you compute the weighted sum, if the weighted sum is greater than or equal to the threshold the output will be 1, if it is less than the threshold the output will be 0, this is the functional behavior of a threshold gate ok.

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Why Important?

- Threshold logic can be used for neuromorphic computing.
 - Can model neurons in brains.
- An interesting alternative in logic design:
 - Can result in much simpler circuit realizations for many functions.
 - Technologies for implementation are also available (e.g. memristors).

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Now, let us try to understand why threshold logical and threshold gates are considered important today. The first thing is that threshold logic has a direct connection with neuromorphic computing, well neuromorphic computing is a branch of you can say computer science, where we are trying to mimic the behavior of a brain. The way the brain works there are basic building blocks in the brain which are called neurons, neurons work in a way which is very similar to threshold gate, there are weights there is a concept of a weighted sum, there is a concept of a threshold, this is how people normally model a neuron.

So, if you have a way to build a threshold gate, then using threshold gates you can possibly model the brain also so, this is called neuromorphic computing. So, you can model the neurons that are present in the brains. Well if you forget neuromorphic computing even in conventional logic design, this can be considered as an alternative. Because we shall see a few examples that you can get much simpler circuit realizations, for many functions not all functions of course, for many functions using threshold gates

you can get very small circuits, very easy to design and implement ok, this can be one advantage.

And second thing is that although threshold logic is relatively old, if this is not a new concept. But technologies for implementing them efficiently is available only recently, earlier the technology was not available that is why people did not build circuits using threshold gates, but today people are thinking about building. For example, we talked about memory stress earlier, using memory stress people are working very actively toward building are threshold logic gates and with applications in neuromorphic computing fine.

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An Example

Consider the function:

$$y = f(x_1, x_2, x_3) = \Sigma(1,2,3,6,7) = x_1'x_3 + x_2$$

The diagram shows a threshold gate with three inputs: x_1 (weight -1), x_2 (weight 2), and x_3 (weight 1). The threshold is $\frac{1}{2}$. The output y is connected to a logic circuit consisting of an AND gate with inputs x_1' and x_3 , and an OR gate with inputs x_2 and the output of the AND gate.

Let us take an example now here we considered a four variable function, let us look from the other side. Let us say that we want to implement a function like this. So, it is a sum or product specification by the true min terms are 1 2 3 6 and 7. So, if you do a minimization using k map for any other method, you will see that will minimum form is this $x_1 \bar{x}_3$ or x_2 .

Now, here I am not telling how, but this is a threshold logic implementation of this function. Now, you look at one thing if you wanted to implement this function using conventional gates, then you would require an AND gate with x_1 you would require a not gate, $x_1 \bar{x}_3$ and this will go to an OR gate, it will be connecting x_2 here.

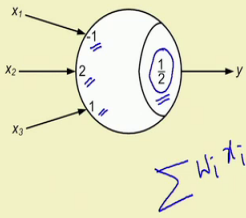
So, you would be requiring three gates, but using threshold logic this is just an example single threshold element you can implement this function.

Now, let us see how this single threshold element actual implement this function you see, here the weights that are used are minus 1 2 and plus 1 and the threshold is half. Let us see how this gate works.


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An Example

Consider the function:

$$y = f(x_1, x_2, x_3) = \sum(1, 2, 3, 6, 7) = x_1'x_3 + x_2$$


Inputs			Weighted Sum	Output
x_1	x_2	x_3	$-x_1 + 2x_2 + x_3$	y
0	0	0	0 ✓	0 ✗
0	0	1	1 ✓	1 ✓
0	1	0	2 ✓	1 ✓
0	1	1	3 ✓	1 ✓
1	0	0	-1 ✓	0 ✗
1	0	1	0 ✓	0 ✗
1	1	0	1 ✓	1 ✓
1	1	1	2 ✓	1 ✓


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Look at this truth table well on one side; we have considered all possible input combination of $x_1 \times x_2 \times x_3$. And because the weights are minus 1 2 and 1, we are computing the weighted sum as I said weight multiplied by the inputs. So, here the expression will be minus x_1 plus 2 x_2 plus x_3 , which is this so, with respect to the values of x_1, x_2, x_3 .

If you if you calculate this expression the value that you get are like this 0 1 2 3 minus 1 0 1 2, you can check for example, for 0 1 1 it will be 0 plus 2 into 1 plus 1 which is 3 minus 1 1 0 0 minus 1 plus 0 plus 0 minus 1.

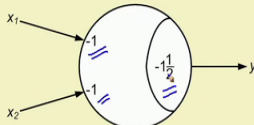
So, now you see because the threshold is 0.5 half. So, among these how many of them are greater than equal to half and how many of them and less than half, you see this is greater than half, this is greater than half 2, 3 is also greater than half 1 is also greater than half 2 is also greater than half, but this 1 0 minus 1 and 0, these are not greater than half that is why you are getting the output 0. So, you can verify this is nothing, but what

you wanted to implement 1 2 3 6 7 you see this is this one corresponds to 0 0 1 which is 1, this is 1 2 0 1 0 this is 2 1 0 1 1, this is 3 1 1 1 is 0 is 6 and 1 1 1 is 7, you see 1 2 3 6 7 this is what you wanted to implement ok. So, this example shows that how using a single threshold element OR gate we can implement a complex switching function a switching expression.

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Threshold Gate is Functionally Complete

- Threshold gate is a generalization of conventional gates.
 - More powerful than conventional gates because it can realize a larger class of functions.
 - Any conventional gate can be realized with a threshold gate.
 - Thus, threshold gates are functionally complete.
- **Example:** NAND implementation



x_1	x_2	$-x_1 - x_2$	y
0	0	0	1
0	1	-1	1
1	0	-1	1
1	1	-2	0

$T = -1.5$

$\geq T$

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The next thing that you want to talk about is well we talked about functional completeness earlier, we said that when you build circuits, we should be confident that the basic building blocks that are given to us those can be used to design an implement any circuit to you want for instance AND OR NOT by definition they are functionally complete. So, if you have these three kind of gates given to us we can design any circuit, similarly we proved NAND is functionally complete NOR is functionally complete. So, if you give us only NAND gates we can design any circuit using only NAND gates.

Now, here we try to show that is threshold gate also a functionally complete gate. So, using threshold gate alone therefore, we can design any function you want let us see what is our logic. So, we are trying to say threshold gate is functionally complete. The first thing is that this already have seen through the example that we showed just, now threshold gate is some kind of a generalization of conventional gates, they are more powerful. Because we can implement functions which otherwise would be requiring several conventional gate, we can implement the same function using a single threshold

element, or a single threshold gate in that way a threshold gate is more powerful, because it can realize a larger class of functions.

Now, the point you want to show here is that because we are saying functionally complete, we want to show that any conventional gate should be realized with a threshold gate. So, if you can show this then you can prove that threshold gates are functionally complete. Now, the way we give the proof is through an example, we show a threshold gate implementation with the input weights minus 1 and minus 1 and the threshold of minus 1.5 minus 1 and half.

So, you can verify that this threshold gate actually implements the NAND is function and, because NAND is functionally complete. So, if you can show that this threshold gates actually implements NAND so, we have proved that threshold gate is functionally complete.

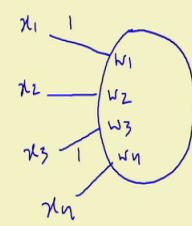
Now, let us see how this implements NAND, let us quickly again draw the truth table here so, x_1 and x_2 are the inputs so, there can be four combinations 0 0 1 1 0 and 1 1. And here what is the weighted sum minus 1 minus 1 so, simply minus x_1 minus x_2 so, the weighted sum is minus x_1 minus x_2 . So, if you just calculate the weighted sum on these it will be 0 minus 1 minus 1 and minus 2.

So, what will be the output y the threshold here, the threshold is minus 1.5. So, if this weighted sum is greater than or equal to T , then the output will be 1. So, in this case you see minus 1.5 the first three they are greater than minus 1.5 minus 2 is less than minus 1.5. So, for the first three the output will be one this the output will be 0 this is nothing, but the NAND function right. So, we have proved that using threshold gate we can construct a NAND gate so, therefore, it is functionally complete ok.

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Can Any Function be Realized using a Single Threshold Gate?

- The answer is no.
- A counterexample:
 - Consider the function: $f(x_1, x_2, x_3, x_4) = x_1x_2 + x_3x_4$


$$\sum w_i x_i$$
$$x_1 w_1 + x_3 w_3$$

$x_1 = 0$
 $x_1 = 1$

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Now, the next question we ask is, can we implement any arbitrary function using a single threshold gate, well earlier we have seen that that implemented a slightly complex function, say $x_1 \bar{x}_3$ plus x_2 that kind of function using a single gate, now the question is given any arbitrary function can we implement using a single threshold gate.

Let us try to answer this question of course; the answer is no all functions cannot be implemented. Now, how do we prove this? We give a counter example, we show an example logic function and give a justification why is this cannot be implemented using threshold logic.

Let us see so, the first thing is that the answer is no as I said so, you cannot implement any function arbitrary function using threshold gate. Now, the example with we take is like this now one thing we would like to just tell you, you just understand how a threshold gate works once more the inputs are x_1 x_2 x_3 and x_4 , with the weights w_1 w_2 w_3 and w_4 .

Now, when you calculate the weighted sum right if, and some particular input let us say x_1 is 0, then that component $x_1 w_1$ will not appear in the weighted sum this will become 0. So, you will have to consider only those combinations for which it is 1, only those terms will come for example, if x_1 is 1 then x_3 is 1, then the weighted sum will be only $x_1 w_1$ plus $x_3 w_3$, x_2 and x_4 will not appear here, because they are 0 right ok.

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Can Any Function be Realized using a Single Threshold Gate?

- The answer is no.
- A counterexample:
 - Consider the function: $f(x_1, x_2, x_3, x_4) = x_1x_2 + x_3x_4$
 - Output must be 1 for the minterms: $x_1x_2x_3x_4'$ and $x_1x_2'x_3x_4$

x_1	x_2	x_3	x_4
1	1	x	x
		0	0

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So, with this example because the product terms here are x_1x_2 and x_3x_4 . So, the output must be one for these two minterms so, x_1x_2 say x_1x_2 must be 1 must be 1, but x_3x_4 can be anything they can be $1x_3x_4$ are actually do not care, then the first product term will be 1, but because we are talking about threshold gate, we are talking about the minimum what is the minimum weighted sum minimum weighted sum will happen, if this 2 as 0 0. So, let us take the 00 part x_3x_4 bar. Similarly $4x_4$ we take x_1x_2 bar, let us take these two minterms.

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Can Any Function be Realized using a Single Threshold Gate?

- The answer is no.
- A counterexample:
 - Consider the function: $f(x_1, x_2, x_3, x_4) = x_1x_2 + x_3x_4$
 - Output must be 1 for the minterms: $x_1x_2x_3x_4'$ and $x_1x_2'x_3x_4$

$$w_1 + w_2 \geq T$$

$$w_3 + w_4 \geq T$$

$$\text{So, } w_1 + w_2 + w_3 + w_4 \geq 2T$$

$w_3x_3 + w_4x_4 > T$

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So, if we take these two min terms the output is supposed to be 1 so, what does this mean just in terms of weighted sum it will be $x_1 W_1$ plus $x_2 W_2$ because these two are 0, they will not come and because $x_1 x_2$ are 1 and 1 it will only W_1 plus W_2 . Now, in order the output be one this must be greater than equal to the threshold.

Similarly for the second min term $x_3 x_4$ are 1 1 so, W_3 plus W_4 must be greater than or equal to T , because your condition was $W_3 x_3$ plus $W_4 x_4$ greater than or equal to T , now x_3 and x_4 are 1 so, it is W_3 plus W_4 greater than or equal to T . So, if you add this to up you get $W_1 W_2 W_3 W_4$ their is greater than or equal to twice T right.

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Can Any Function be Realized using a Single Threshold Gate?

- The answer is no.
- A counterexample:
 - Consider the function: $f(x_1, x_2, x_3, x_4) = x_1 x_2 + x_3 x_4$
 - Output must be 1 for the minterms: $x_1 x_2 x_3' x_4'$ and $x_1' x_2' x_3 x_4$
 - Output must be 0 for the minterms: $x_1' x_2 x_3' x_4$ and $x_1 x_2' x_3 x_4'$

CONFLICT :- Not Possible

$$w_1 + w_2 \geq T$$

$$w_3 + w_4 \geq T$$

$$\text{So, } w_1 + w_2 + w_3 + w_4 \geq 2T$$

$$w_2 + w_4 < T$$

$$w_1 + w_3 < T$$

$$\text{So, } w_1 + w_2 + w_3 + w_4 < 2T$$

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Now, let us look at the faults min terms, the output will be 0 for which min term now the reverse you see output will be 1, if either $x_1 x_2$ is 1 or $x_3 x_4$ is 1. Now, output will be 0, if one of $x_1 x_2$ and one of $x_3 x_4$ as 0.

So, let us look at two conditions here x_1 is 0 and x_3 is 0 here x_2 is 0 and x_4 is 0, just let us take these two so, others also you can take and you can make a similar justification. So, if you take this then you can arrive at conditions like this, because for this case your output is supposed to be 0 so, the weighted sum must be less than the threshold for this case, because x_2 and x_4 are 1 so, W_2 plus W_4 must be less than T and for the second case x_1 and x_3 W_1 plus W_3 must be less than T .

So, if you add them up you see their sum is less than twice T. So, you see you have arrived at a contradiction so, in one side your saying that the sum of the words must be greater than equal to 2 T on the other side you are saying it must be less than 2 T. So, the overall conclusion is we cannot find the value of weights $W_1 W_2 W_3 W_4$ such that these conditions are satisfied simultaneously. So, we can say that this function cannot be implemented using a single threshold gate. So, we have actually showed this as a counter example, because we have a counterexample. So, this condition is not true fine.

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The slide is titled "A Function that Requires Two T-gates". On the left, the function is written as $f = (x_1x_2 + x_3)x_4x_5 + x_6$. On the right, there are two diagrams. The top diagram is a conventional logic circuit: an AND gate with inputs x_1 and x_2 outputs x_4 ; another AND gate with inputs x_3 and x_4 outputs x_5 ; an OR gate with inputs x_4 and x_5 outputs x_6 ; and a final AND gate with inputs x_6 and x_5 outputs f . The bottom diagram shows two threshold gates. Gate 2 has inputs x_1, x_2, x_3 with weights $1, 1, 2$ and threshold 2 . Gate 3 has inputs x_4, x_5, x_6 with weights $1, 1, 3$ and threshold 3 . The output of Gate 2 is x_4 and the output of Gate 3 is f .

Let us take another example where is showing a function, that requires more than one threshold gates for implementation. So, here I am just showing the final solution not going to this steps, this is a function of variables so, the lines faint I am actually showing the gates in using convectional logic, we will be requiring four gates to implement 2 AND gates and 2 OR gates.


Now, what we are showing here is that using threshold logic gates, you can have two threshold logic gates to implement the same functions. So, you can actually verify that it works. So, I live in a exercise for you for the first gates the inputs you are applying are $x_1 x_2$ and x_3 this inputs are $x_1 x_2$ and x_3 the weights are $1, 1$ and 2 and the threshold is 2 . For the second gate the inputs are $x_4 x_5$ the output of this gate is also an input and x_6 and the weights are $1, 1$ and 3 and the threshold is 3 . So, the function f that you get will correspond to this function, this you can verify. So, this is one function which can be

implemented using two threshold gate and this is an example realization of that right this you can check.

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Threshold Logic: The Basic Design Problem

- Given a switching function $f(x_1, x_2, \dots, x_n)$, determine whether it is realizable by a single threshold element, and if so, find appropriate weights and threshold.
 - Such a function is called a *threshold function*.
- A straightforward approach:
 - Obtain inequality constraints from each row of the truth table.
 - Solve the set of 2^n simultaneous inequalities.



2^n

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So, the basic design problem in threshold gate is given any arbitrary switching function let us of n variables, the main question you want to answer is to determine, whether it can be realized by a single threshold gate by a single threshold element and, if it is possible what should be the weights what should be the threshold value. So, the design problem of a threshold element or a threshold gate is to determine, or find out what will be the value of my weights what will be the value of the threshold. So, once we have to do this your synthesis problem is solved ok.

So, if a function can be implemented using a single threshold element, we say that it is a threshold function. So, a threshold function is a function which can be implemented by a single threshold gate like this.

So, we shall take an example to show how we can systematically check for this and how to determine the weights. So, we construct the truth table and, from every row the truth table we obtain some inequality constraints we shall see how. So, we will be having 2^n to the power n such constraints, because there are two to the power n rows in the truth table. So, will have to solve those 2^n inequalities at the end right, this is the basic idea.

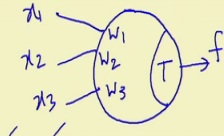
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An Example



- Consider the function: $f(x_1, x_2, x_3) = x_1'x_2' + x_1'x_3$

$D=0 \rightarrow T$ must be negative
 $D=2 \rightarrow w_2$ must be negative
 $D=4 \rightarrow w_1$ must be negative
 $D=3,5 \rightarrow w_2 > w_1$
 $D=1 \rightarrow w_3 \geq T$
 Thus, $w_3 \geq T > w_2 > w_1$

$w_1 = -2, w_2 = -1, w_3 = 1, T = -0.5$



D	x_1	x_2	x_3	f	Inequality
0	0	0	0	1	$0 \geq T$
1	0	0	1	1	$w_3 \geq T$
2	0	1	0	0	$w_2 < T$
3	0	1	1	1	$w_2 + w_3 \geq T$
4	1	0	0	0	$w_1 < T$
5	1	0	1	0	$w_1 + w_3 < T$
6	1	1	0	0	$w_1 + w_2 < T$
7	1	1	1	0	$w_1 + w_2 + w_3 < T$



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Let us take an example to work this out, we consider this function and for this function, we have constructed the truth table. So, we are showing the inputs $x_1 \times x_2 \times x_3$ the output f and also the decimal equivalent of the inputs 0 1 2 3 4 5 6 7 8. Now, suppose I have I mean I am that I can build a threshold gate to implement this function. So, the inputs will be $x_1 \times x_2$ and x_3 , my weight will be $w_1 \ w_2$ and w_3 there will be a threshold three and the output will be f .

So, if I have input 0 0 0 so, my weighted sum will be 0, because the output is 1 that weighted sum must be greater than equal to T . Similarly for 0 0 1 the weighted sum will be $x_3 \ w_3$ only w_3 this is 1 so, w_3 also must be greater than equal to T for the row number 2 0 1 0 only x_2 is 1 so, w_2 it is 0 so, w_2 must be less than T that is why it is 0 here, both x_2 and x_3 are 1 so, w_2 plus w_3 this is one should be greater than or equal to T , here w_1 0 less than equal to T w_1 plus w_3 0 less than or equal to T w_1 plus w_2 0 again less than T and all 1 it is 0. So, their sum also should be less than T .

So, you see we have got 8 inequality constraints directly from the truth table from here, we have to solve for $w_1 \ w_2 \ w_3$ well, there are ways of solutions and, showing you just intuitively how you can go about doing it D equal to 0 if you row it says T should be negative. So, the first conclusion is T must be negative look at row 2 D equal 2 it says $w_2 < T$ T is less than T , because T is negative. So, w_1 also must be negative and should be less

than T . Similarly row number four says W_1 less than T , because T is negative W_1 should also be negative.

So, there is you can see directly and from rows 3 and 5, what you can see it says W_2 plus W_3 is greater than equal to T and W_1 plus W_3 is greater than equal to T so, W_3 cancels out less than T it says W_2 greater than, or equal to T W_1 less than T so, from that you can reduce W_2 greater than W_1 well understanding that W_1 and W_2 are all negative and lastly from the D equal to 1 row W_3 is greater than equal T this is another condition.

So, combining this you have, these conditions to be satisfied that W_3 should be greater than equal to T , T must be you have this conditions T must be greater than W_2 greater than W_1 , because this condition are there T must be greater than W_2 T must be greater than W_1 and already we have shown W_2 must be greater than the 1.

So, you can have any arbitrary choice that satisfies this and of course, T must be negative so, one possible choice I am showing here, there are infinite possible solutions so, one solution is W_1 is minus 2 W_2 is minus 1 W_3 is plus 1 and T is minus 0.5, you see all this conditions satisfied W_3 greater than equal T , T greater than or equal to W_2 , W_2 greater than equal to W_1 ok. So, this is how given a function given a truth table well, if it is a threshold function you can reduce the values of the weights and the threshold ok.

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Properties of Threshold Gates

- A threshold gate is characterized by the weight threshold vector:
 - $V = \{W_1, W_2, \dots, W_n, T\}$
- A property:
 - Let $f(x_1, x_2, \dots, x_n)$ be realized by $V_1 = \{W_1, W_2, \dots, W_n, T\}$.
 - If x_j is complemented, f can be realized by $V_2 = \{W_1, W_2, \dots, -W_j, \dots, W_n, T - W_j\}$ with inputs $x_1, x_2, \dots, x_j', \dots, x_n$.

x_j

1

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Now, we shall look at a couple of properties of threshold gates, or threshold functions. The first property says that any threshold gate as you know, it is characterized by the weights of the inputs and also the threshold, they taken together is called weight threshold vector. So, any threshold gate is characterized by its weight threshold vector.

So, consider a function f , which can be realized by weight threshold vector W_1, W_2, \dots, W_n and T . Let us call it if you want let us say if we have any particular variable x_j have any particular variable exchange the corresponding what is W_j the corresponding weight is W_j .

So, if we compliment x_j so, instead of x_j in the function we make \bar{x}_j , then the corresponding function can be implemented only by negating the weight of that W_j and changing the threshold from T to T minus W_j , this can be proved ok. So, given a function if you want to realize another function for one of the inputs are complimented, then you can just adjust the weights like this is one result I wanted to show.

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• Another property:

- If $f(x_1, x_2, \dots, x_n)$ is realizable by a single threshold element with $V_1 = \{w_1, w_2, \dots, w_n, T\}$, then its complement f' is realizable by a single threshold element with $V_2 = \{-w_1, -w_2, \dots, -w_n, -T\}$.

- From V_1 :

$$\sum_{i=1}^n w_i x_i > T \quad \text{where } f = 1$$

$$\sum_{i=1}^n w_i x_i < T \quad \text{where } f = 0$$

- Multiplying both sides by -1:

$$\sum_{i=1}^n -w_i x_i < -T \quad \text{whenever } f = 1 \text{ or } f' = 0$$

$$\sum_{i=1}^n -w_i x_i > -T \quad \text{whenever } f = 0 \text{ or } f' = 1$$

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And another result is let us considered this f is a threshold function and this is the corresponding weight threshold vector.

Now, suppose we want to implement the complement of f , it says the compliment function can be directly realized using the weight threshold vector, where you negate all the weights and the thresholds minus W_1 minus W_2 minus W_n and minus T , well it is

very easy to prove this, because we see from the first condition from V_1 , because this is a threshold gate the weighted sum of $W_n \times x_i$ must be greater than or equal to T , whenever f is 1.

And it should be less than T , whenever f is 0. Now, if you multiply both sides of this inequality by minus 1 so, this $W_i \times x_i$ will become minus $W_i \times x_i$ it will become minus T and, this greater than will become less than less than will become greater than right. So, from here you can see that if you make the weights and threshold negative and, this less than greater than gets reversed. So, so in that earlier case, if you was 1 now it is becoming 0.

Now, earlier case it was 0, now it is becoming 1 so, it is actually the complement function right. So, we have seen a few such properties there are many such properties of threshold functions so, so we are not going to go into the detail of that any of you are interested, there are some very nice literature available and books available on threshold logic functions you can go through them, but I just wanted to give you a brief overview about threshold logic and threshold gates and how they can be used to implement switching functions.

Thank you.